

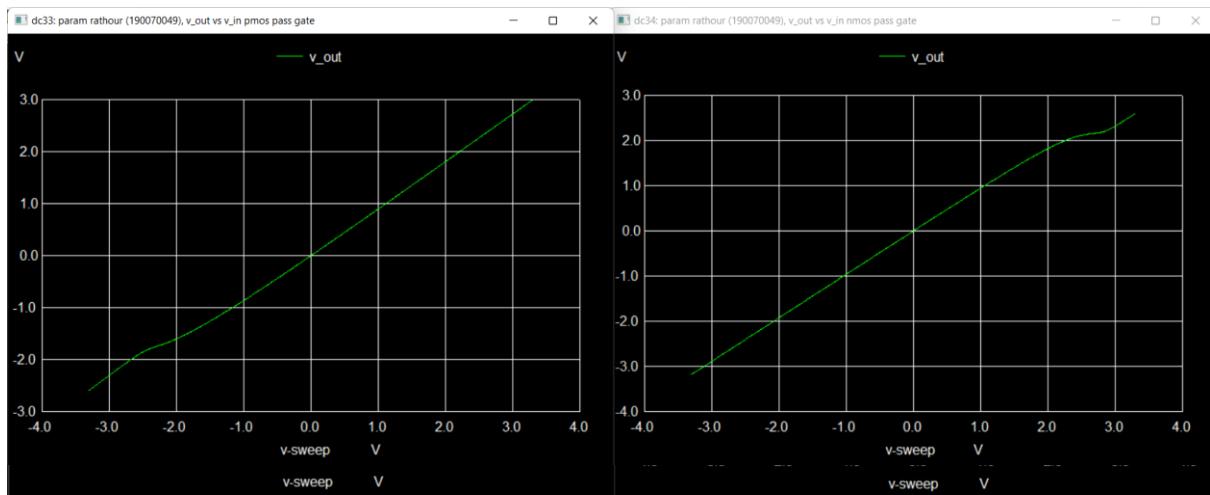
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Course: Electronic Devices Lab

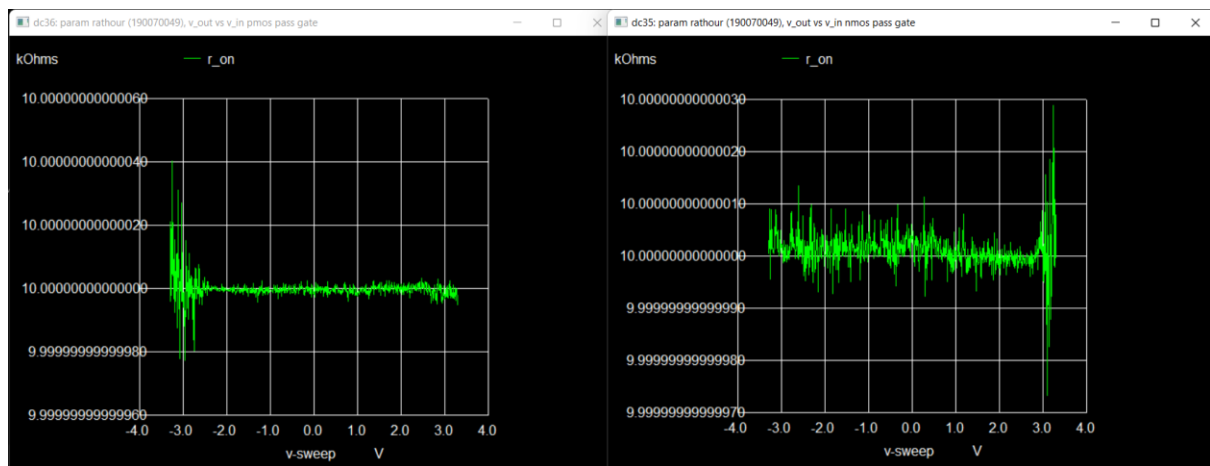
Course Code: EE236

Q1) Part 1



V_{out} vs V_{in} (PMOS and NMOS), $V_{out} \approx V_{in}$ in both cases

Both plots are inaccurate at one boundary of V_{in}

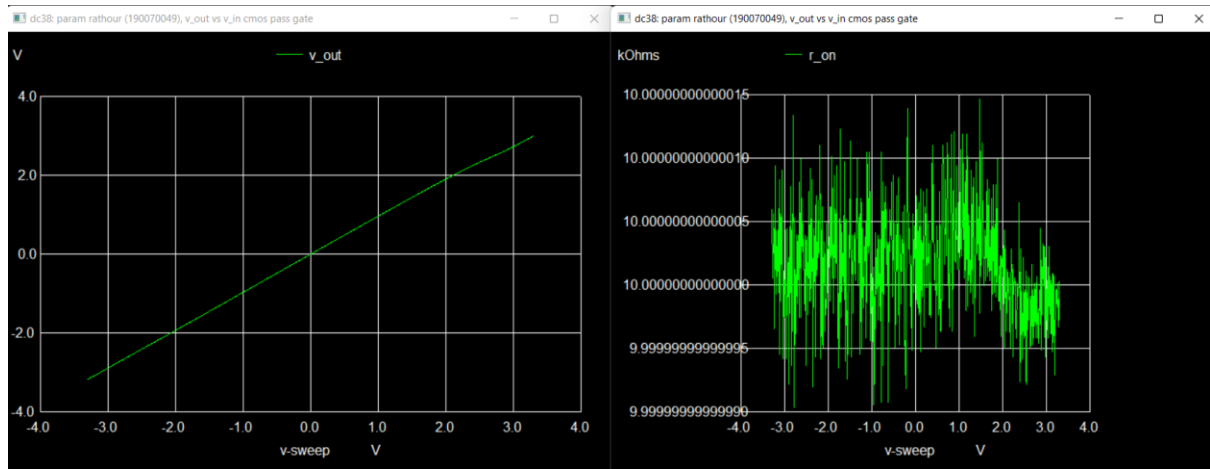


(PMOS and NMOS)

R_{on} is a bit higher in PMOS

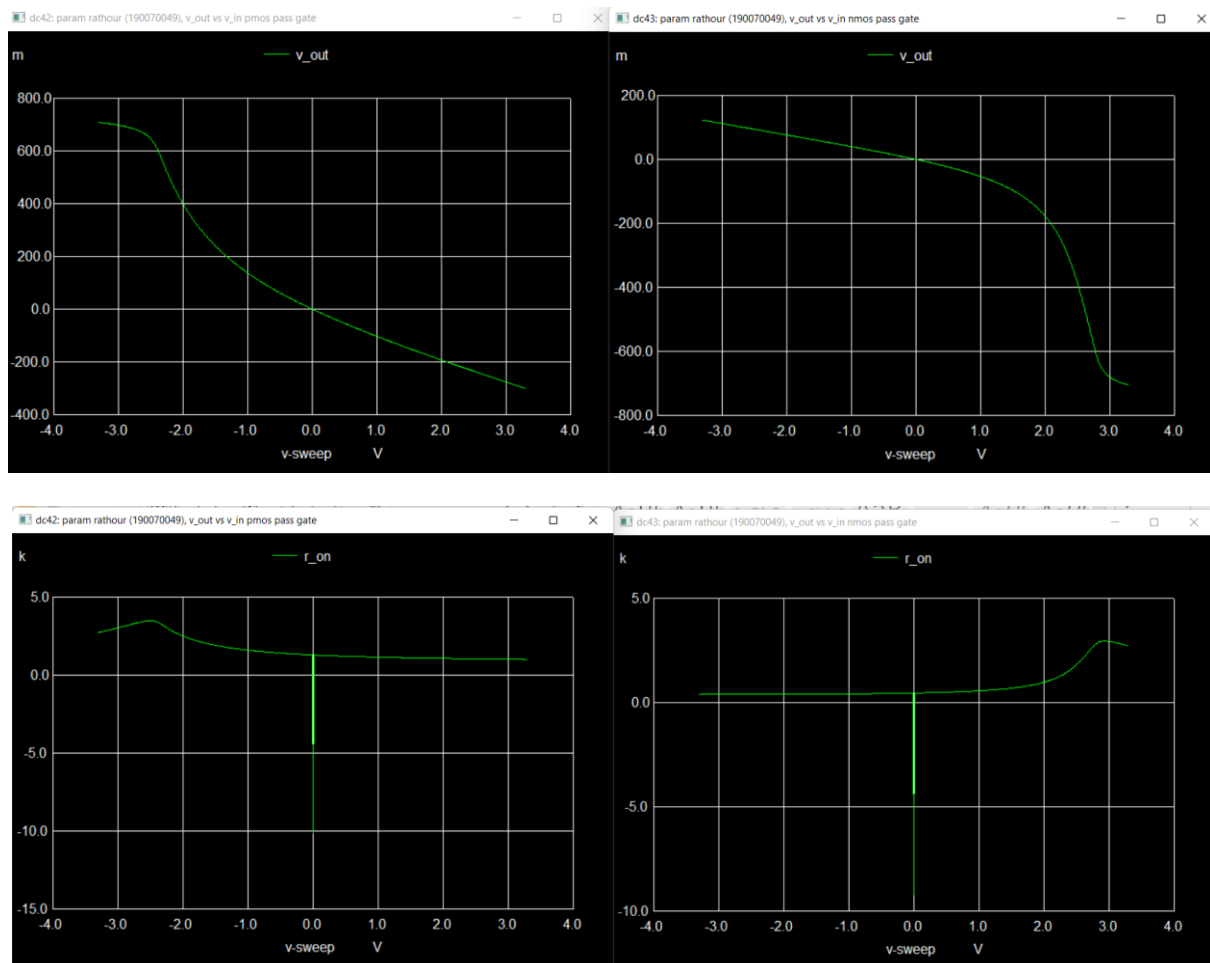
Part 2

$V_{out} \approx V_{in}$, greater accuracy than part 1 outputs



Maximum $R_{on} = 10k + 15 \cdot 10^{-15}$ ohm

Part 1



R_{on} is a bit higher in PMOS

Part 2

