Quiz: Limited Direct Execution

Total points 50/55

Take the quiz solo, but feel free to consult a partner student, the book, the videos or other resources if needed. Re-take quiz if your score is less than 80% or if you just want some more practice.

The respondent's email (parth2@pdx.edu) was recorded on submission of this form.

✓ What's the primary benefit of "Direct Execution"? *	5/5
Ease of Use	
Security	
Portability	
Speed/Performance	✓
Honesty	
✓ Why do we need limits on direct execution? *	5/5
✓ Why do we need limits on direct execution? * ○ prevent programmer errors (bugs)	5/5
	5/5
prevent programmer errors (bugs)	5/5
prevent programmer errors (bugs) honesty	5/5
prevent programmer errors (bugs) honesty fairness of resource use	5/5
 prevent programmer errors (bugs) honesty fairness of resource use prevent OS crashes and process crashes caused by other processes' behavior 	5/5

✓	Is a segmentation fault an example of limited direct execution? *	5/5
0	Yes, concurrent access to I/O devices is not allowed	
0	No, seg faults are caused by programmer error	
•	Yes, the MMU limits which memory addresses can be accessed by a program	✓
0	No, it is a fault not an interrupt	
×	How do system calls help to implement limits on direct execution? *	0/5
0	because there are a limited number of system calls	
0	system calls allow the OS to expose a limited set of functionality to application programs	
•	system calls are invoked mainly via timer interrupts which may not by altered by the application programmer	×
0	by allowing the OS to run before and during the application's request. the OS can then enforce whatever limits it needs to.	
0	by handling faults for such problems as divide by zero	
~	A typical processor chip has multiple cores. The OS runs a process on one core and a different process on a different core. This is an example of what kind of sharing? *	5/5
0	time sharing	
•	space sharing	✓

An OS allocates some memory for a process. Then, when the not running, the OS reclaims the physical memory and allow process to use the same frames of memory. This is an exam kind of sharing? *	vs another
time sharing	✓
space sharing	

How freque	ently is each	type of limit u	ısed, checke	ed or invoked?	*	
	Multiple times per instruction	Approximately once per instruction	Once per many (variable number of) instructions	Approximately once per 10ms	Score	
timer interrupt	\circ	0	0		1/1	✓
system call interface	0	0	•	0	1/1	~
MMU address translation	•	0	0	0	1/1	✓
status register mode bit	0		0	0	1/1	✓

Which limit prevents which bad thing from occurring? * OS does System Mode bit Timer MMU Call not limit in Status Score Interrupt Interface this Register program attempts 1/1 to run forever program has a 1/1 logical bug changing the mode 1/1 bit rewriting an 1/1 interrupt handler program tries to read 1/1 memory of another program changing the 1/1 interrupt vector program tries to write file for which 1/1 it does not have privileges 1/1 modify the time period of

the timer interrupt

✓	Can the trap instruction (called "int" on Intel x86 chips) be called from user application code? *	5/5
•	Yes	✓
O	No	

	yes, OS runs at	no, OS does not		
	this time	run at this time	Score	
within a program's while loop	0		1/1	~
during a system call	•	0	1/1	✓
on every I/O device interrupt	•	0	1/1	✓
when a fault occurs	•	0	1/1	✓
at boot time	•	\circ	1/1	✓
when a timer interrupt occurs	•	0	1/1	✓
during a function call	0		1/1	~
during each CPU			1/1	

This form was created inside of Portland State University.

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