



Implementation of High-Level Cryptographic Protocols using a SoC Plateform

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Abstract

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$R\acute{e}sum\acute{e}$

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Chapter 1

Introduction

- 1.1 Challenge
- 1.2 Network security

Chapter 2

Presentation

Here we talk about the protocols, the platform. Show The OS stack (kernel/user)

2.1 Experimental setup

The experimental environment is build around a standard x86 host and an ARM Cortex-A9 alongside an Altera Cyclone V FPGA as the target.

2.1.1 x86 host

OS Ubuntu 12.04 LTS, kernel 3.16

CPU Intel Core-i3 ... (two logical core out of four)

RAM 1.5GB DDR3

2.1.2 Altera Socrates SoCFPGA

OS Yocto project, kernel 3.14

CPU Dual core ARM Cortex-A9, 800MHz

RAM ...GB DDR3

FPGA Altera Cyclone V

Chapter 3

Implementation

Appendix A

Cross-compilation

- A.1 OpenSSL
- A.2 OpenVPN
- A.3 nginx
- A.4 Strongswan