



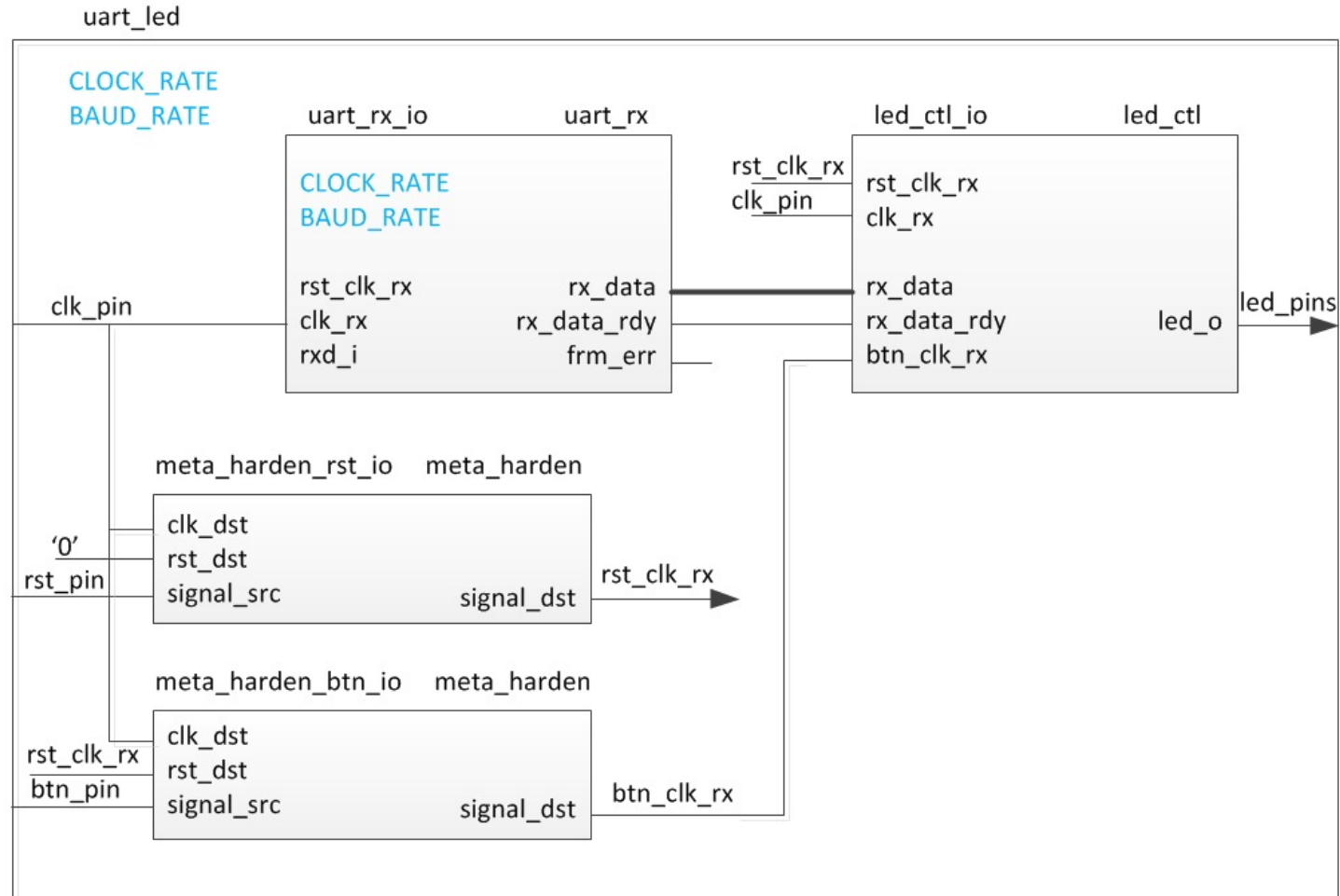
Lab6 Intro

Hardware Debugging

Introduction

- ▶ In this lab you will use the `uart_led` design that was introduced in the previous labs. You will use Mark Debug feature and also the available Integrated Logic Analyzer (ILA) core (in IP Catalog) to debug the hardware

The Design



Procedure

- ▶ Create the project
- ▶ Add the ILA core
- ▶ Synthesize the design and Mark Debug
- ▶ Implement and generate bitstream
- ▶ Debug in hardware

Summary

- ▶ You used ILA core from the IP Catalog and Mark Debug feature of Vivado to debug the hardware design.



Thank You

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