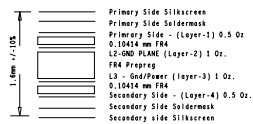


PRIMARY SIDE

4 LAYER STACKUP

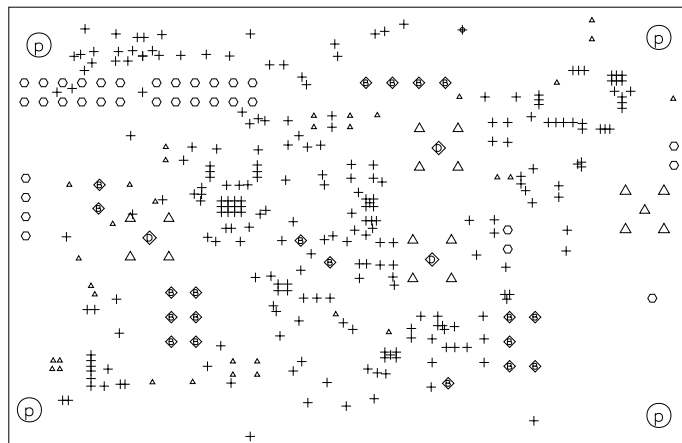


CHARACTERISTIC IMPEDANCE NOTES - Not Applicable to this layout

Primary Side - 50 Ohm - 100 Ohm - 150 Ohm Single Ended Impedance - using Layer-2 as Ground-reference-layer
Network Line and Signal Spacing for 50 Ohm Return:
0.625 - 0.034 - 0.625 (trace/spacing)-to-cu

Primary & Secondary Sides - 50 Ohm - 100 Ohm Single Ended Impedance

Network Line for single Ended Impedance:
0.625mm (layer-2 using-layer-2 as ground-reference-layer)
0.625mm (layer-2 using-layer-2 as ground-reference-layer)



FABRICATION NOTES:


1. Material: 4 Layer FR4 glass epoxy laminate 1.6mm +/- 10%, material to be RoHS compliant. Board to be fabricated per IPC-6012A, CLASS 2.
2. Plated thru holes and conductive pattern electroplated with 25uM min. thick copper. Terminal areas and plated thru holes to be ENIG plated.
3. Processing tolerance:
 - A. Conductive pattern front to back registration within 0.125mm total.
 - B. Minimum annular ring surrounding holes 0.05mm.
 - C. Finished conductive pattern to be within 0.05mm of true size.
 - D. Minimum feature size 0.2mm
 - E. Minimum air gap 0.12mm
4. Warp and twist within 0.1875mm per 25mm (0.75%)
5. Dimensions are for the finished part.
6. SOLDERMASK: liquid Photo Imageable soldermask over bare copper (SMOBC), colour BLUE both sides using the pattern provided. No mask is permitted on the terminal areas. Soldermask to etch registration within 0.125mm total.
7. SCREENING: Screen component outlines and nomenclature using indelible white ink on both sides. Nomenclature shall be legible. Screen to etch registration within 0.15mm total.
8. Electrically test 100%.
9. Break all sharp edges 0.35mm Radius max.
10. Manufacturer UL and Date codes on the bottom side silkscreen as indicated.
11. Via-in-pad. Any via in pad must be filled and levelled.

DRILL CHART: TOP to BOTTOM
ALL UNITS ARE IN MILLIMETERS

FIGURE	FINISHED SIZE	PLATED	QTY
+	0.2489	PLATED	243
△	0.4089	PLATED	33
◆	1.0211	PLATED	20
○	1.1405	PLATED	33
◇	1.27	PLATED	3
△	1.5189	PLATED	13
△	1.651	PLATED	4
Ⓟ	3.2004	NON-PLATED	4

HOLE TOLERANCE

UNLESS SPECIFIED
PLATED: +/- 0.0762mm
NON PLATED: +/- 0.0254mm

UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN MM TOLERANCES DECIMALS FRACTIONS ANGLES .XX +/-0.25 +/- 1/32 +/- 2 .XXX +/-0.127	FABRICATION		 ANALOG DEVICES ANALOG DEVICES INTERNATIONAL ERDC Building Raheen Business Park Raheen, LIMERICK, IRELAND	
	APPROVAL	DATE	TITLE	
MATERIAL	DRAWN BY Pat Sheahan	01NOV2019	EVAL-CN0540-ARDZ	
	DESIGNED			
	CHECKED			
FINISH	APPROVED		SIZE	FSCM NO
	MFG ENGINEER		A4	
			DRAWING NUMBER	REV
DO NOT SCALE DWG			09-059358	B
			SCALE 1/1	SHEET 1 OF 1