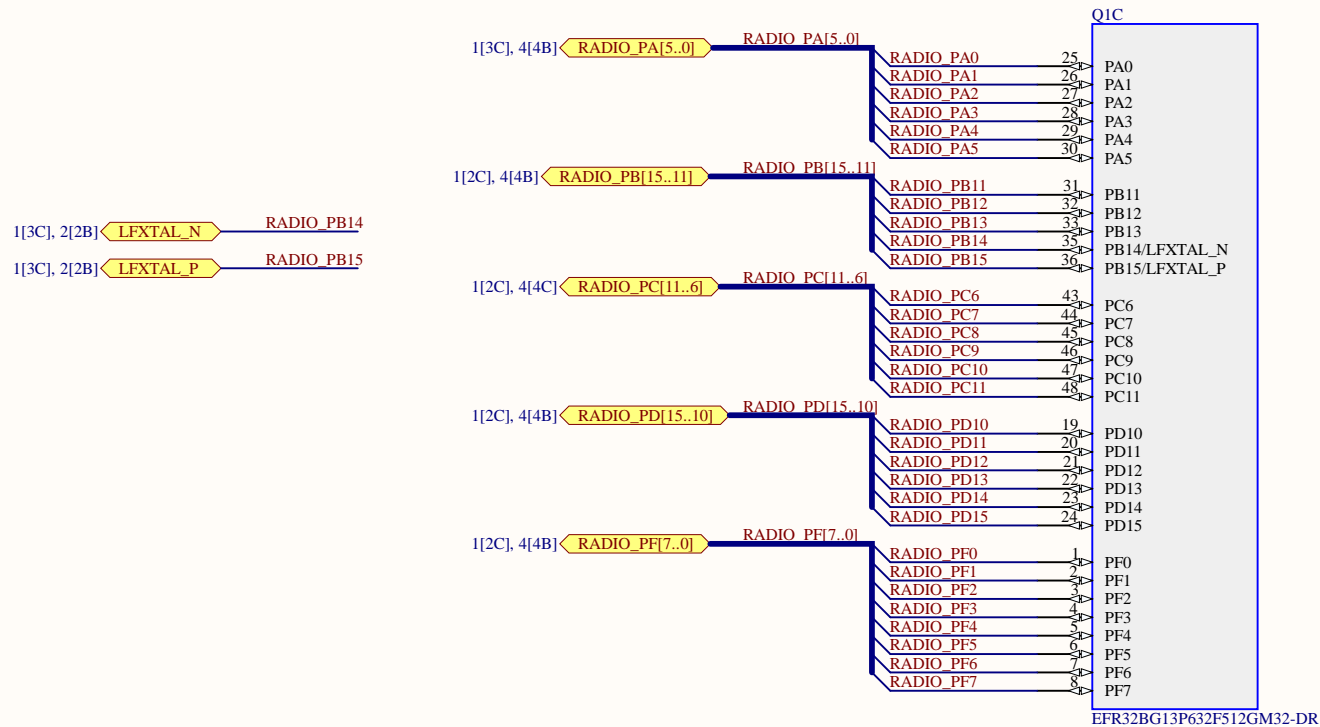


EFRBG13 GPIOs



VCOM_TX

VCOM_RX

SWDIO

SWCLK

SWO

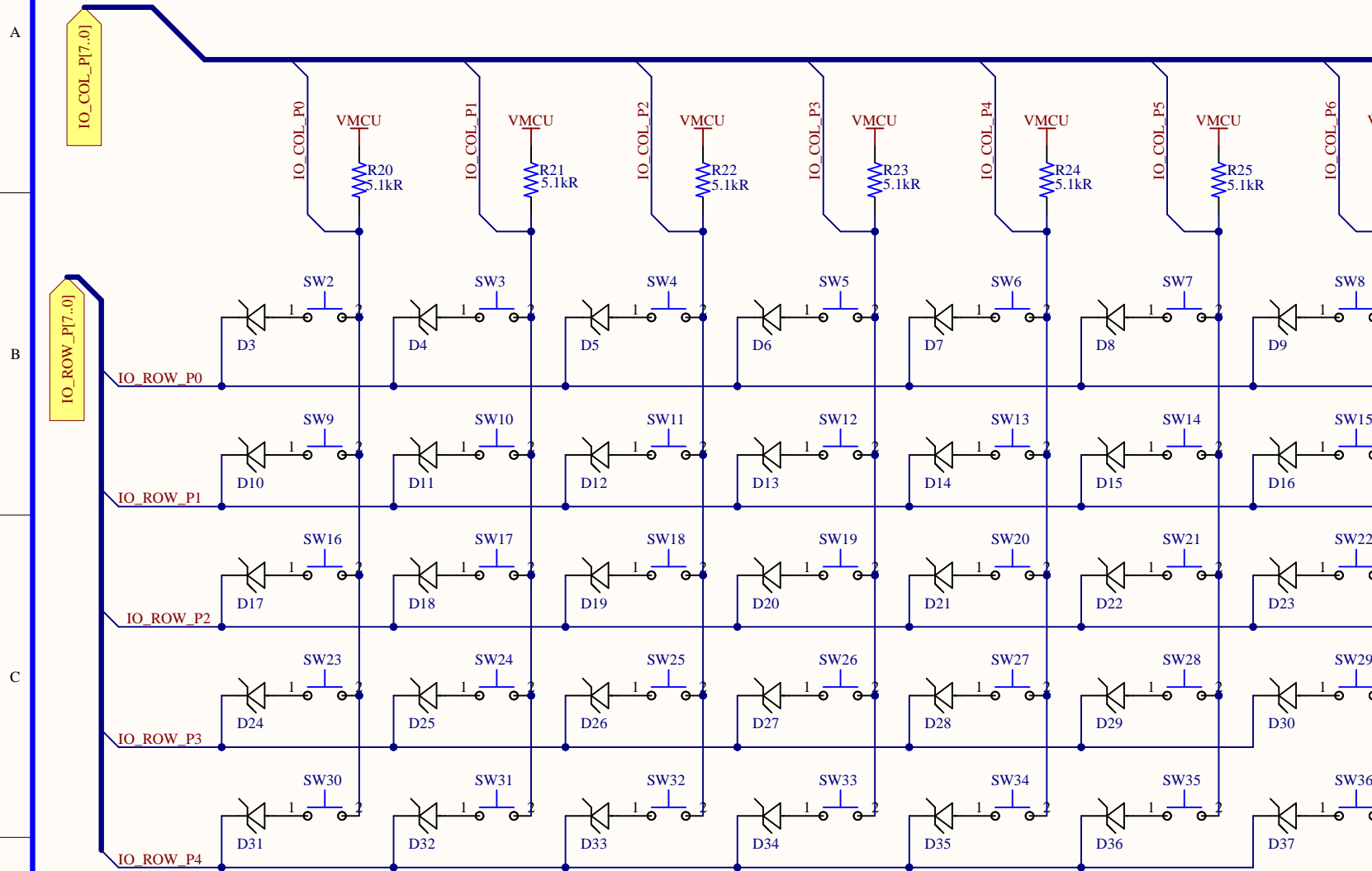
^A Reset input, active low. This pin is internally pulled up to AVDD. To apply an external reset source to this pin, it is required to only drive this pin low during reset, and let the internal pull-up ensure that reset is released.

Mini Simplicity Connector

- Serial Wire Debug (SWD) with SWO
- Packet Trace Interface (PTI)
- Virtual COM port (VCOM)
- AEM monitored voltage rail

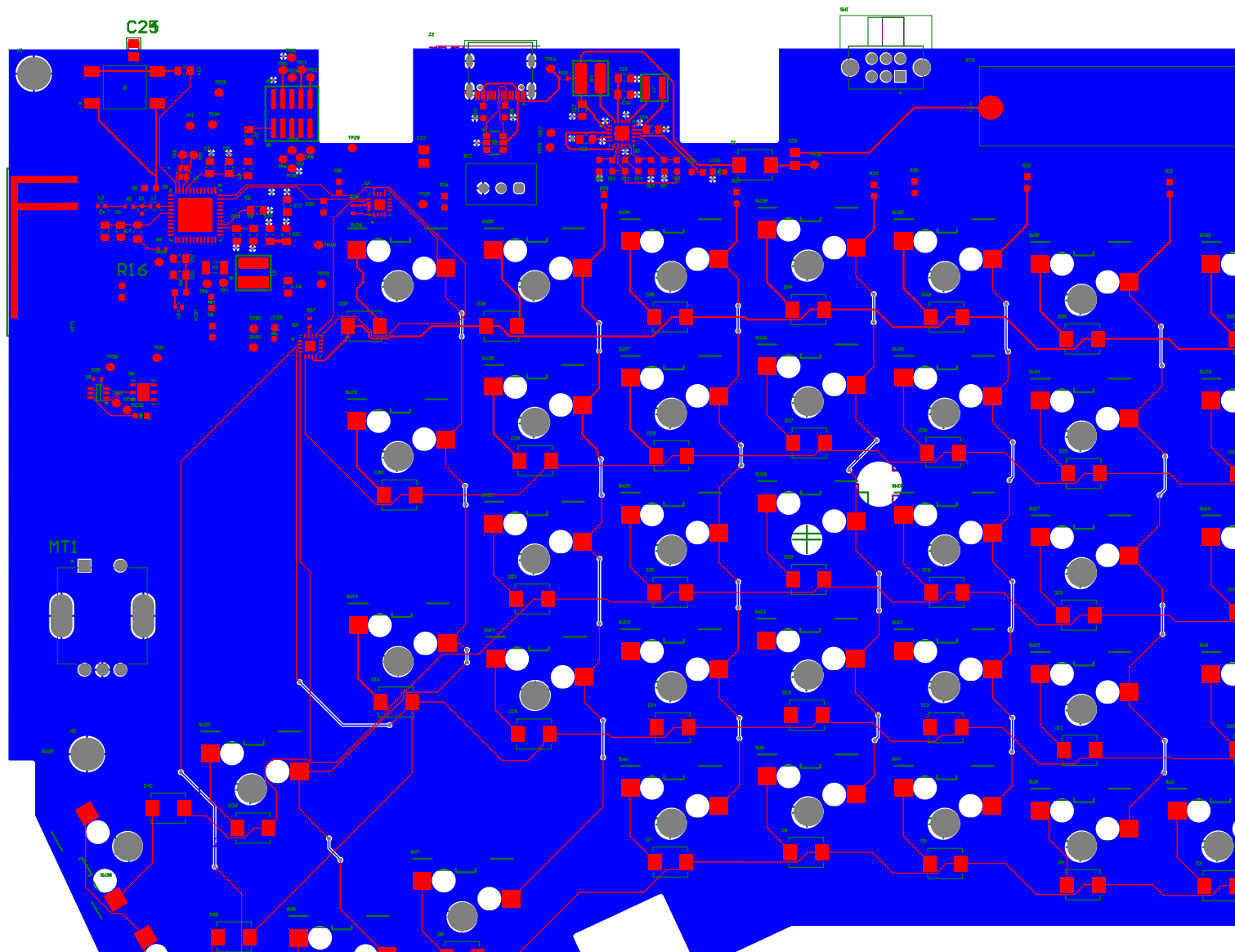
	VMCU	H9
VAEM	1	
RESET	3	
VCOM_TX	5	
SWDIO	7	
PTI_FRAME	9	

Mechanical Switches



R33 R3

*1 *2 R34 R35



Board Stack Rep