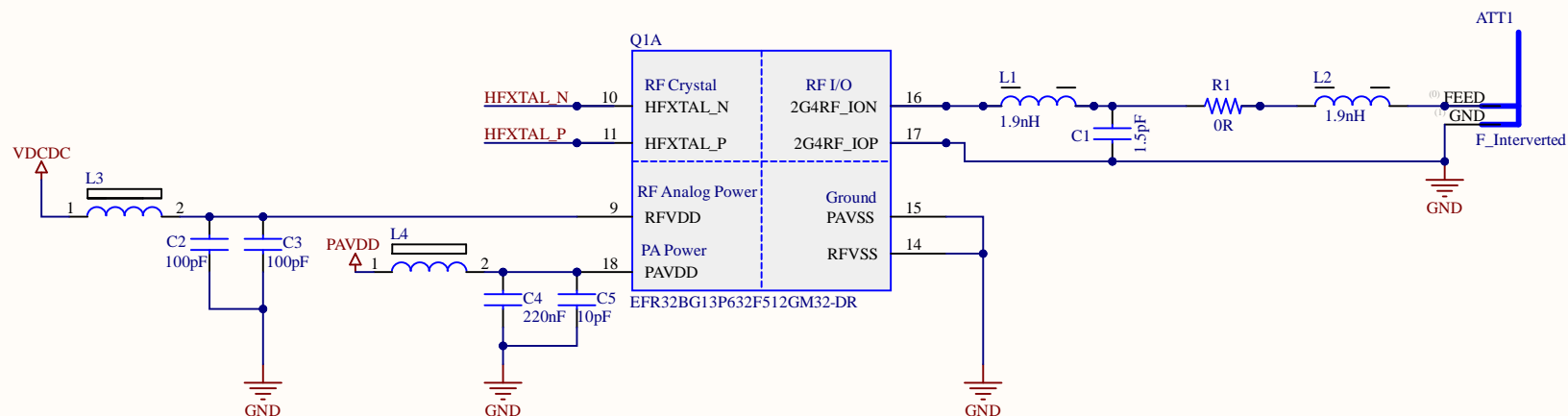


1

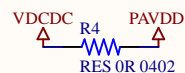
2

3

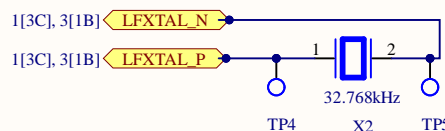
4



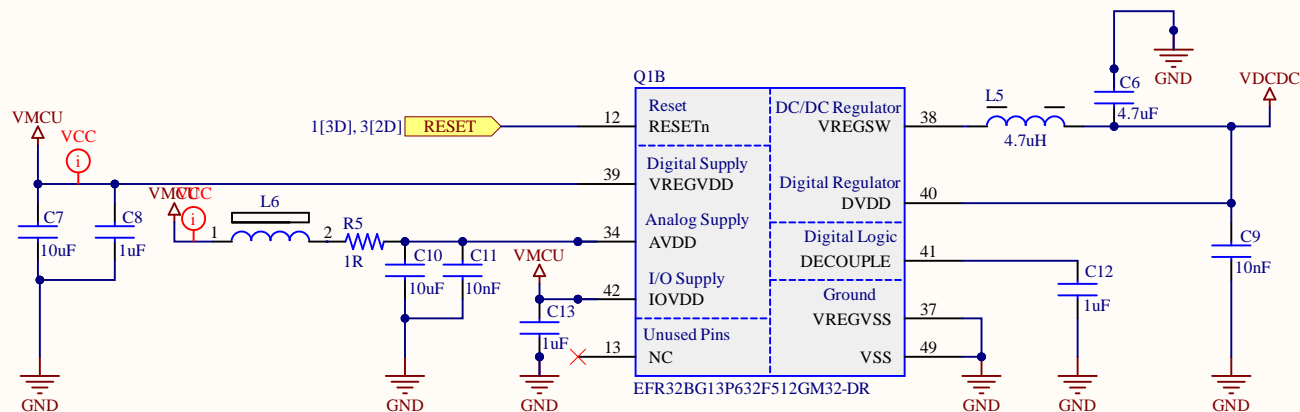
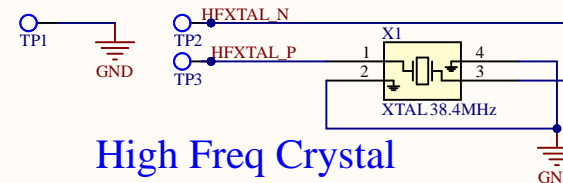
## Antenna & Radio Interface



## Low Freq Crystal



## High Freq Crystal



## Power & Decoupling

Title		
RF and Crystal		
Size	Number	Revision
A4	2	v1.0
Date:	10/14/2024	Sheet of Low Self Esteem
File:	\\..RF Antenna Power.SchDoc	Drawn By: Parth Thakkar

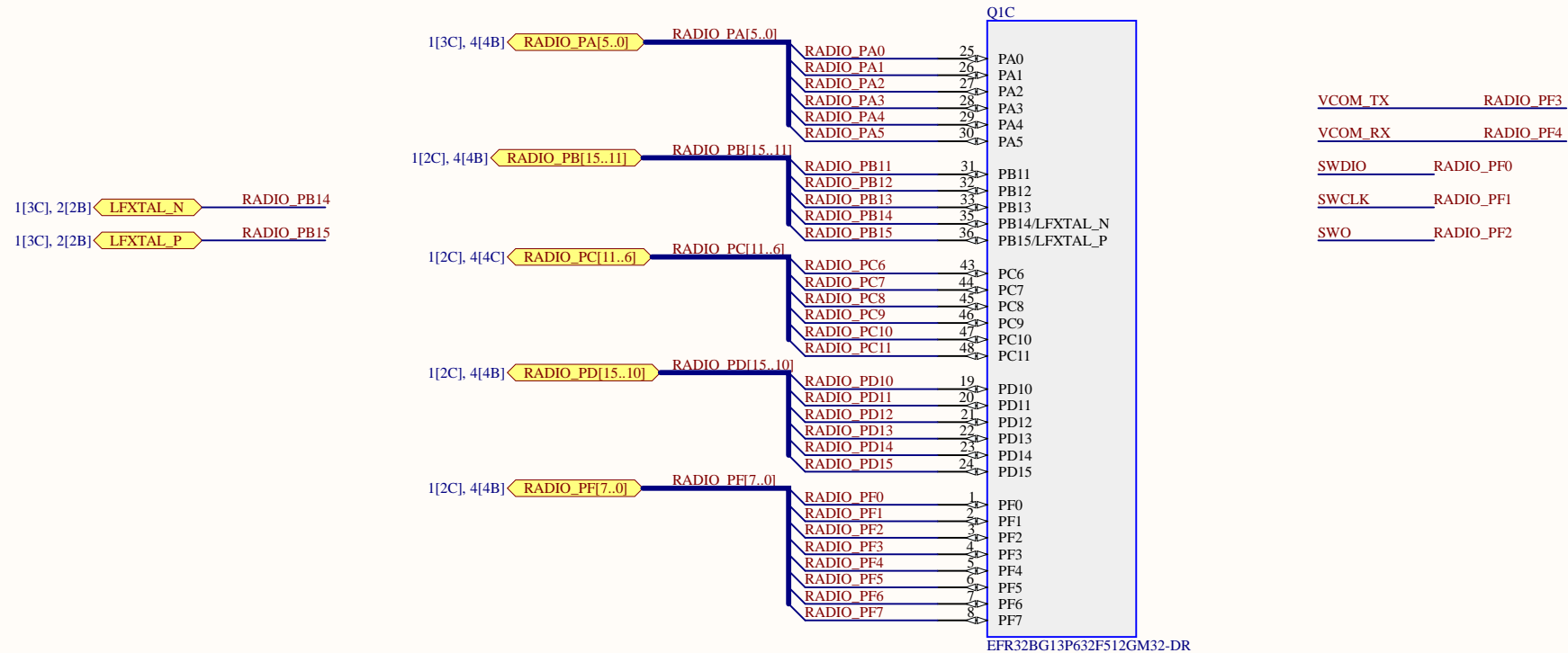
1

2

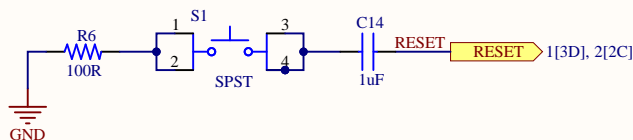
3

4

# EFRBG13 GPIOs



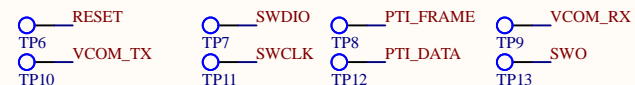
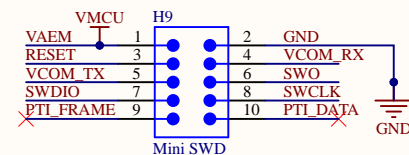
Reset input, active low. This pin is internally pulled up to AVDD. To apply an external reset source to this pin, it is required to only drive this pin low during reset, and let the internal pull-up ensure that reset is released.



Reset

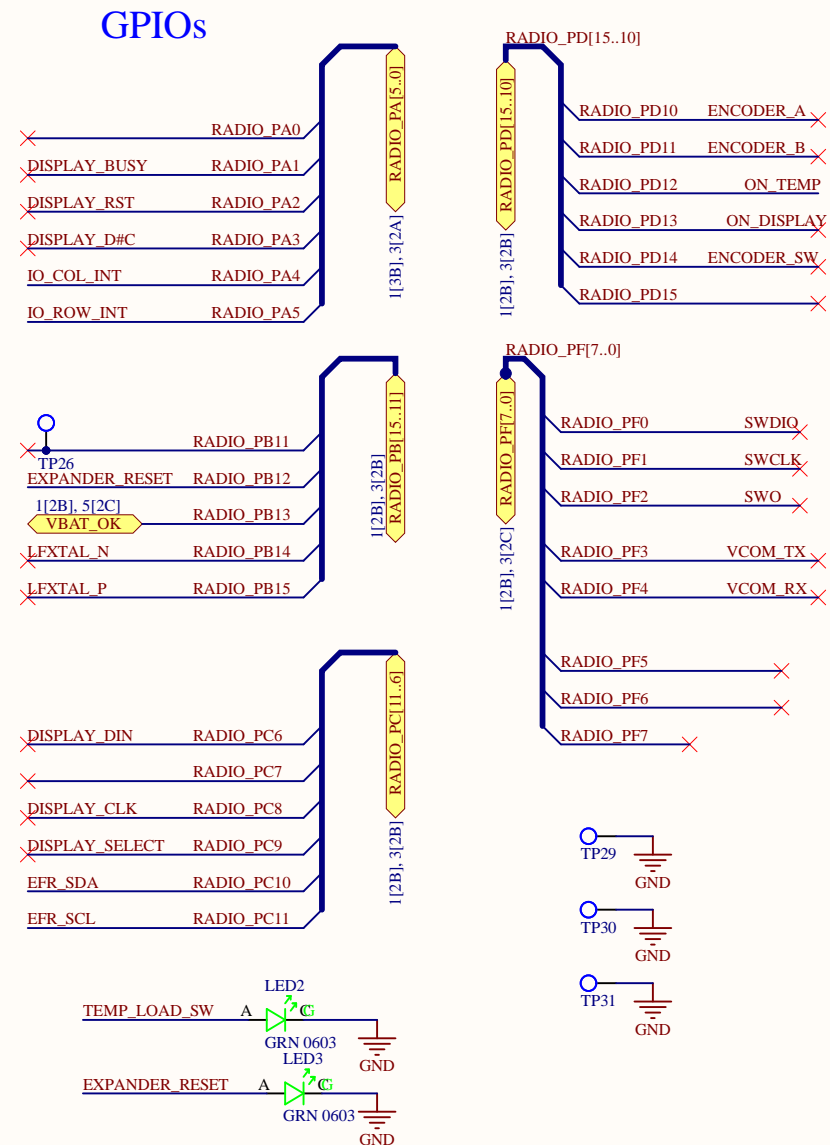
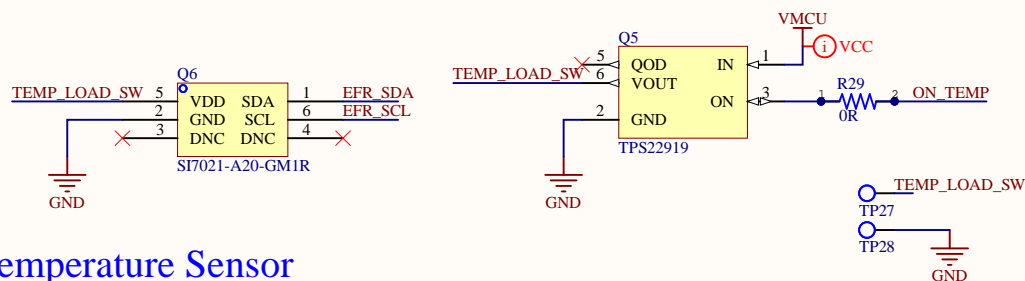
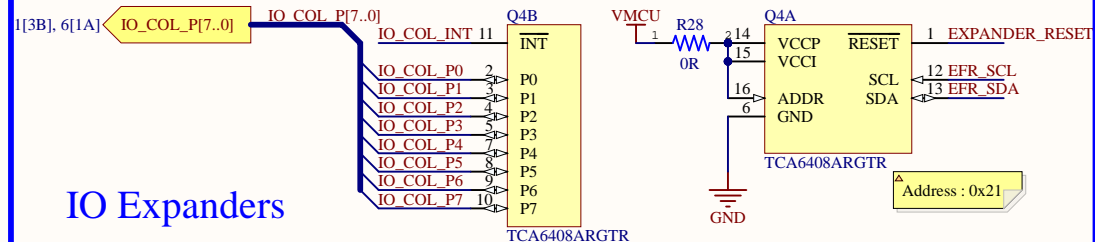
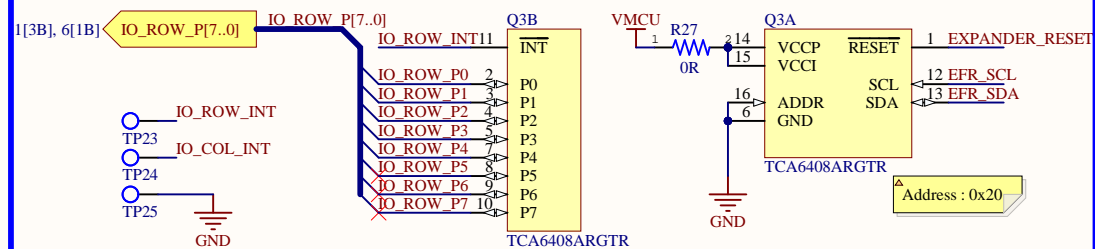
## Mini Simplicity Connector

- Serial Wire Debug (SWD) with SWO
- Packet Trace Interface (PTI)
- Virtual COM port (VCOM)
- AEM monitored voltage rail

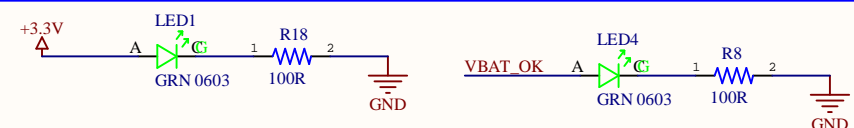
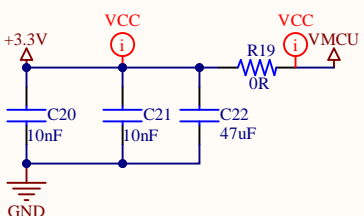
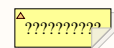
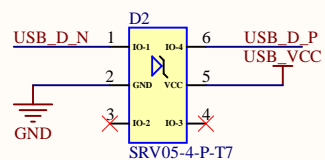
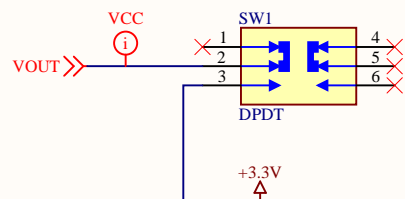
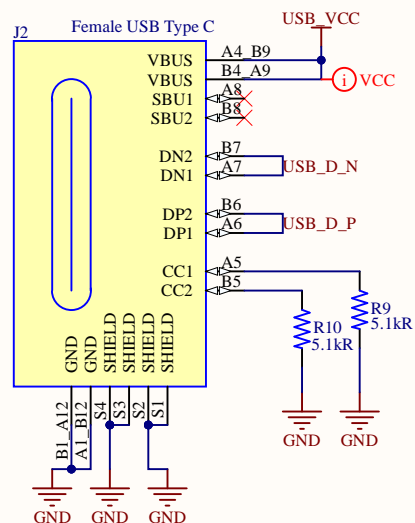


IO Connections			
Title	Size	Number	Revision
	Letter	3	
Date:	10/14/2024	Sheet of	Low Self Esteem
File:	\\.\EFR32_IO.SchDoc	Drawn By:	P. Thakkar

v1.0

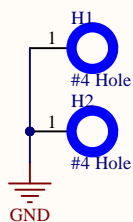
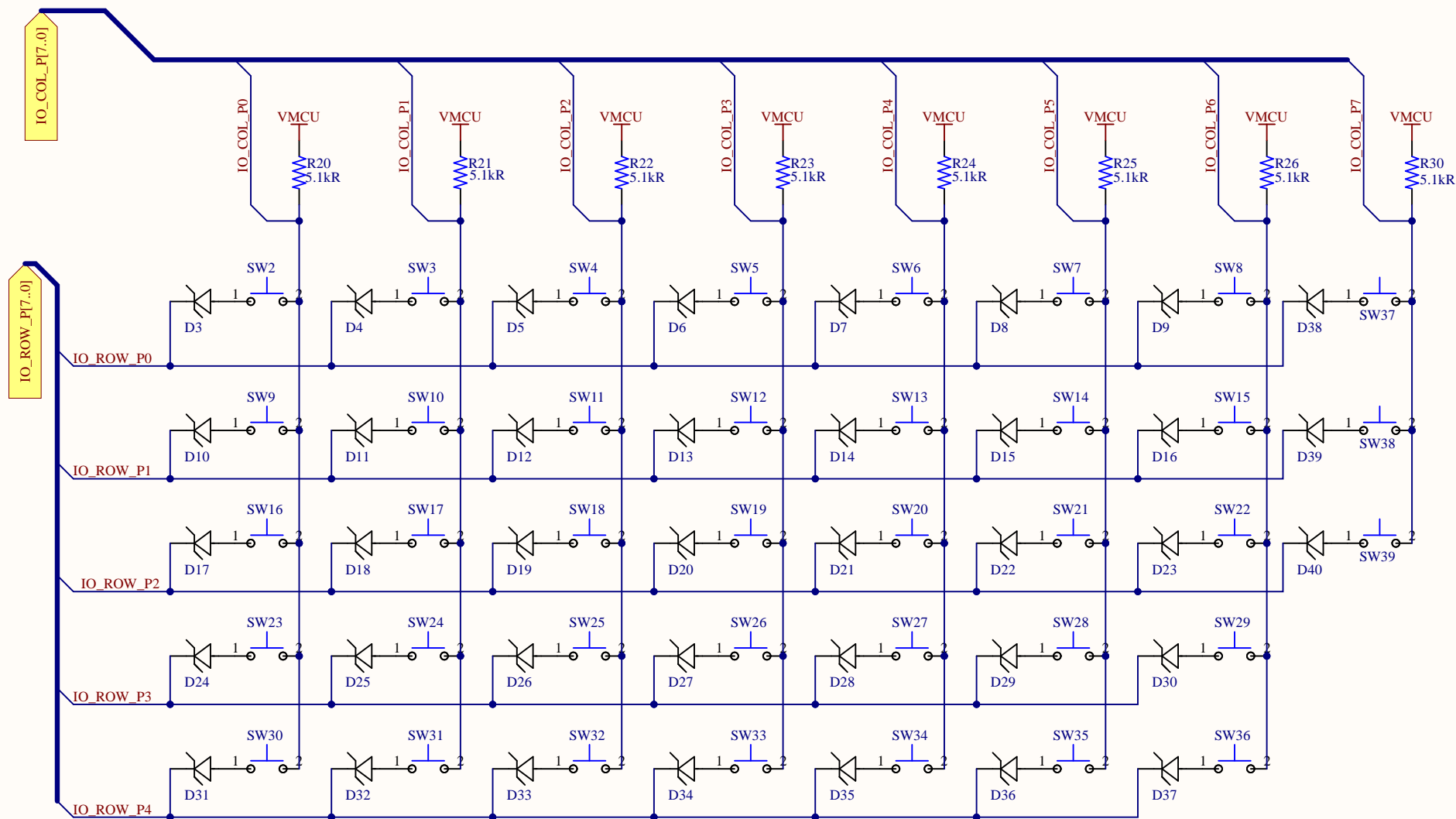


Title			
Size Letter	Number		Revision
Date:	10/14/2024	Sheet of	
File:	\\...\Peripherals.SchDoc	Drawn By:	



Title				Power Management			
Size		Number			Revision		
A4		5			v1.0		
Date:	10/14/2024				Sheet of		Low Self Esteem
File:	\\...\Power.SchDoc				Drawn By:		Parth Thakkar

# Mechanical Switches



# Mechanical Holes

Title		
Mechanical Switches		
Size	Number	Revision
A	6	v1.0
Date:	10/14/2024	Sheet of Low Self Esteem
File:	\\.\Switches.SchDoc	Drawn By: Parth Thakkar

