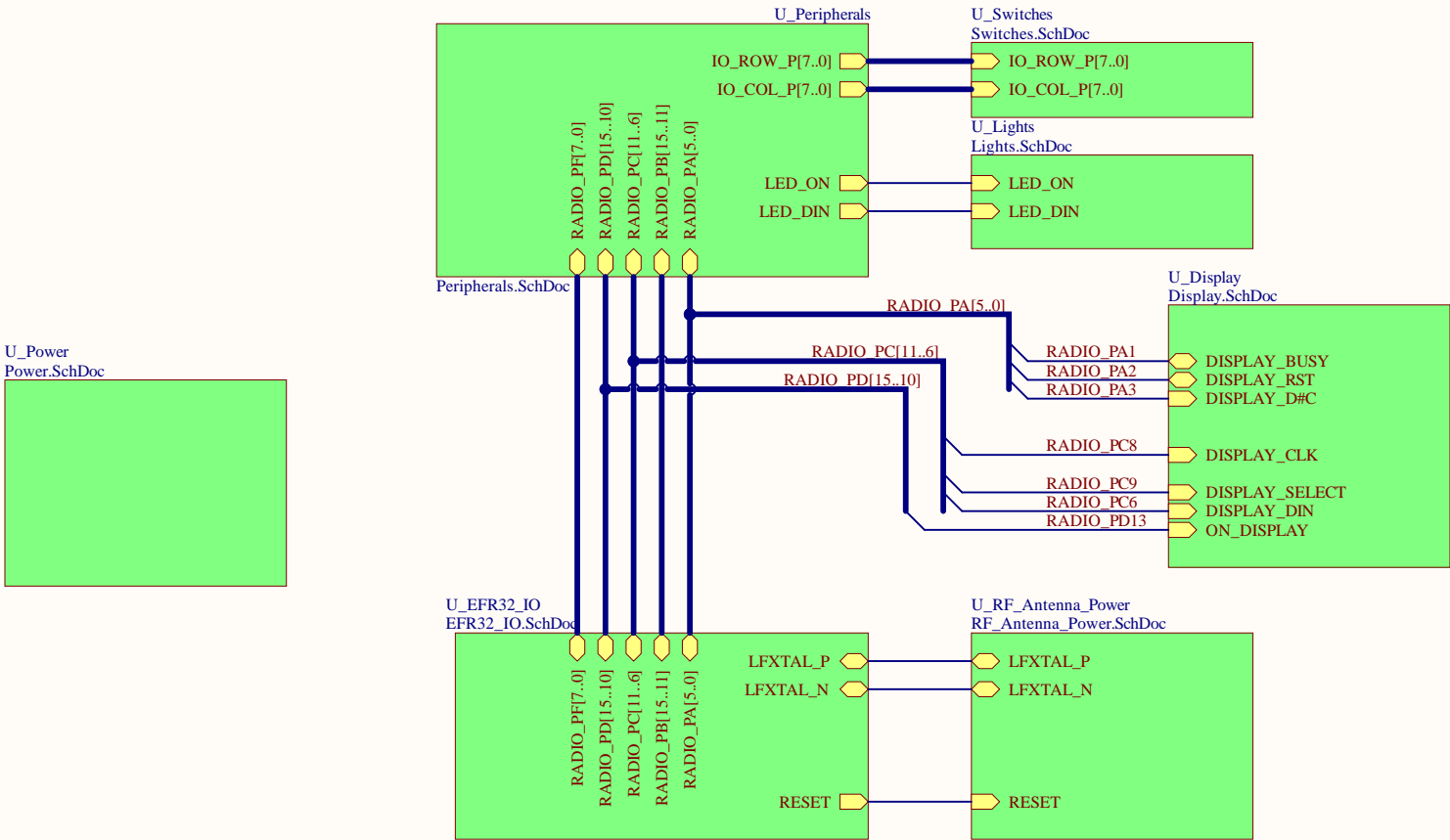
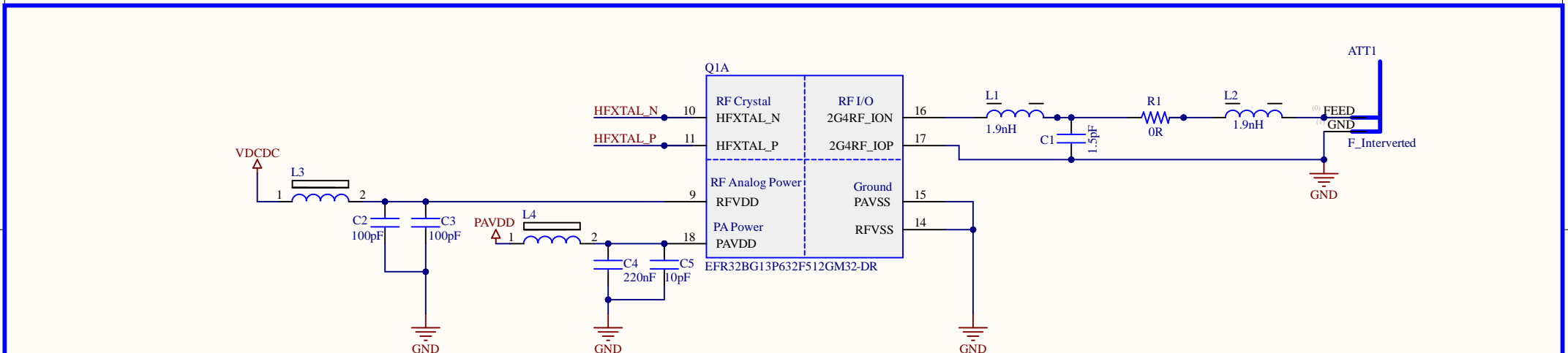


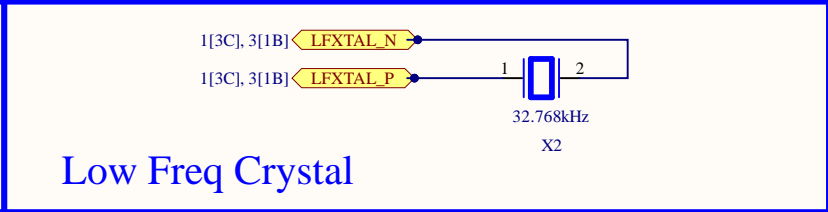
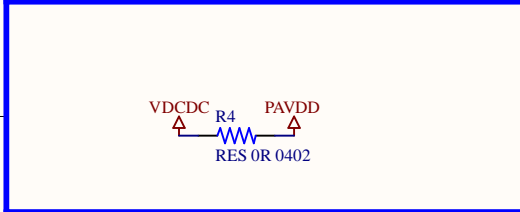
I2C



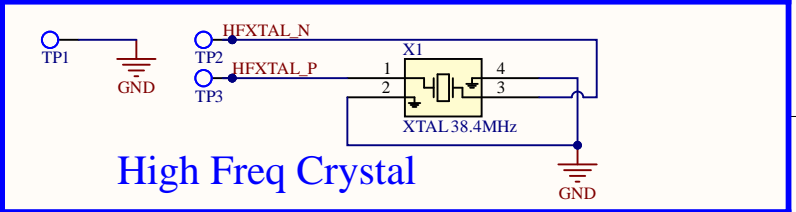
Title		
Size	Number	Revision
A4		
Date:	10/23/2024	Sheet of
File:	C:\Parth\Keyboard-Left.SchDoc	Drawn By:



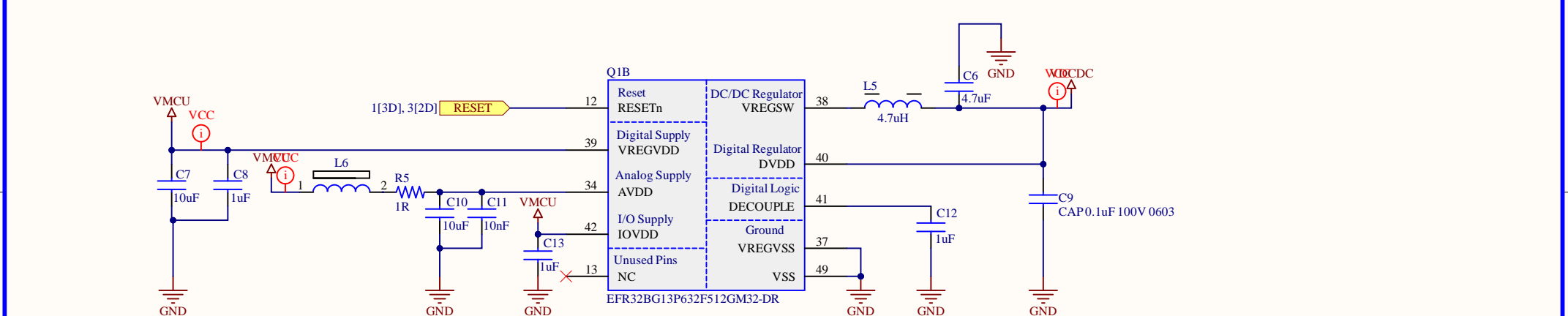
Antenna & Radio Interface



Low Freq Crystal



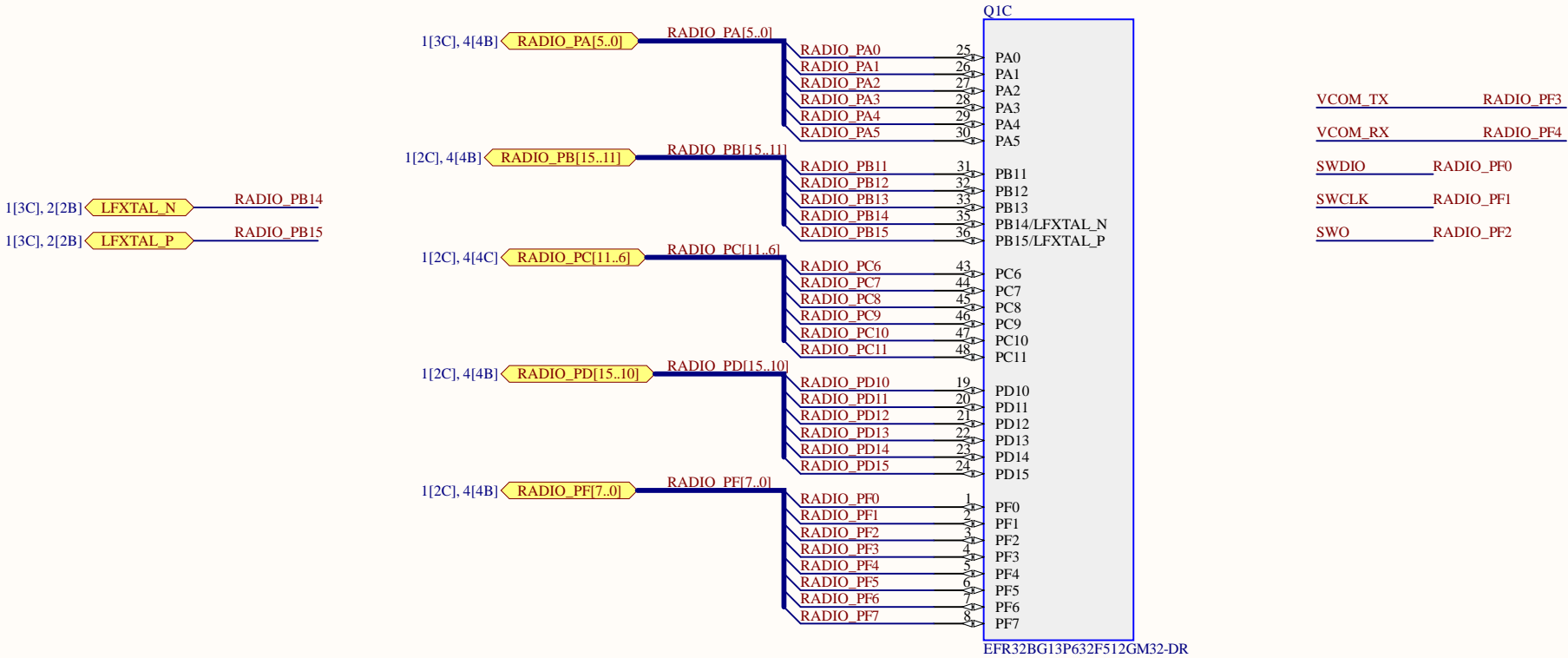
High Freq Crystal



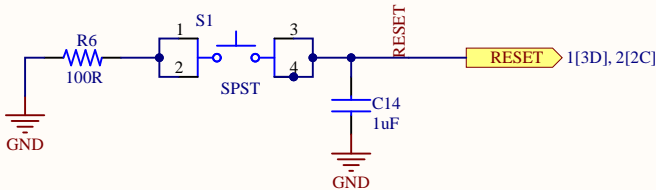
Power & Decoupling

Title		
RF and Crystal		
Size	Number	Revision
A4	2	v1.0
Date:	10/23/2024	Sheet of Low Self Esteem
File:	C:\Parth\RF Antenna Power.SchDoc	Drawn By: Parth Thakkar

EFRBG13 GPIOs



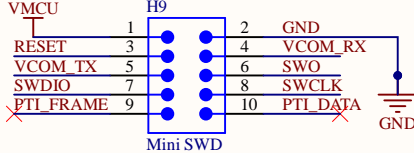
Reset input, active low. This pin is internally pulled up to AVDD. To apply an external reset source to this pin, it is required to only drive this pin low during reset, and let the internal pull-up ensure that reset is released.



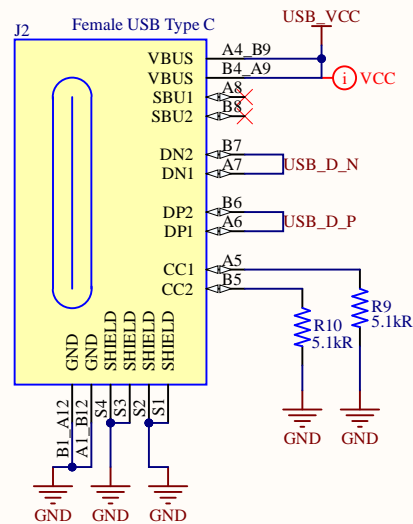
Reset

Mini Simplicity Connector

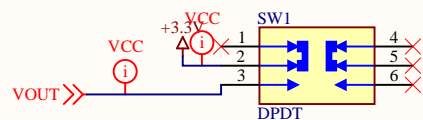
- Serial Wire Debug (SWD) with SWO
- Packet Trace Interface (PTI)
- Virtual COM port (VCOM)
- AEM monitored voltage rail



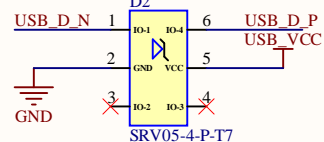
IO Connections		
Title	Revision	
Size	Number	Revision
Letter	3	
Date:	10/23/2024	Sheet of
File:	C:\Parth\...\EFR32_IO.SchDoc	Drawn By:



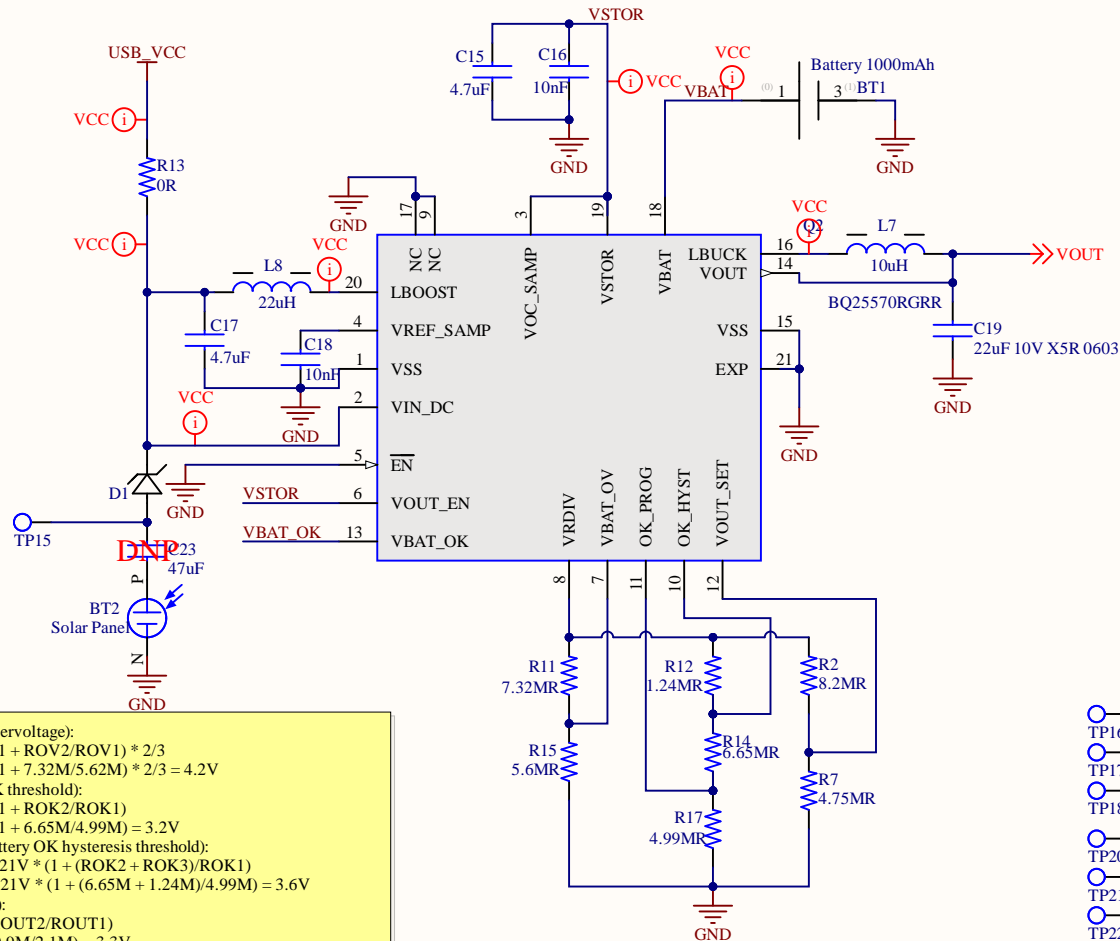
Type C



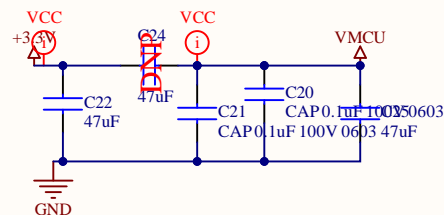
DPDT Switch



TVS Diode



Power Management IC

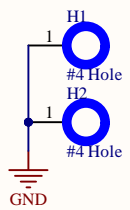
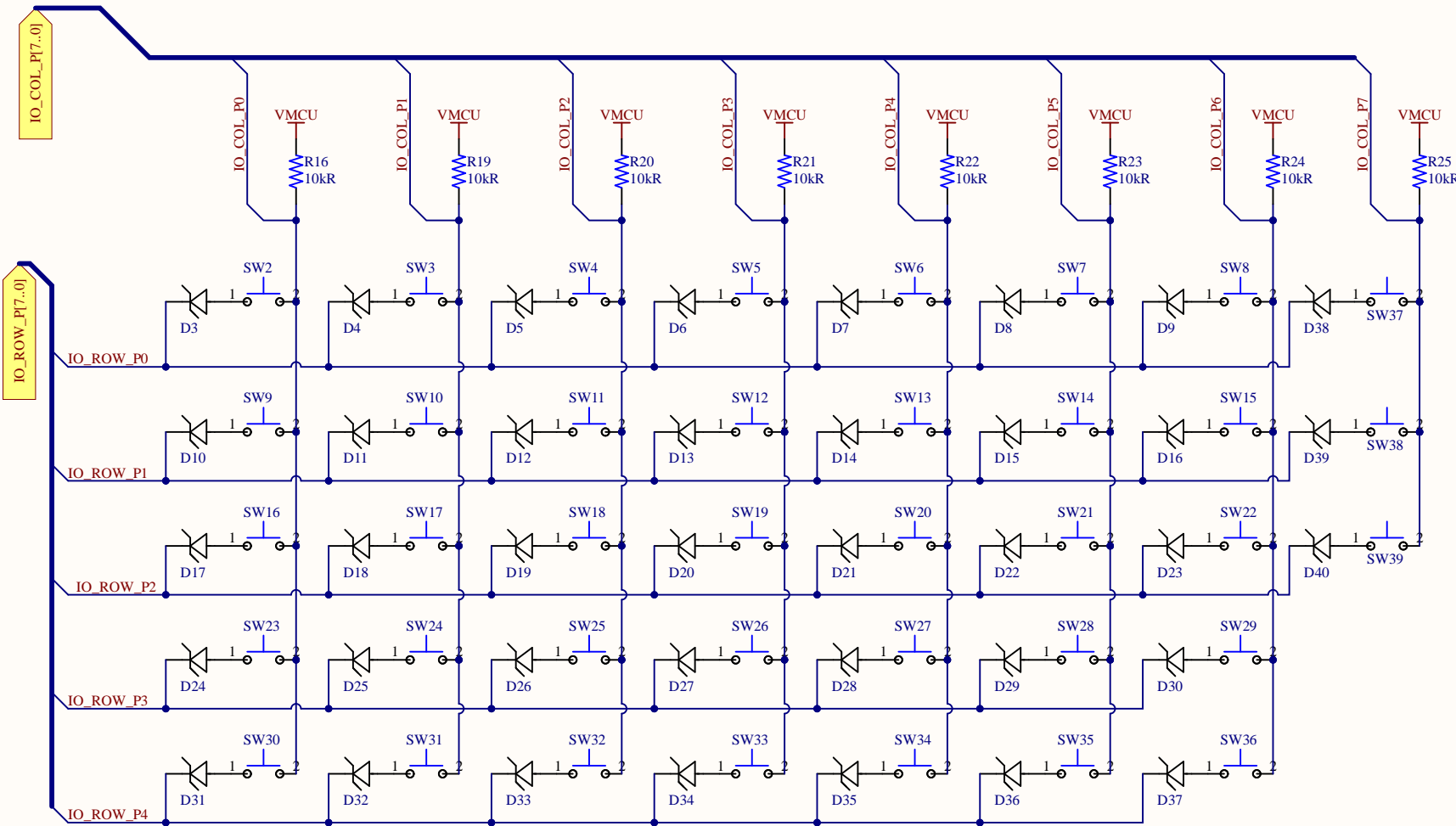


Decoupling and Bulk Cap



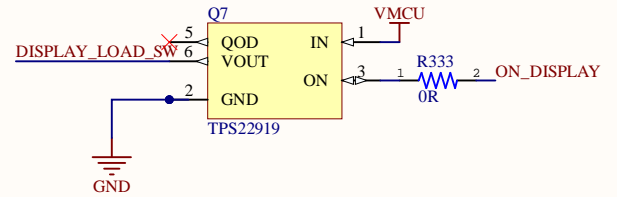
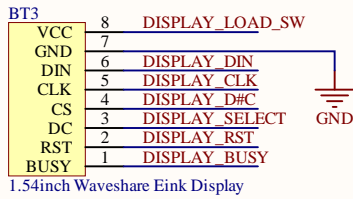
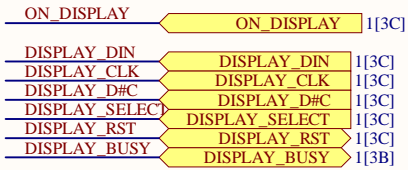
Title			
Power Management			
Size	Number	Revision	
A4	5	v1.0	
Date:	10/23/2024	Sheet of	Low Self Esteem
File:	C:\Parth\...\Power.SchDoc	Drawn By:	Parth Thakkar

Mechanical Switches



Mechanical Holes

Title		
Mechanical Switches		
Size	Number	Revision
A	6	v1.0
Date:	10/23/2024	Sheet of Low Self Esteem
File:	C:\Parth\...\Switches.SchDoc	Drawn By: Parth Thakkar



Title		
Size	Number	Revision
A		
Date:	10/23/2024	Sheet of
File:	C:\Parth\..\Display.SchDoc	Drawn By:

A

A

B

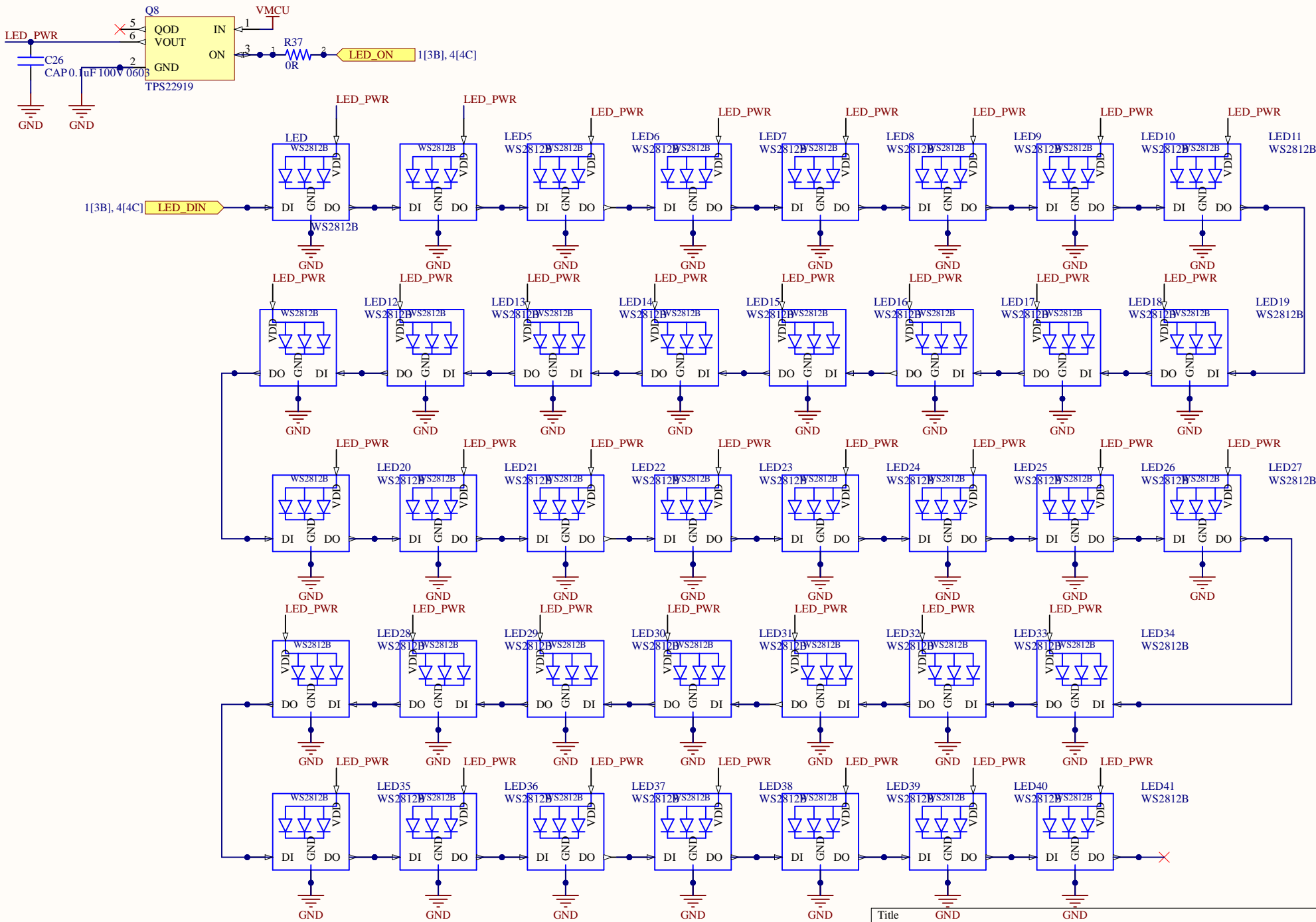
B

C

C

D

D



Title		
Size	Number	Revision
Letter		
Date:	10/23/2024	Sheet of
File:	C:\Parth\...\Lights.SchDoc	Drawn By: