

## **ECEN5623, Real-Time Systems:**

### **Exercise #5 – Cyclic Executives in FreeRTOS and Linux**

DUE: As Indicated on Canvas

Please thoroughly read Chapters 9, 10 & 11 in the text.

Please see example code provided on Canvas – seqgen.c and seqgen2x.c. Note that this code has been written with the Jetson and Linux in mind, but with slight modification can be used on a virtual machine, Raspberry Pi, TIVA or on the DE1-SoC.

This lab emulates a cyclic executive real-time system with multiple services. Each service runs at a separate frequency, so this is a multi-frequency executive. This can be useful as the basis for your final project code.

#### **Exercise #5 Requirements:**

- 1) [20 points] Download seqgen.c and seqgen2x.c (or unzip them from the provided zip file for this exercise) and build them in Linux on the Altera DE1-SOC, Raspberry Pi or Jetson board and execute the code. Describe what it is doing and make sure you understand how to use it to structure an embedded system. Determine the worst case execution time (WCET) for each service by printing or logging timestamps between two points in your code or by use of a profiling tool. Determine D, T, and C for each service and create an RM schedule in Cheddar using your WCET estimates. Calculate the % CPU utilization for this system.
- 2) [30 points] Revise seqgen.c and seqgen2x.c to run under FreeRTOS on the DE1-SoC or TIVA board, by making each service a FreeRTOS task. **Use the associated startup file in place of the existing startup file in FreeRTOS.** Use an ISR driven by the PIT hardware timer to release each task at the given rate (you could even put the sequencer in the ISR). Build and execute the new code. Determine the worst case execution time (WCET) for each service by printing or logging timestamps between two points in your code or by use of a profiling tool. Determine D, T, and C for each service and create an RM schedule in Cheddar using your WCET estimates. Calculate the % CPU utilization for this system. Compare this with the results you achieved under Linux in (1).
- 3) [40 points] Revise seqgen.c from both previous systems to increase the sequencer frequency and all service frequencies by a factor of 100 (3000 Hz). Build and execute the code under Linux and FreeRTOS on your target boards as before. For both operating systems determine the worst case execution time (WCET) for each service by printing or logging timestamps between two points in your code or by use of a profiling tool. Determine D, T, and C for each service and create an RM schedule in Cheddar using your WCET estimates. Calculate

the % CPU utilization for these systems. Compare results between Linux and FreeRTOS in this higher-speed case.

- 4) [10 points] Demonstrate the results and answer questions from the TA demonstrating that your sequencer works in both Linux and FreeRTOS as described in #1-#3 above.

Overall, provide a well-documented professional report of your findings, output, and tests so that it is easy for a colleague (or instructor) to understand what you've done. Include any C/C++ source code you write (or modify) and Makefiles needed to build your code. I will look at your report first, so it must be well written and clearly address each problem providing clear and concise responses to receive credit.

### **Grading Rubric**

[20 points] Linux Cyclic Executive:

Section Evaluation	Points Possible	Score	Comments
Explanation and Execution of code	10		
Timestamps and WCET estimates	2		
D, C, and T Table for all services	3		
Cheddar schedule and % CPU utilization	5		
Total	10		

Explain the API's used with a brief description of the code.

[30 points] FreeRTOS Cyclic Executive:

Section Evaluation	Points Possible	Score	Comments
Explanation and execution of code on each target. Also, include the steps on how you ported the code to run on freeRTOS, the API's used and peripherals you used to run the code in the TIVA board, the changes required in any ISR and the synchronization mechanism used.	15		
Timestamps and WCET estimates	2		
D, C, and T Table for all services	3		
Cheddar schedule and % CPU utilization	5		

Comparison between Linux and FreeRTOS	5		
Total	30		

[40 points] High-Rate Cyclic Executive:

Section Evaluation	Points Possible	Score	Comments
Explanation and execution of code on targets	25		
Timestamps and WCET estimates	2		
D, C, and T Table for all services	3		
Cheddar schedule and % CPU utilization	5		
Comparison between Linux and FreeRTOS	5		
Total	40		

The final predictable response jitter should be analyzed through a graph (Jitter vs No of Frames & No. of frames & Execution Time) and table for the various transformation techniques. Provide at least two graphs per transformation technique.

[10 points] TA Demonstration

Section Evaluation	Points Possible	Score	Comments
Individually answer questions about the Exercise sections 1-3	5		
Demonstrate to the TAs that your code for section 1 through 3 is complete and working.	5		
TOTAL	10		