Resource Report

- **Compilation Report**

Flow Summary

Flow Status	Successful - Mon Sep 29 20:39:41 2025
Quartus II 64-Bit Version	13.0.1 Build 232 06/12/2013 SP 1 SJ Web Edition
Revision Name	i2c_project
Top-level Entity Name	i2c_project
Family	Cyclone IV GX
Total logic elements	152 / 14,400 (1 %)
Total combinational functions	147 / 14,400 (1 %)
Dedicated logic registers	64 / 14,400 (< 1 %)
Total registers	64
Total pins	13 / 81 (16 %)
Total virtual pins	0
Total memory bits	0 / 552,960 (0 %)
Embedded Multiplier 9-bit elements	0
Total GXB Receiver Channel PCS	0 / 2 (0 %)
Total GXB Receiver Channel PMA	0 / 2 (0 %)
Total GXB Transmitter Channel PCS	0 / 2 (0 %)
Total GXB Transmitter Channel PMA	0 / 2 (0 %)
Total PLLs	0 / 3 (0 %)
Device	EP4CGX15BF14C6
Timing Models	Final

Timing analysis

- **Setup summary**

Summary (Setup)

	Clock	Slack	End Point TNS
1	clk	-2.234	-106.183

- **Hold Summary**

	Clock	Slack	End Point TNS
Summary (Hold)			
1	clk	0.341	0.000

- **Report recovery summary**

Summary (Recovery)

No paths to report.

- **Report removal summary**

Summary (Removal)

No paths to report.

- **Report minimum pulse width summary**

	Clock	Slack	End Point TNS
Summary (Minimum Pulse Width)			
1	clk	-3.000	-67.000

- **Report max skew summary**

Summary (Max Skew)

Nothing to report.

Analysis and synthesis report

Analysis & Synthesis Summary	
Analysis & Synthesis Status	Successful - Mon Sep 29 20:52:57 2025
Quartus II 64-Bit Version	13.0.1 Build 232 06/12/2013 SP 1 SJ Web Edition
Revision Name	i2c_project
Top-level Entity Name	i2c_project
Family	Cyclone IV GX
Total logic elements	149
Total combinational functions	147
Dedicated logic registers	64
Total registers	64
Total pins	13
Total virtual pins	0
Total memory bits	0
Embedded Multiplier 9-bit elements	0
Total GXB Receiver Channel PCS	0
Total GXB Receiver Channel PMA	0
Total GXB Transmitter Channel PCS	0
Total GXB Transmitter Channel PMA	0
Total PLLs	0