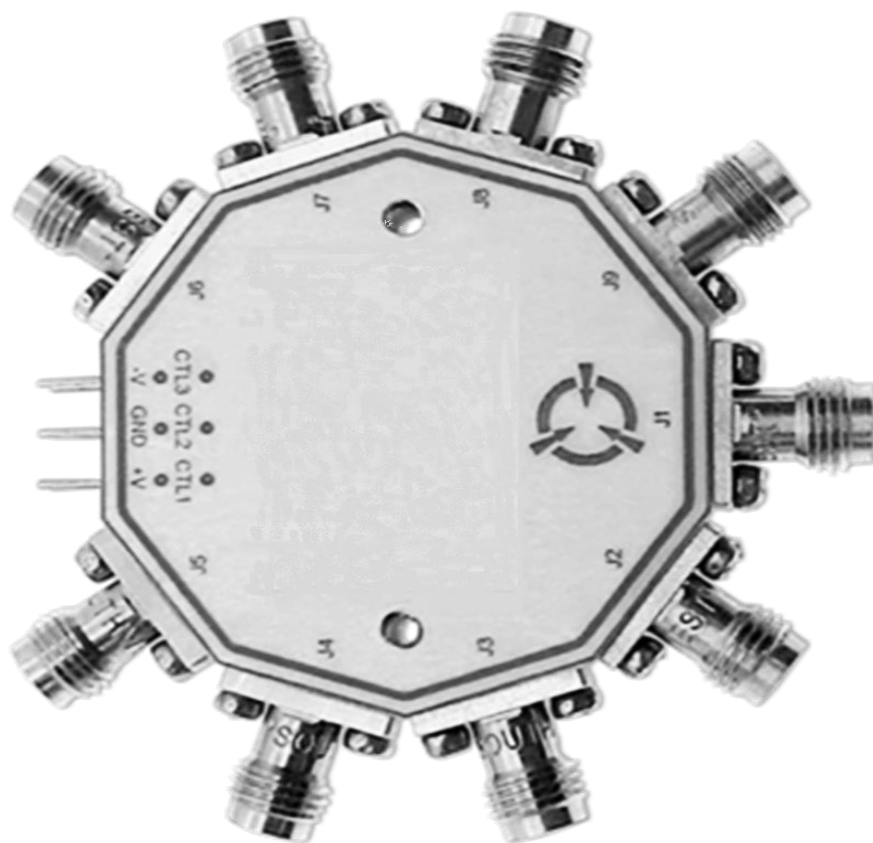


SP8T PIN DIODE ABSORPTIVE SWITCH OPERATING
Model TBIE-SW-SP8T-0218-X



TELONIC BERKELEY INC.

AS9100D AND ISO 9001:2015 CERTIFIED AND AN ITAR CONTROLLED COMPANY

SPECIFICATION

FREQUENC RANGE	0.1 TO 50 GHz
Reflective / Absorptive	Absorptive
Insertion Loss (Typ)	16 dB
Insertion Loss	6.0 dB (0.1 to 18.0 GHz) 9.0 dB (18.0 to 40.0 GHz) 16.0 dB (40.0 to 50.0 GHz)
Isolation (Typ)	90 dB (typ)
Isolation (Typ)	90 dB (0.1 to 1.0 GHz) 90 dB (1.0 to 40.0 GHz) 80 dB (40.0 to 50.0 GHz)
Input Power	20 dBm CW Max
Switching Speed (Typ)	50 ns
Switching Speed	50 ns Max
Control Signal	TTL Logic
Connectors	2.4mm (FEMALE)
VSWR I/P / O/P	2.2:1 (0.1 to 18.0 GHz) Typ 2.5:1 (18.0 to 40.0 GHz) Typ 3.5:1 (40.0 to 50.0 GHz) Typ
DC Voltage Supply	+5 V @ 400 mA MAX, -5 V @ 300 mA MAX
Finish	Gold Plated

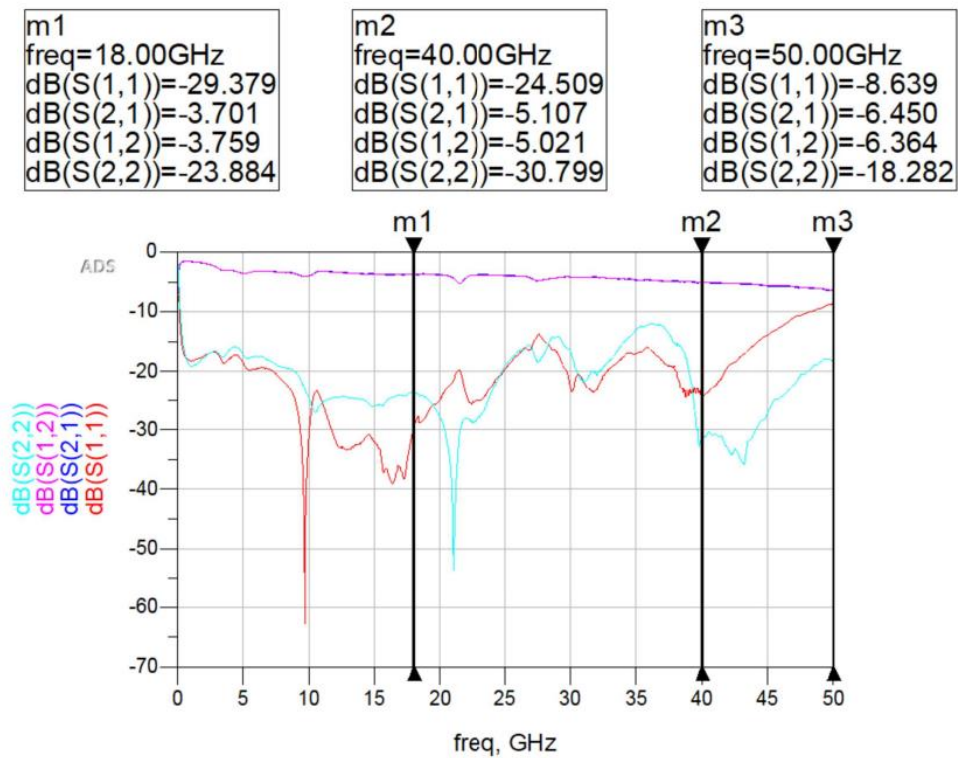
ENVIRONMENTAL CLASSIFICATION:

Max I/P power	+20dBm CW
Output Power Variation vs Input Power	±2.0dB over operating temp
Operating Temperature	-55 to +85 C
Storage Temperature	-65 to +125 C

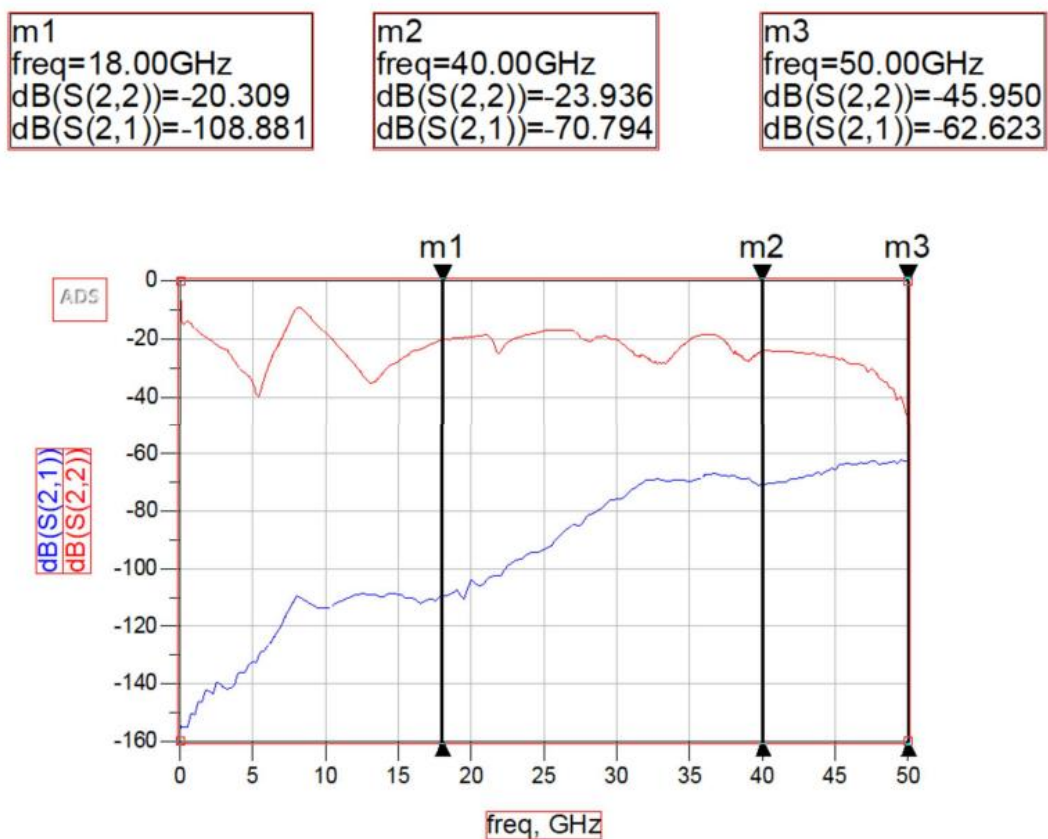
Warning: There is no protection against reverse voltage or overvoltage integrated into this device. The specifications listed above may be modified or updated.

TYPICAL PERFORMANCE PLOTS

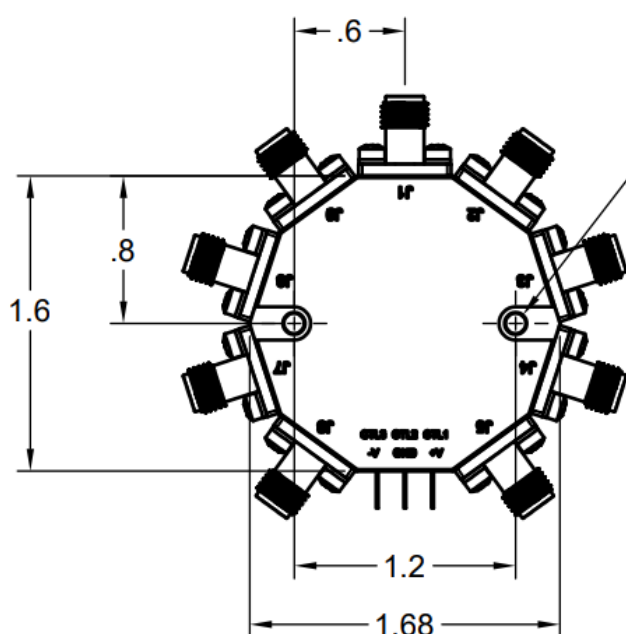
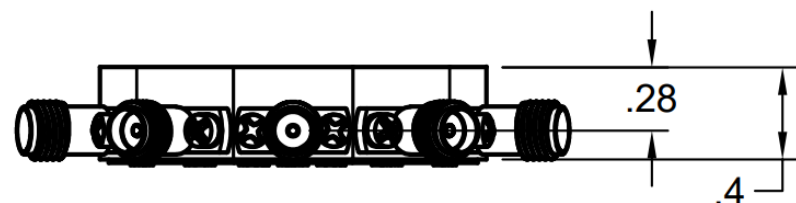
INSERTION LOSS & RETURN LOSS TYPICAL:



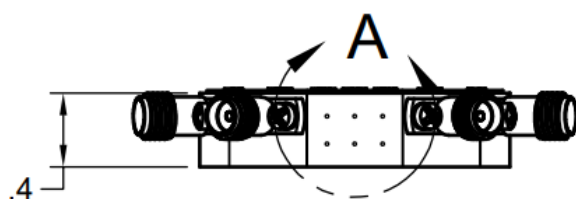
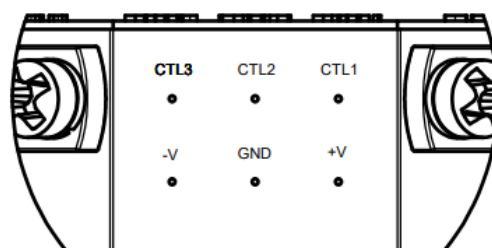
ISOLATION & OFF STATE OUTPUT PORT RETURN LOSS



OUTLINE DRAWING



MOUNTING HOLE
COUNTERBORE Ø0.12;
BY 0.3 DEEP
TAP 4-40; 0.25 DEEP MIN



TTL CONTROL LOGIC TABLE

CTL3	CTL2	CTL1	FUNCTION
0	0	0	J1 TO J2
0	0	1	J1 TO J3
0	1	0	J1 TO J4
0	1	1	J1 TO J5
1	0	0	J1 TO J6
1	0	1	J1 TO J7
1	1	0	J1 TO J8
1	1	1	J1 TO J9

ALL DIMENSIONS IN INCHES



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