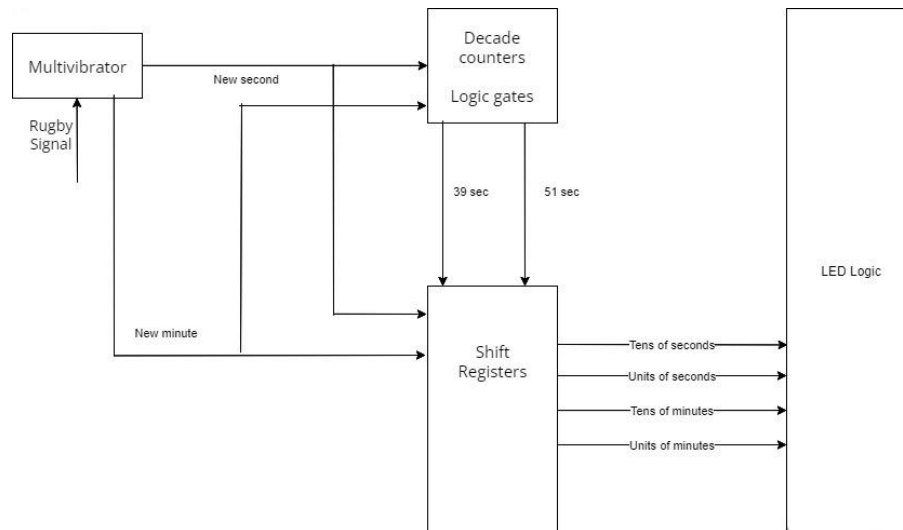# Group 10 Project Block Diagram & Test Report

The above block diagram shows the outline of Group 10's project at the moment.

At the moment, the group has designed and built the LED display and the logic necessary for a clock to function without a given signal and is designing the rest of the circuit. For the remaining parts, the decade counters, logic gates, multivibrators and shift registers are connected in Multisim and are tested in order to find the right setup. Once the logic part of the circuit is finished, it will be built and tested on the breadboard as well. The LED part of the circuit has been tested already.

The group members did not have specific tasks assigned to them. The group discussed what needs to be done next in order to accomplish the project during the meetings and tasks were divided based on the group member's preferences. The way in which each group member participated can be seen in the table above. The group members also worked in teams in order to complete the tasks therefore the same task appears for multiple members.

Group member	Completed tasks
Shivraj Bhathal	● Building and designing the LED part of the circuit ● Designing segments of the Multivibrator part of the circuit
Parul Kumari	
Boris LeBras	● Designing and testing the LED part of the circuit ● Designing and testing the Multivibrator part of the circuit
Nicoleta Marcu	● Designing the LED part of the circuit ● Designing the Multivibrator part of the

	circuit