

Parul Damahe

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EDUCATION

Texas A&M University, College Station, Texas, USA

Master of Science, Computer Engineering - GPA: 3.66 / 4

August'22 - Dec'23

Coursework : Software Engineering, Distributed Systems & Cloud computing, Internet protocols, Machine Learning, Data Mining and Analysis, Pattern Recognition

International Institute of Information Technology (IIIT), Naya Raipur

Bachelor of Technology in Electronics and Communication - GPA: 8.88 / 10.00

May'20

TECHNICAL SKILLS

Programming: Python, C++, Ruby, Django, MySQL, HTML, CSS, Javascript

PROJECTS

- **Website development - Study abroad experience reviews**([Under Prof. Philip Ritchey](#))
Developing an end-to-end platform for Texas A&M students to share and view the experiences of their abroad student exchange trips.
Consists of features like saving posts as bookmarks, adding a gallery section, getting directions links etc.
Technologies used:- Ruby on Rails, HTML, CSS, Javascript
- **Created Company Employee Management portal using Django Rest framework**
Developed company employee management portal for maintaining company-wise employee information. It performs operations like adding and retrieving the company and employee details.
Technologies used:- Django Rest Framework, Python
- **Real Estate Price Prediction**
Developed a machine learning system to predict the real estate price in Bangalore city. Performed Linear Regression, Lasso Regression and Decision Tree Regressor with 5 fold cross validation for the prediction.

WORK EXPERIENCE

Aays Analytics, India,

Software Developer in Data Science

Feb'22 - July'22

- Developed Vendor Risk Management system by combining data from five data sources. Unified them using Fuzzy logic to create vendor risk assessment system.
- Performed data modeling and data analysis on two-wheeler sales data for markets of multiple countries.

Agrometrics, India,

Software Developer in Data Science

Dec'21 - Feb'22

- Implemented Frequent Pattern Growth Algorithm to perform Market Basket Analysis of customers purchasing behaviours at brand level for an Austrian business company.
- Reduced processing time and storage of PowerBI files by moving logic from PowerBI to Databricks for Key performance indicator(KPIs) pre-calculations.

OSTTRA (IHS Markit & CME GROUP Joint Venture), India,

Software Developer Engineer in Test

Aug'20 - Dec'21

- Worked on manual tools testing and tools regression testing.
- Managed employee training for 5 employees over period of 2 months offering continuous guidance and mentorship on best practices while providing constructive feedback.

ASIC2 Lab, Technion, Israel,

Research Intern, Supervisor - Prof. Shahar Kvatinisky

Jan'20-June'20

- A neuromorphic ADC has been proposed based on the memristor (Y Flash Device fabricated by TowerJazz) technology trained using the stochastic gradient descent machine learning algorithm. Implemented algorithm using MATLAB.

PUBLICATIONS

- Arushi Shrivastava, **Parul Damahe**, V.R. Kumbhare, Manoj Kumar Majumder, "Designing SRAM Using CMOS and CNTFET at 32 nm Technology", *IEEE International Symposium on Smart Electronic Systems (iSES)*, 2019. (Published)
- **Parul Damahe**, K.S. Nikhil, "On improving the ESD immunity of SiGe FinFETs for SoC applications", *International Workshop on Physics of Semiconductor Devices (IWPSD)*, 2019. (Accepted)
- Danial L., **Damahe P.**, Agrawal P., Dhamnani R., Kvatinisky S. (2020) "Neuromorphic Data Converters using memristor". Springer Book on Emerging Computing: From Devices to Systems