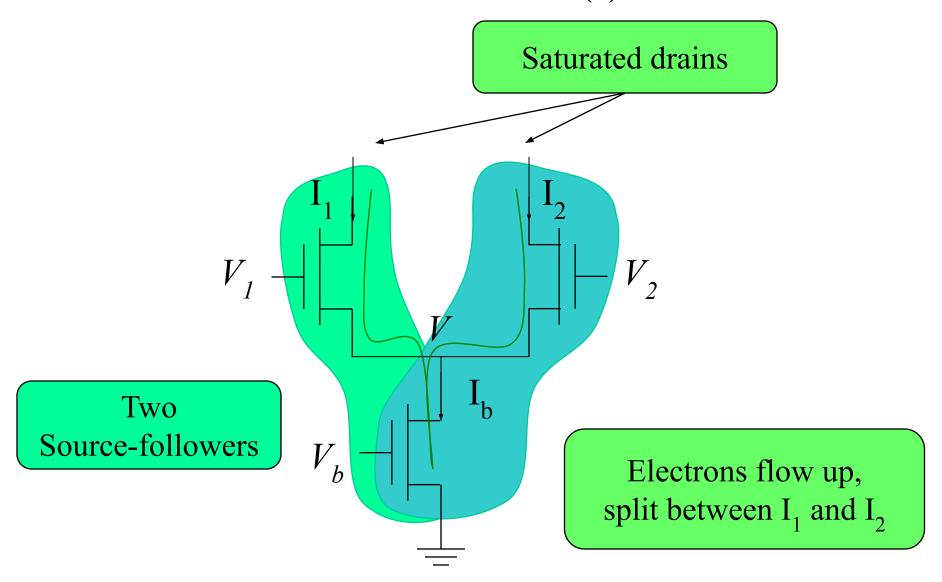
Transconductance Amplifier Shih-Chii Liu Fall 2021

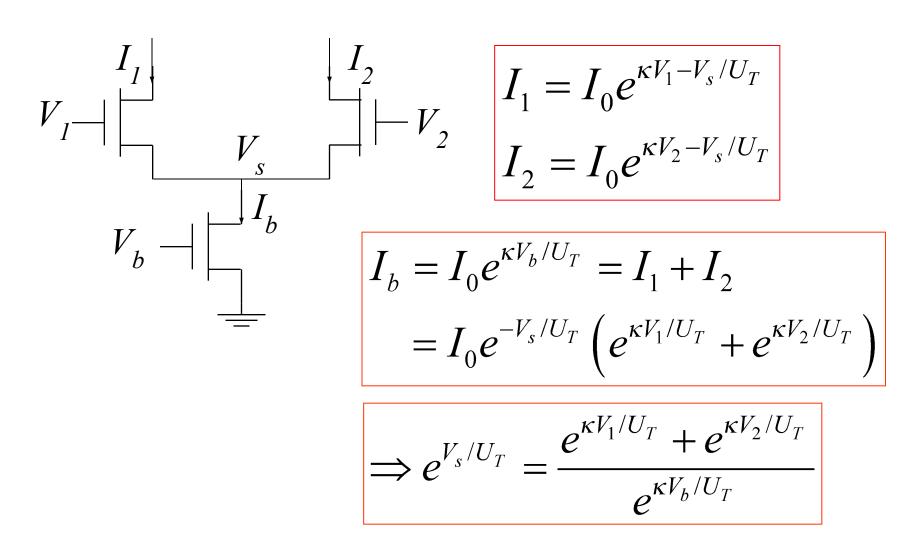
Outline:

- Differential pair
- Transconductance amplifier (and its g_m and A)
- Voltage amplifier
- Wide range transconductance amplifier

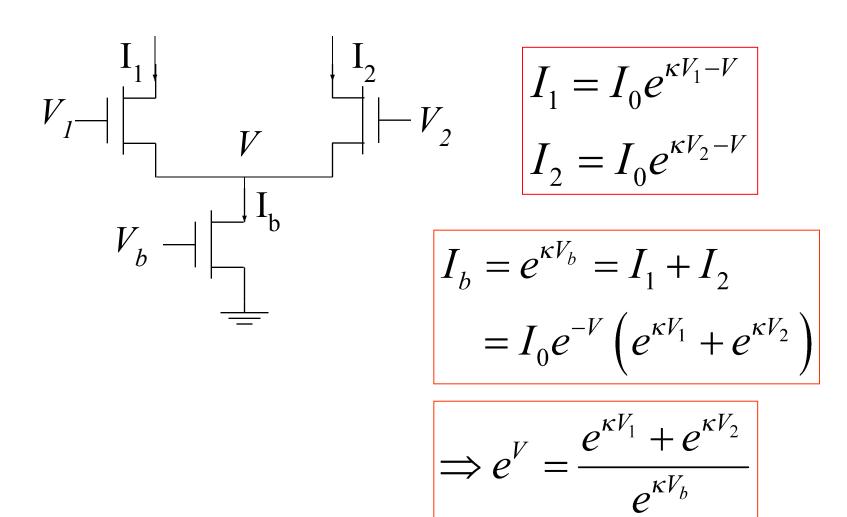
Differential Pair (I)



Differential Pair (II)



Differential Pair (II)



Differential Pair (III)

$$V_{I} = \begin{bmatrix} I_{1} \\ V \\ V_{b} \end{bmatrix} + V_{2}$$

$$V_{b} = \begin{bmatrix} V = \ln \left(e^{\kappa V_{1}} + e^{\kappa V_{2}} \right) - \kappa V_{b} \\ \approx \kappa \left(V_{1} - V_{b} \right) \text{ for } V_{1} - V_{2} \ge 100 \text{mV} \end{bmatrix}$$

Differential Pair (IV)

Output currents:

$$I_{1} = I_{b} \frac{e^{\kappa V_{1}/U_{T}}}{e^{\kappa V_{1}/U_{T}} + e^{\kappa V_{2}/U_{T}}} = \frac{I_{b}}{1 + e^{\kappa (V_{2}-V_{1})/U_{T}}}$$

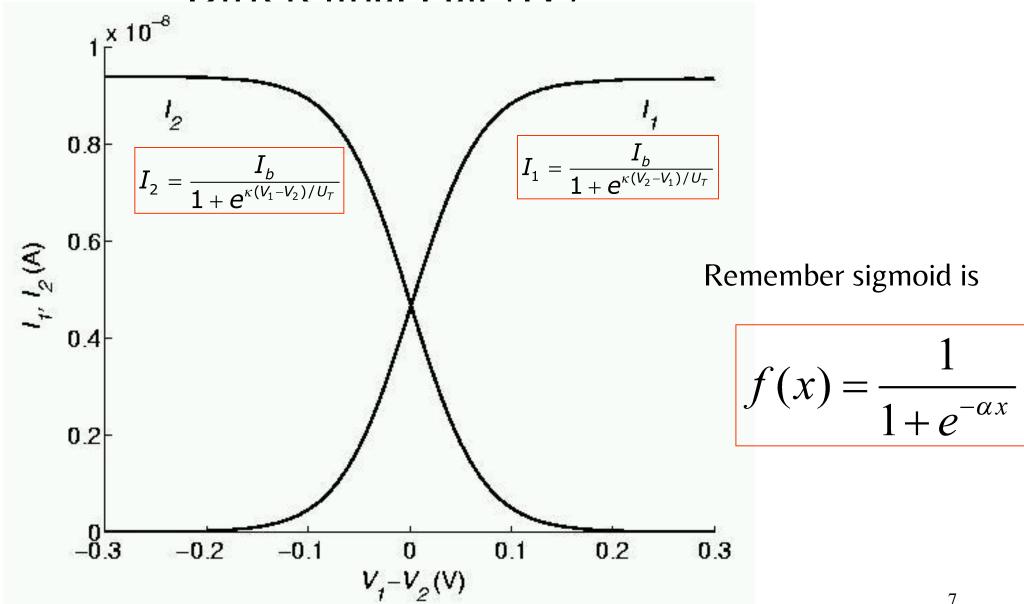
$$I_2 = \frac{I_b}{1 + e^{\kappa (V_1 - V_2)/U_T}}$$

Fermi function

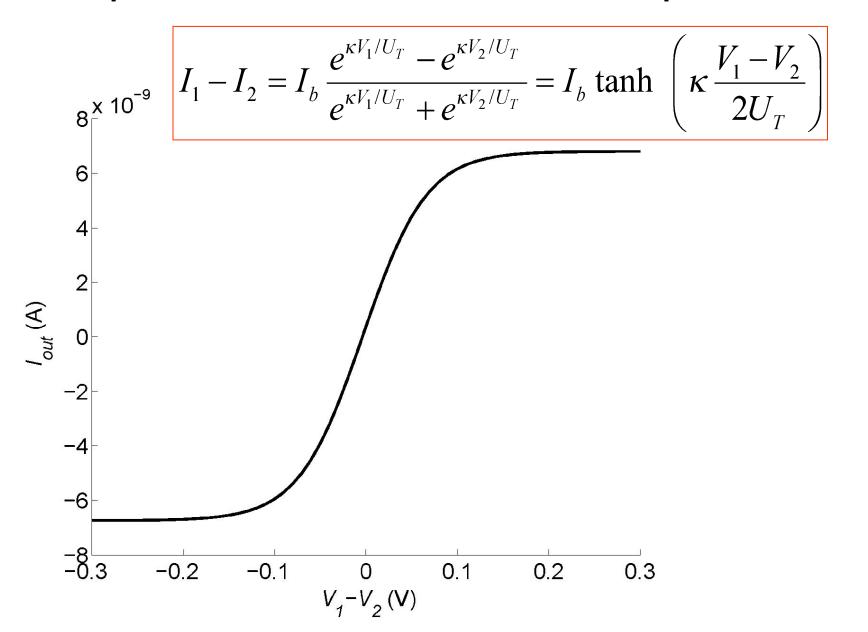
Difference of output currents:

$$I_{1} - I_{2} = I_{b} \frac{e^{\kappa V_{1}/U_{T}} - e^{\kappa V_{2}/U_{T}}}{e^{\kappa V_{1}/U_{T}} + e^{\kappa V_{2}/U_{T}}} = I_{b} \tanh \left(\kappa \frac{V_{1} - V_{2}}{2U_{T}}\right)$$

Differential Pair (IV)



Output Current vs Differential Input

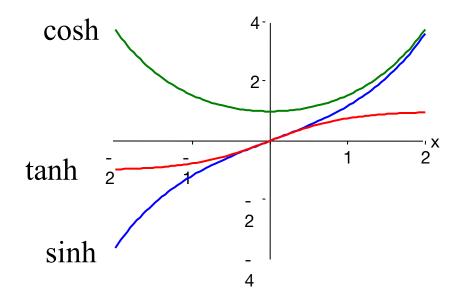


Digression: Hyperbolic functions

$$\sinh(x) = \frac{e^x - e^{-x}}{2} \approx x \text{ for small } x$$

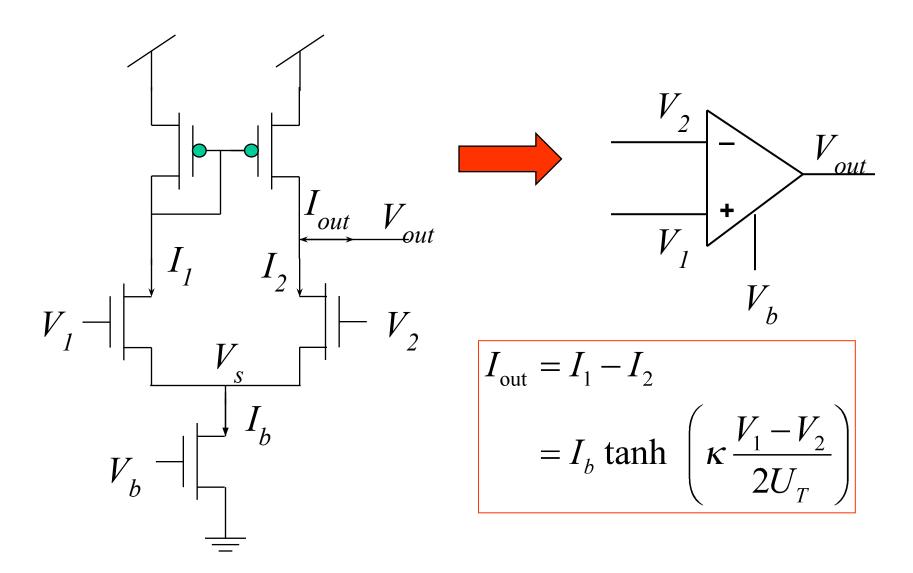
$$\cosh(x) = \frac{e^x + e^{-x}}{2} \approx 1 + \frac{x^2}{2} \text{ for small } x$$

$$\tanh(x) = \frac{\sinh(x)}{\cosh(x)} = \frac{e^x - e^{-x}}{e^x + e^{-x}} \approx x \text{ for small } x$$



tanh: compressive

sinh: expansive



In subthreshold:

For small differential voltages ($|V_1 - V_2|$ e.g. < 200mV), the tanh(.) relationship is approximately linear:

$$I_{out} = I_b \tanh \left(\kappa \frac{V_1 - V_2}{2U_T} \right)$$

can be reduced to:

$$I_{out} \approx g_m \left(V_1 - V_2 \right)$$
 where
$$g_m \approx \frac{\kappa I_b}{2U_T}$$

Output conductance is defined as:

$$g_d = -\frac{\partial I_{out}}{\partial V_{out}} \approx \frac{I_b}{V_E}$$

Transconductance in Strong Inversion

In above threshold, the output current of the diff pair is

$$I_{1} - I_{2} = \frac{\beta}{2} (V_{1} - V_{2}) \sqrt{\frac{4I_{b}}{\beta} - (V_{1} - V_{2})^{2}}$$

where
$$\beta = \mu C_{ox} \frac{W}{L}$$
.

For $|V_1 - V_2| = \sqrt{2I_b / \beta}$, the transconductance is given by

$$g_m = \sqrt{\beta I_b}$$

Differential pair in strong inversion (V)

•In **strong inversion**, solve for $I_1 \& I_2$ this way: $\Delta V \equiv V_1 - V_2 = \sqrt{\frac{2I_1}{\beta}} - \sqrt{\frac{2I_2}{\beta}}$

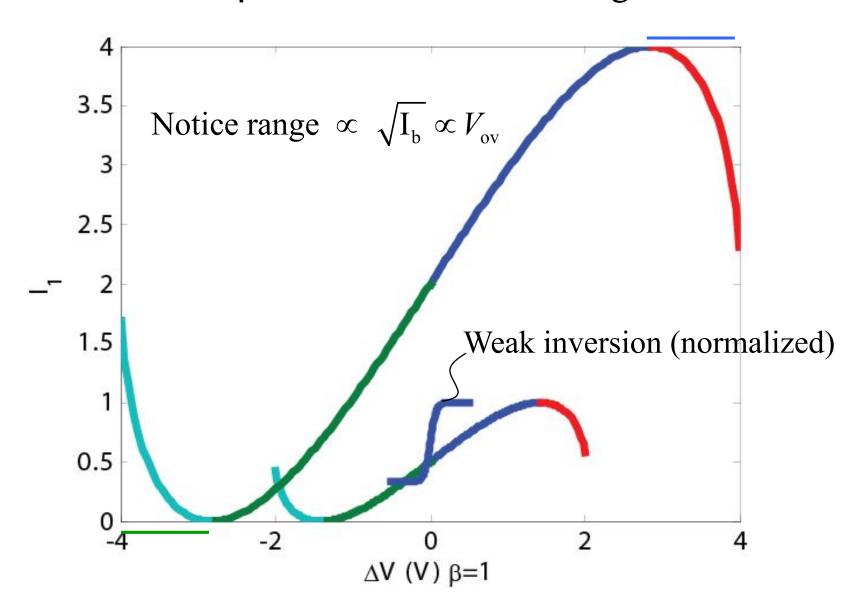
You eventually obtain

for
$$\frac{\beta \Delta V^2}{2} < I_b$$

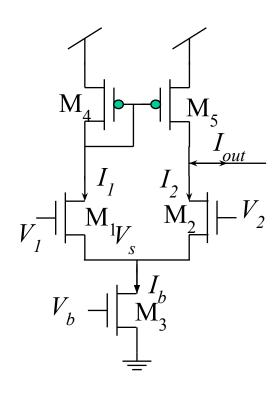
$$I_{1,2} = \frac{I_b}{2} \left(1 + -\sqrt{\frac{\beta \Delta V^2}{I_b} - \left(\frac{\beta \Delta V^2/2}{I_b}\right)^2} \right)$$

$$I_1 - I_2 = I_b \sqrt{\frac{\beta \Delta V^2}{I_b} - \left(\frac{\beta \Delta V^2/2}{I_b}\right)^2}$$

Differential pair in weak and strong inversion



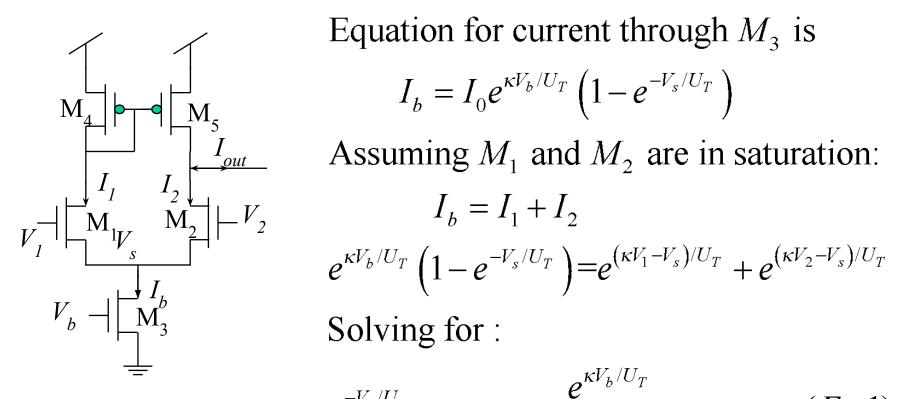
Assumptions for Deriving Tanh Output



To obtain tanh function, we assume M_1 to M_5 are in saturation:

$$I_{1} = I_{0}e^{\kappa V_{1} - V_{s}/U_{T}}$$
 $I_{2} = I_{0}e^{\kappa V_{2} - V_{s}/U_{T}}$
 $I_{b} = I_{0}e^{\kappa V_{b}/U_{T}}$
 $I_{aut} = I_{1} - I_{2}$

Deriving Common Source (I)



Assume bias transistor is not in saturation, Equation for current through M_3 is

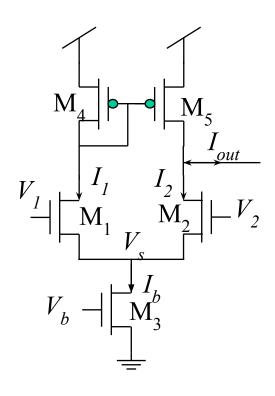
$$I_b = I_0 e^{\kappa V_b/U_T} \left(1 - e^{-V_s/U_T} \right)$$

$$I_{b} = I_{1} + I_{2}$$

$$e^{\kappa V_{b}/U_{T}} \left(1 - e^{-V_{s}/U_{T}}\right) = e^{(\kappa V_{1} - V_{s})/U_{T}} + e^{(\kappa V_{2} - V_{s})/U}$$

$$e^{-V_s/U_T} = \frac{e^{\kappa V_b/U_T}}{e^{\kappa V_b/U_T} + e^{\kappa V_1/U_T} + e^{\kappa V_2/U_T}} \quad (Eq. 1)$$

Deriving Common Source (II)



For the bias transistor to be in saturation,

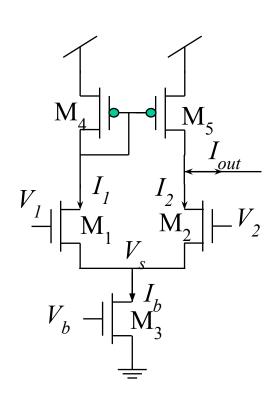
$$V_s > 4U_T$$
 therefore $e^{-V_s/U_T} \ll 1$

$$e^{-V_{s}/U_{T}} = \frac{e^{\kappa V_{b}/U_{T}}}{e^{\kappa V_{b}/U_{T}} + e^{\kappa V_{1}/U_{T}} + e^{\kappa V_{2}/U_{T}}} \ll 1$$

$$e^{\kappa V_{b}/U_{T}} \ll e^{\kappa V_{b}/U_{T}} + e^{\kappa V_{1}/U_{T}} + e^{\kappa V_{2}/U_{T}}$$

$$\frac{e^{\kappa V_{1}/U_{T}} + e^{\kappa V_{2}/U_{T}}}{e^{\kappa V_{b}/U_{T}}} \gg 1 \quad (Eq.2)$$

Deriving Common Source (III)



From

$$e^{-V_{s}/U_{T}} = \frac{e^{\kappa V_{b}/U_{T}}}{e^{\kappa V_{b}/U_{T}} + e^{\kappa V_{1}/U_{T}} + e^{\kappa V_{2}/U_{T}}}$$

we can derive:
$$V_{s} = -\kappa V_{b} + U_{T} \ln \left(e^{\kappa V_{b}/U_{T}} + e^{\kappa V_{1}/U_{T}} + e^{\kappa V_{2}/U_{T}} \right)$$

Since $e^{\kappa V_1/U_T} + e^{\kappa V_2/U_T} >> e^{\kappa V_b/U_T}$ and assuming that $|V_1 - V_2| > 4U_T$ we can simplify the ln(.).

Deriving Common Source (IV)

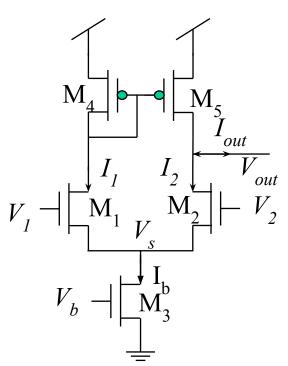
The common node voltage V_s of the transconductance amplifier is:

$$V_s = \kappa \left(\max \left(V_1, V_2 \right) - V_b \right)$$

and the saturation condition for M_3 , $V_s > 4U_T$ is:

$$\max(V_1, V_2) > V_b + \frac{4U_T}{\kappa}$$

Output Transistors



The output transistors M_2 and M_5 should also be in saturation:

To keep M_5 in saturation : $V_{out} - V_{out} > 4U_T$ To keep M_2 in saturation : $V_{out} - V_s > 4U_T$ which means

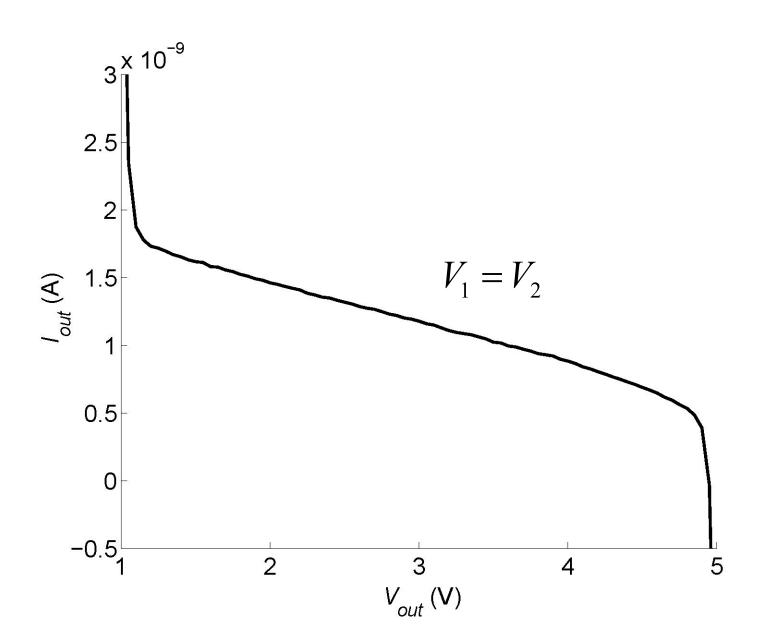
$$V_{out} > \kappa \left(\max \left(V_1, V_2 \right) - V_b \right) + 4U_T \quad (Eq.3)$$

Therefore there is a V_{\min} problem, i.e. minimum V_{out} depends on V_1 , V_2 and V_b .

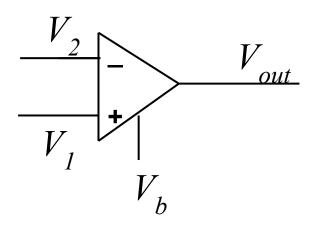
Final condition:

$$\kappa \left(\max(V_1, V_2) - V_b \right) + 4U_T < V_{out} < V_{dd} - 4U_T$$

Curves of lout vs Vout



Voltage Amplifier



Transconductance amplifier can be used a differential-input voltage amplifier: $V_{out} = A(V_1 - V_2)$

Transfer function:

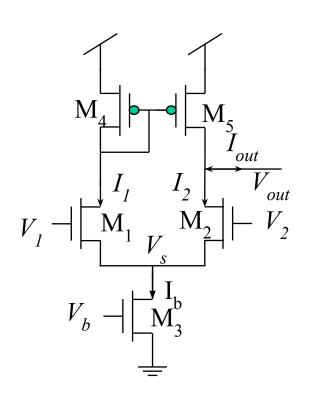
$$A = \frac{dV_{out}}{d(V_1 - V_2)} = \frac{dI_{out}}{d(V_1 - V_2)} \frac{dV_{out}}{dI_{out}} = \frac{g_m}{g_d}$$

Subthreshold: $A \approx \frac{\kappa V_E}{2U_T}$; Above threshold: $A \approx \sqrt{\frac{\beta}{I_b}} V_E$

Voltage Amplifier

- The open-circuit voltage gain *A* increases with Early voltage, and therefore with the length of the output transistors.
- Typical subthreshold values are between 100 and 1000.
- Because of the large voltage gain and transistor mismatch effects, the amplifier is usually used in a negative-feedback configuration.
- In open-voltage mode, it is used mainly as a comparator. V_{out} is "high" when $V_1 > V_2$ and vice-versa.

Output Voltage Limits (I)



Compute limits of voltage swing

a)
$$V_1 > V_2$$

For $V_1 > V_2 + 4U_T$ current through M_2 is much smaller than M_1 , V_{out} goes almost to V_{dd} to shut off M_5 .

 M_5 goes out of saturation.

b)
$$V_2 > V_1$$

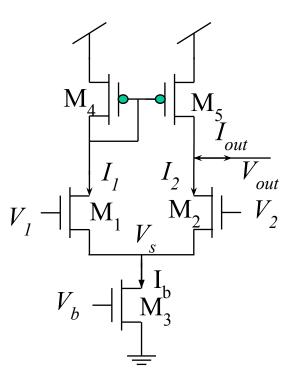
Forward current through $M_2 \gg \text{current through } M_1$.

Top current mirror forces $I_1 \approx I_2$.

If $V_2 \gg V_1$, voltage drop across M_2 is close to 0,

 $V_{out} = V_s$ and M_2 goes out of saturation.

Output Voltage Limits (II)



The output transistors M_2 and M_5 should also be in saturation:

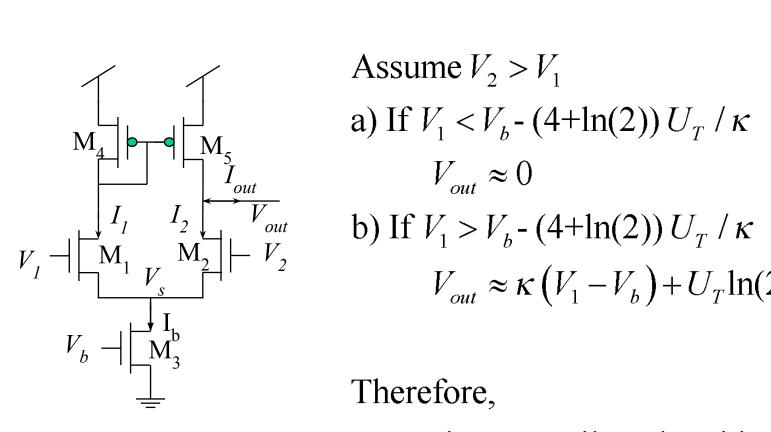
Assume
$$V_2 > V_1$$
 therefore $V_{out} \approx V_s$

$$e^{-V_s/U_T} = \frac{e^{\kappa V_b/U_T}}{e^{\kappa V_b/U_T} + e^{\kappa V_1/U_T} + e^{\kappa V_2/U_T}}$$

$$e^{-V_s/U_T} = \frac{e^{\kappa V_b/U_T} + e^{\kappa V_1/U_T} + e^{\kappa V_2/U_T}}{e^{\kappa V_b/U_T} + 2e^{\kappa V_1/U_T}}$$

$$e^{-V_s/U_T} = \frac{0.5e^{\kappa V_b/U_T}}{0.5e^{\kappa V_b/U_T} + e^{\kappa V_1/U_T}}$$

Output Voltage Limits (III)



Assume
$$V_2 > V_1$$

a) If
$$V_1 < V_b$$
- $(4+\ln(2)) U_T / \kappa$
 $V_{out} \approx 0$

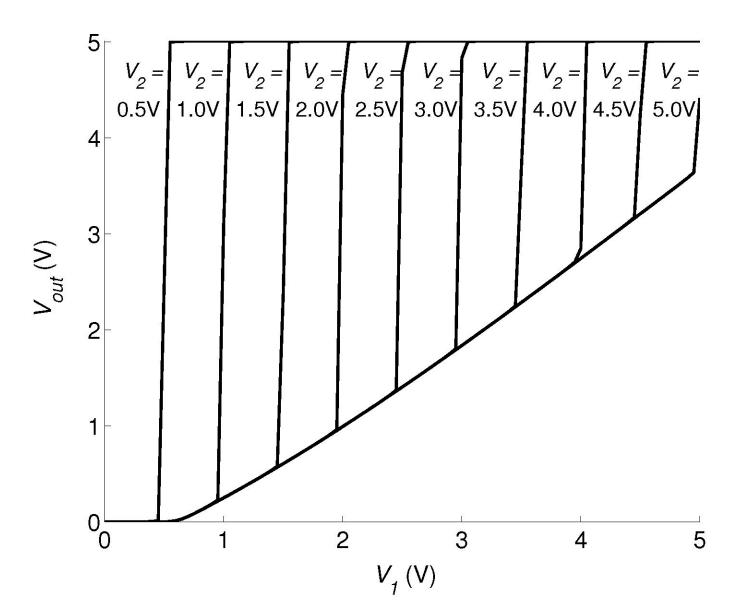
b) If
$$V_1 > V_b$$
 - $(4+\ln(2)) U_T / \kappa$

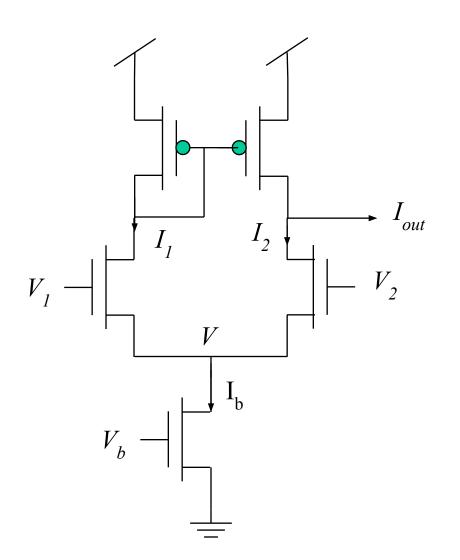
$$V_{out} \approx \kappa (V_1 - V_b) + U_T \ln(2)$$

Therefore,

 V_{out} increases linearly with V_1 with a slope of κ

Output Voltage vs Input Voltage



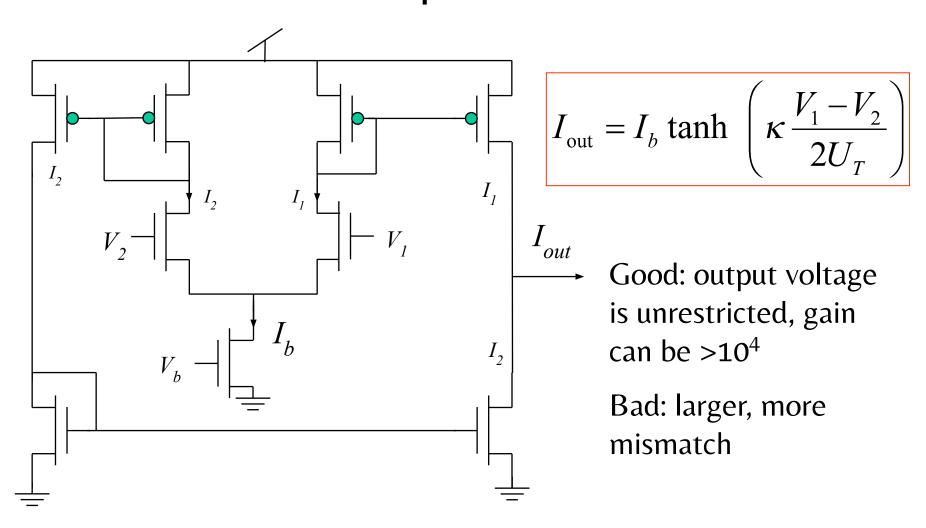


$$\begin{split} I_{\text{out}} &= I_1 - I_2 \\ &= I_b \tanh \left(\kappa \frac{V_1 - V_2}{2U_T} \right) \end{split}$$

Good: Simple, cheap

Bad: output voltage is restricted, voltage gain is limited

Wide-Output Range Transconductance Amplifier



THE END

Next week: Linear systems