

		CTRL REG	0x00	TORQUE	0x01	OFF Reg	0x02	BLANK Reg	0x03	DECAY Reg	0x04	Stall Reg	0x05	Drive	0x06	Status	0x07
bit	0	ENBL	0	TORQUE 0	100	TOFF 0	200	TBLANK 0	100	TDECAY 0	50	SDTHR 0	64	OCPTH 0	0	OTS	0
bit	1	RDIR	0	TORQUE 1		TOFF 1		TBLANK 1		TDECAY 1		SDTHR 1		OCPTH 1	0	AOCP	0
bit	2	RSTEP	0	TORQUE 2		TOFF 2		TBLANK 2		TDECAY 2		SDTHR 2		OCPDEG 0	1	BOCP	0
bit	3	Mode 0	0000: Full-step,	TORQUE 3		TOFF 3		TBLANK 3		TDECAY 3		SDTHR 3		OCPDEG 1	0	APDF	0
bit	4	Mode 1		TORQUE 4		TOFF 4		TBLANK 4		TDECAY 4		SDTHR 4		TDRIVEN 0	0	BPDF	0
bit	5	Mode 2		TORQUE 5		TOFF 5		TBLANK 5		TDECAY 5		SDTHR 5		TDRIVEN 1	1	UVLO	0
bit	6	Mode 3		TORQUE 6		TOFF 6		TBLANK 6		TDECAY 6		SDTHR 6		TDRIVEP 0	0	STD	0
bit	7	EXTSALL	0	TORQUE 7		TOFF 7		TBLANK 7		TDECAY 7		SDTHR 7		TDRIVEP 1	1	STDLAT	0
bit	8	ISGAIN 0	1	SMPLTH 0	001: 100 μs	PWMMODE	0	ABT	0	DECMOD 0	000: Force slow decay	SDCNT 0	0	IDRIVEN 0	1	Reserved	0
bit	9	ISGAIN 1	1	SMPLTH 1		Reserved	0	Reserved	0	DECMOD 1		SDCNT 1	0	IDRIVEN 1	0	Reserved	0
bit	10	DTIME 0	1	SMPLTH 2		Reserved	0	Reserved	0	DECMOD 2		VDIV 0	0	IDRIVEP 0	1	Reserved	0
bit	11	DTIME 1	1	Reserved	0	Reserved	0	Reserved	0	Reserved	0	VDIV 1	0	IDRIVEP 1	0	Reserved	0
bit	12	A0	0	A0	1	A0	0	A0	1	A0	0	A0	1	A0	0	A0	1
bit	13	A1	0	A1	0	A1	1	A1	1	A1	0	A1	0	A1	1	A1	1
bit	14	A2	0	A2	0	A2	0	A2	0	A2	1	A2	1	A2	1	A2	1
bit	15	RW	0	RW	0	RW	0	RW	0	RW	0	RW	0	RW	0	RW	0
		MSB	F	MSB	11	MSB	20	MSB	30	MSB	40	MSB	50	MSB	65	MSB	70
		LSB	0	LSB	64	LSB	C8	LSB	64	LSB	32	LSB	40	LSB	A4	LSB	0
		Copy	[0xF 0x0]	Copy	[0x11 0x64]	Copy	[0x20 0xC8]	Copy	[0x30 0x64]	Copy	[0x40 0x32]	Copy	[0x50 0x40]	Copy	[0x65 0xA4]	Copy	[0x70 0x0]