

		CTRL REG	0x00	TORQUE	0x01	OFF Reg	0x02	BLANK Reg	0x03
bit	0	ENBL	0	TORQUE 0	100	TOFF 0	200	TBLANK 0	100
bit	1	RDIR	0	TORQUE 1		TOFF 1		TBLANK 1	
bit	2	RSTEP	0	TORQUE 2		TOFF 2		TBLANK 2	
bit	3	Mode 0	0000: Full-step,	TORQUE 3		TOFF 3		TBLANK 3	
bit	4	Mode 1		TORQUE 4		TOFF 4		TBLANK 4	
bit	5	Mode 2		TORQUE 5		TOFF 5		TBLANK 5	
bit	6	Mode 3		TORQUE 6		TOFF 6		TBLANK 6	
bit	7	EXTSALL	0	TORQUE 7		TOFF 7		TBLANK 7	
bit	8	ISGAIN 0	1	SMPLTH 0	001: 100 $\mu$ s	PWMMODE	0	ABT	0
bit	9	ISGAIN 1	1	SMPLTH 1		Reserved	0	Reserved	0
bit	10	DTIME 0	1	SMPLTH 2		Reserved	0	Reserved	0
bit	11	DTIME 1	1	Reserved	0	Reserved	0	Reserved	0
bit	12	A0	0	A0	1	A0	0	A0	1
bit	13	A1	0	A1	0	A1	1	A1	1
bit	14	A2	0	A2	0	A2	0	A2	0
bit	15	RW	0	RW	0	RW	0	RW	0
		MSB	F	MSB	11	MSB	20	MSB	30
		LSB	0	LSB	64	LSB	C8	LSB	64
		Copy	[0xF 0x0]	Copy	[0x11 0x64]	Copy	[0x20 0xC8]	Copy	[0x30 0x64]