

My L^AT_EX Template

pastglory*

摘要

This is abstract

1 简介

你好, L^AT_EX! 这是另一篇论文 [1]. 现在使用一句长句来调行间距, 以达到自定义命令的需求

下图为 FPGA 基本单元结构图。

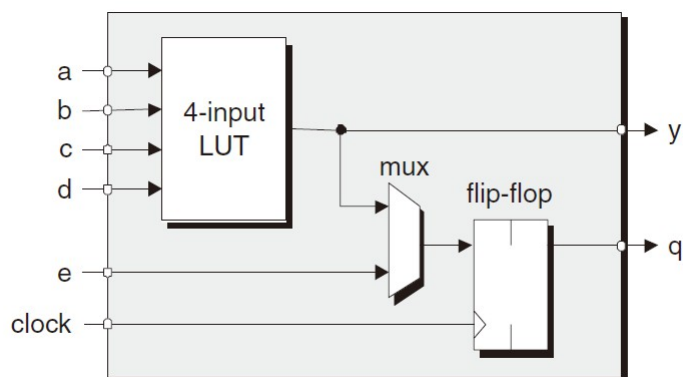


图 1: Slice of FPGA

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参考文献

- [1] M. Sadi, G. K. Contreras, J. Chen, L. Winemberg, and M. Tehranipoor, “Design of reliable socs with bist hardware and machine learning,” *IEEE Transactions on Very Large Scale Integration (VLSI) Systems*, vol. 25, no. 11, pp. 3237–3250, 2017.