

My L^AT_EX Template

pastglory*

2020 年 12 月 24 日

Hello, L^AT_EX. This is a paper [1].

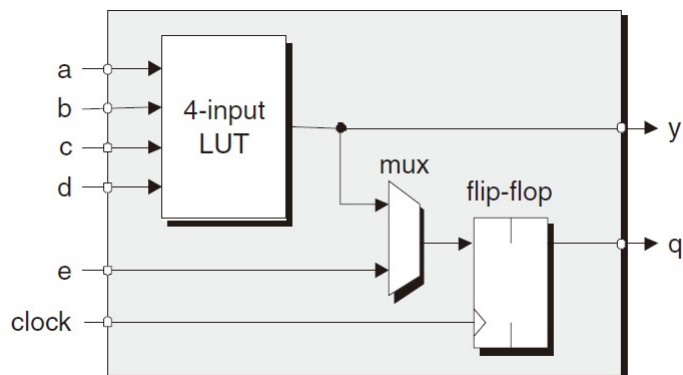


图 1: Slice of FPGA

参考文献

- [1] Z. Zhang, H. Wang, S. Han, and W. J. Dally, “Sparch: Efficient architecture for sparse matrix multiplication,” in *2020 IEEE International Symposium on High Performance Computer Architecture (HPCA)*. IEEE, 2020, pp. 261–274.

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