

My L^AT_EX Template

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摘要

这是一段摘要

第一章 简介

Hello, L^AT_EX. This is a paper [1]. 你好, L^AT_EX! 这是另一篇论文 [2]. 现在使用一句长句来调行间距, 以达到自定义命令的需求

第二章 测试

只是一个测试

2.1 结构

下图为 FPGA 基本单元结构图。

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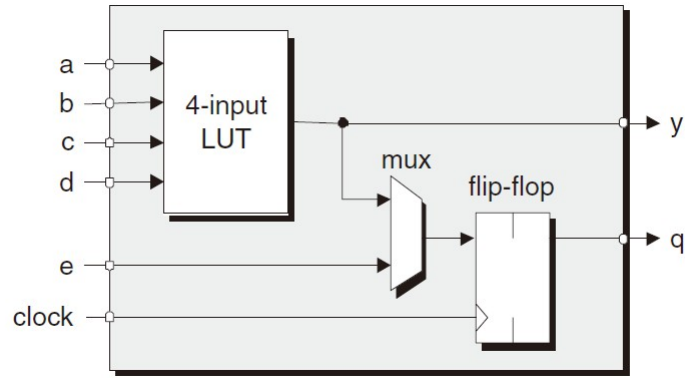


图 1: Slice of FPGA

参考文献

- [1] Z. Zhang, H. Wang, S. Han, and W. J. Dally, "Sparch: Efficient architecture for sparse matrix multiplication," in *2020 IEEE International Symposium on High Performance Computer Architecture (HPCA)*. IEEE, 2020, pp. 261–274.
- [2] M. Sadi, G. K. Contreras, J. Chen, L. Winemberg, and M. Tehranipoor, "Design of reliable socs with bist hardware and machine learning," *IEEE Transactions on Very Large Scale Integration (VLSI) Systems*, vol. 25, no. 11, pp. 3237–3250, 2017.