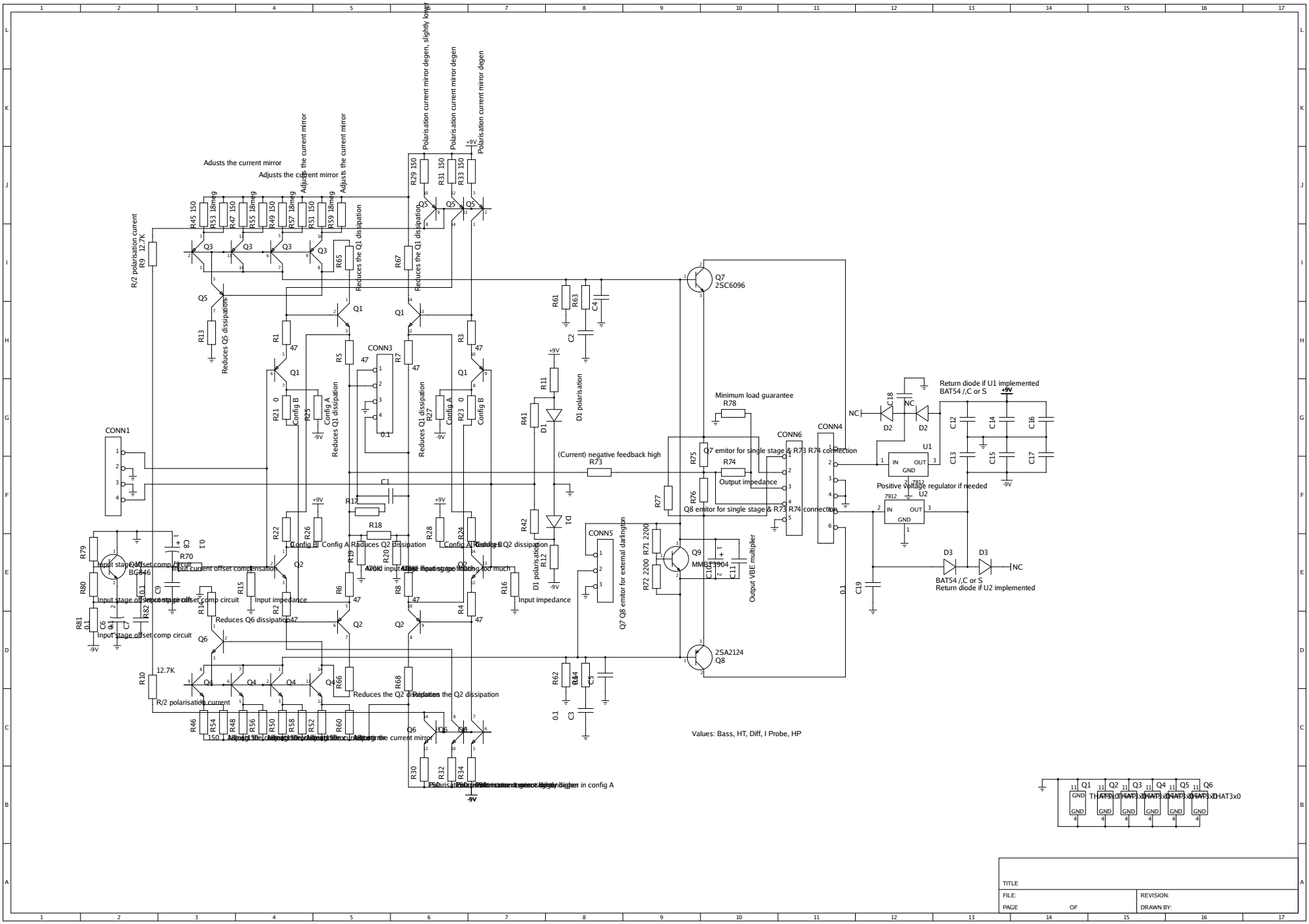
Polarisation current mirror degen, slightly lower in config A
Polarisation current mirror degen
Polarisation current mirror degen
Adusts the current mirror
Adjusts the current mirror
Adjusts the current mirror
Adjusts the current mirror
R/2 polarisation current
R9 12.7K
R45 150
R53 18meg
R47 150
R55 18meg
R49 150
R57 18meg
R51 150
R59 18meg
R29 150
R31 150
R33 150
Q5
Q3
R65
R67
Reduces the Q1 dissipation
Reduces the Q1 dissipation
R13
Reduces Q5 dissipation
Q1
R1 47
R3 47
R5 47
R7 47
CONN3
R21 0
R23 0
Config B
Config A
R25
R27
Reduces Q1 dissipation
Reduces Q1 dissipation
R61
R63
C4
C2
Q7
2SC6096
R11
D1 polarisation
R41
D1
Minimum load guarantee
R78
CONN6
CONN4
Return diode if U1 implemented
BAT54 /,C or S
NC
D2
C18
C12
C14
C16
C13
C15
C17
U1
7912
IN
OUT
GND
Positive voltage regulator if needed
U2
CONN1
(Current) negative feedback high
R73
R75
Q7 emitor for single stage & R73 R74 connection
R74
Output impedance
R76
Q8 emitor for single stage & R73 R74 connection
R77
C1
R17
R18
R22
R26
R28
R24
Config B
Config A Reduces Q2 dissipation
Config A Reduces Q2 dissipation
Config B
R42
D1
D1 polarisation
R12
CONN5
Q7 Q8 emitor for external darlington
R71 2200
R72 2200
Q9
MMBT3904
C13
C11
Output VBE multiplier
D3
BAT54 /,C or S
Return diode if U2 implemented
C19
R79
Q4
BC846
Input stage offset comp circuit
Input current offset compensation
C8
R70
R80
R81
Q6
C7
R82
C9
R14
R15
R2
R6
R8
R4
R16
Input impedance
Input impedance
Reduces Q6 dissipation
Q2
Q6
R10
12.7K
R/2 polarisation current
R66
R68
Reduces the Q2 dissipation
Reduces the Q2 dissipation
R62
C5
C3
R46
R54
R48
R56
R50
R58
R52
R60
Adjusts the current mirror
R30
R32
R34
Q8
2SA2124
Values: Bass, HT, Diff, I Probe, HP
THAT3x0
TITLE
FILE:
REVISION:
PAGE
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