* Format of Test Profiles
* EFI Requirements Test Profile

**File Path: SCT\Dependency\EfiCompliantBBTest\EfiCompliant.Ini**

[Platform Specific]

ConsoleDevices = <yes: if this platform includes console devices>

GraphicalConsoleDevices = <yes: if this platform includes graphical console devices>

PointerDevices = <yes: if this platform includes a pointer device as part of its console support>

BootFromDiskDevices = <yes: if this platform supports to boot from a disk device>

BootFromNetworkDevices = <yes: if this platform supports to boot from a network device>

UartDevices = <yes: if this platform includes a byte-stream device such as a UART>

PciBusSupport = <yes: if this platform includes PCI bus support>

UsbBusSupport = <yes: if this platform includes USB bus support>

ScsiPassThru = <yes: if this platform includes an I/O system that uses SCSI command packets>

DebugSupport = <yes: if this platform supports debugging capabilities>

PlatformDriverOverride = <yes: includes the ability to override the default driver>

* EFI\_PCI\_ROOT\_BRIDGE\_IO\_PROTOCOL Test Profile

[PollMem\_Func]

DevicePath = <The PCI root bridge device path string>

Address = <The memory address controlled by this root bridge>

RootBridgeIoWidth = <The EFI\_PCI\_ROOT\_BRIDGE\_IO\_PROTOCOL\_WIDTH>

TargetValue = <The target value to be set and polled in destination address, in hex format>

AlternateValue = <The alternate value to be set in destination address, in hex format>

[PollIo\_Func]

DevicePath = <The PCI root bridge device path string>

Address = <The Io address controlled by this root bridge>

RootBridgeIoWidth = <The EFI\_PCI\_ROOT\_BRIDGE\_IO\_PROTOCOL\_WIDTH>

TargetValue = <The target value to be set and polled in destination address, in hex format>

AlternateValue = <The alternate value to be set in destination address, in hex format>

[MemRead\_Func]

DevicePath = <The PCI root bridge device path string>

Address = <The memory address controlled by this root bridge>

RootBridgeIoWidth = <The EFI\_PCI\_ROOT\_BRIDGE\_IO\_PROTOCOL\_WIDTH>

Length = <The tested address length, in hex format>

DataUnits = <The data unit to be written in to tested area, this item can be **NULL**>

[MemWrite\_Func]

DevicePath = <The PCI root bridge device path string>

Address = <The memory address controlled by this root bridge>

RootBridgeIoWidth = <The EFI\_PCI\_ROOT\_BRIDGE\_IO\_PROTOCOL\_WIDTH>

Length = <The tested address length, in hex format>

DataUnits = <The data unit to be written in to tested area, this item can be **NULL**>

[IoRead\_Func]

DevicePath = <The PCI root bridge device path string>

Address = <The Io address controlled by this root bridge>

RootBridgeIoWidth = <The EFI\_PCI\_ROOT\_BRIDGE\_IO\_PROTOCOL\_WIDTH>

Length = <The tested address length, in hex format>

DataUnits = <The data unit to be written in to tested area, this item can be **NULL**>

[IoWrite\_Func]

DevicePath = <The PCI root bridge device path string>

Address = <The Io address controlled by this root bridge>

RootBridgeIoWidth = <The EFI\_PCI\_ROOT\_BRIDGE\_IO\_PROTOCOL\_WIDTH>

Length = <The tested address length, in hex format>

DataUnits = <The data unit to be written in to tested area, this item can be **NULL**>

[PciRead\_Func]

DevicePath = <The PCI root bridge device path string>

Address = <The PCI address controlled by this root bridge>

RootBridgeIoWidth = <The EFI\_PCI\_ROOT\_BRIDGE\_IO\_PROTOCOL\_WIDTH>

Length = <The tested address length, in hex format>

DataUnits = <The data unit to be written in to tested area, this item can be **NULL**>

[PciWrite\_Func]

DevicePath = <The PCI root bridge device path string>

Address = <The PCI address controlled by this root bridge>

RootBridgeIoWidth = <The EFI\_PCI\_ROOT\_BRIDGE\_IO\_PROTOCOL\_WIDTH>

Length = <The tested address length, in hex format>

DataUnits = <The data unit to be written in to tested area, this item can be **NULL**>

[CopyMem\_Func]

DevicePath = <The PCI root bridge device path string>

Address = <The memory address controlled by this root bridge>

RootBridgeIoWidth = <The EFI\_PCI\_ROOT\_BRIDGE\_IO\_PROTOCOL\_WIDTH>

Length = <The tested address length, in hex format>

DataUnits = <The data unit to be written in to tested area, this item can be **NULL**>

[MemRead\_Conf]

DevicePath = <The PCI root bridge device path string>

RootBridgeIoWidth = <The EFI\_PCI\_ROOT\_BRIDGE\_IO\_PROTOCOL\_WIDTH invalid for this system>

[MemWrite\_Conf]

DevicePath = <The PCI root bridge device path string>

RootBridgeIoWidth = <The EFI\_PCI\_ROOT\_BRIDGE\_IO\_PROTOCOL\_WIDTH invalid for this system>

[IoRead\_Conf]

DevicePath = <The PCI root bridge device path string>

RootBridgeIoWidth = <The EFI\_PCI\_ROOT\_BRIDGE\_IO\_PROTOCOL\_WIDTH invalid for this system>

[IoWrite\_Conf]

DevicePath = <The PCI root bridge device path string>

RootBridgeIoWidth = <The EFI\_PCI\_ROOT\_BRIDGE\_IO\_PROTOCOL\_WIDTH invalid for this system>

[PciRead\_Conf]

DevicePath = <The PCI root bridge device path string>

RootBridgeIoWidth = <The EFI\_PCI\_ROOT\_BRIDGE\_IO\_PROTOCOL\_WIDTH invalid for this system>

[PciWrite\_Conf]

DevicePath = <The PCI root bridge device path string>

RootBridgeIoWidth = <The EFI\_PCI\_ROOT\_BRIDGE\_IO\_PROTOCOL\_WIDTH invalid for this system>

[CopyMem\_Conf]

DevicePath = <The PCI root bridge device path string>

RootBridgeIoWidth = <The EFI\_PCI\_ROOT\_BRIDGE\_IO\_PROTOCOL\_WIDTH invalid for this system>

* ��EFI\_PCI\_IO\_PROTOCOL Test Profile

[PollMem\_Func]

DevicePath = <The Pci Device Path String>

BarIndex = <The BAR Index valid value is 0-5>

AddressOffset = <The Address offset in this BAR, in hex format>

PciIoWidth = <The EFI\_PCI\_IO\_PROTOCOL\_WIDTH. For example EfiPciIoWidthUint8>

TargetValue = <The target value to Poll in destination address, in hex format>

AlternateValue = <The alternate value set in destination address, in hex format>

[PollIo\_Func]

DevicePath = <The Pci Device Path String>

BarIndex = <The BAR Index valid value is 0-5>

AddressOffset = <The Address offset in this BAR, in hex format>

PciIoWidth = <The EFI\_PCI\_IO\_PROTOCOL\_WIDTH. For example EfiPciIoWidthUint8>

TargetValue = <The target value to Poll in destination address, in hex format>

AlternateValue = <The alternate value set in destination address, in hex format>

[MemRead\_Func]

DevicePath = <The Pci Device Path String>

BarIndex = <The BAR Index valid value is 0-5>

AddressOffset = <The Address offset in this BAR, in hex format>

Length = <The Address length to be tested, in hex format>

PciIoWidth = <The EFI\_PCI\_IO\_PROTOCOL\_WIDTH. For example EfiPciIoWidthUint8>

DatUnits = <The data units to be write into the destination address, can be **NULL**>

[MemWrite\_Func]

DevicePath = <The Pci Device Path String>

BarIndex = <The BAR Index valid value is 0-5>

AddressOffset = <The Address offset in this BAR, in hex format>

Length = <The Address length to be tested, in hex format>

PciIoWidth = <The EFI\_PCI\_IO\_PROTOCOL\_WIDTH. For example EfiPciIoWidthUint8>

DatUnits = <The data units to be write into the destination address, can be **NULL**>

[IoRead\_Func]

DevicePath = <The Pci Device Path String>

BarIndex = <The BAR Index valid value is 0-5>

AddressOffset = <The Address offset in this BAR, in hex format>

Length = <The Address length to be tested, in hex format>

PciIoWidth = <The EFI\_PCI\_IO\_PROTOCOL\_WIDTH. For example EfiPciIoWidthUint8>

DatUnits = <The data units to be write into the destination address, can be **NULL**>

[IoWrite\_Func]

DevicePath = <The Pci Device Path String>

BarIndex = <The BAR Index valid value is 0-5>

AddressOffset = <The Address offset in this BAR, in hex format>

Length = <The Address length to be tested, in hex format>

PciIoWidth = <The EFI\_PCI\_IO\_PROTOCOL\_WIDTH. For example EfiPciIoWidthUint8>

DatUnits = <The data units to be write into the destination address, can be **NULL**>

[PciRead\_Func]

DevicePath = <The Pci Device Path String>

AddressOffset = <The Address offset in configuration space for this device, in hex format>

Length = <The Address length to be tested, in hex format>

PciIoWidth = <The EFI\_PCI\_IO\_PROTOCOL\_WIDTH. For example EfiPciIoWidthUint8>

DatUnits = <The data units to be write into the destination address, can be **NULL**>

[PciWrite\_Func]

DevicePath = <The Pci Device Path String>

AddressOffset = <The Address offset in configuration space for this device, in hex format>

Length = <The Address length to be tested, in hex format>

PciIoWidth = <The EFI\_PCI\_IO\_PROTOCOL\_WIDTH. For example EfiPciIoWidthUint8>

DatUnits = <The data units to be write into the destination address, can be **NULL**>

[CopyMem\_Func]

DevicePath = <The Pci Device Path String>

SrcBarIndex = <Source BAR index valid value is 0-5>

DestBarIndex = <Destination BAR index valid value is 0-5>

SrcAddressOffset = <The address offset in source BAR resource>

DestAddressOffset = <The address offset in destination BAR resource>

Length = <The Address length to be tested, in hex format>

PciIoWidth = <The EFI\_PCI\_IO\_PROTOCOL\_WIDTH. For example EfiPciIoWidthUint8>

DatUnits = <The data units to be write into the source address, can be **NULL**>

[PollMem\_Conf]

DevicePath = <The Pci Device Path String>

PciIoWidth = <The invalid EFI\_PCI\_IO\_PROTOCOL\_WIDTH. For example EfiPciIoWidthUint64 on IA32 platform>

[PollIo\_Conf]

DevicePath = <The Pci Device Path String>

PciIoWidth = <The invalid EFI\_PCI\_IO\_PROTOCOL\_WIDTH. For example EfiPciIoWidthUint64 on IA32 platform>

[MemRead\_Conf]

DevicePath = <The Pci Device Path String>

PciIoWidth = <The invalid EFI\_PCI\_IO\_PROTOCOL\_WIDTH. For example EfiPciIoWidthUint64 on IA32 platform>

[MemWrite\_Conf]

DevicePath = <The Pci Device Path String>

PciIoWidth = <The invalid EFI\_PCI\_IO\_PROTOCOL\_WIDTH. For example EfiPciIoWidthUint64 on IA32 platform>

[IoRead\_Conf]

DevicePath = <The Pci Device Path String>

PciIoWidth = <The invalid EFI\_PCI\_IO\_PROTOCOL\_WIDTH. For example EfiPciIoWidthUint64 on IA32 platform>

[IoWrite\_Conf]

DevicePath = <The Pci Device Path String>

PciIoWidth = <The invalid EFI\_PCI\_IO\_PROTOCOL\_WIDTH. For example EfiPciIoWidthUint64 on IA32 platform>

[PciRead\_Conf]

DevicePath = <The Pci Device Path String>

PciIoWidth = <The invalid EFI\_PCI\_IO\_PROTOCOL\_WIDTH. For example EfiPciIoWidthUint64 on IA32 platform>

[PciWrite\_Conf]

DevicePath = <The Pci Device Path String>

PciIoWidth = <The invalid EFI\_PCI\_IO\_PROTOCOL\_WIDTH. For example EfiPciIoWidthUint64 on IA32 platform>

[CopyMem\_Conf]

DevicePath = <The Pci Device Path String>

PciIoWidth = <The invalid EFI\_PCI\_IO\_PROTOCOL\_WIDTH. For example EfiPciIoWidthUint64 on IA32 platform>

* �EFI\_DEVICE\_IO\_PROTOCOL Test Profile

[MemRead\_Func]

DevicePath = <The Device IO Protocol instance device path>

ValidBaseAddress = <The Memory address in this Device>

ValidEfiIoWidth = <The valid EFI\_IO\_WIDTH value>

Length = <The Data length to be tested>

[MemWrite\_Func]

DevicePath = <The Device IO Protocol instance device path>

ValidBaseAddress = <The Memory address in this Device>

ValidEfiIoWidth = <The valid EFI\_IO\_WIDTH value>

Length = <The Data length to be tested>

[IoRead\_Func]

DevicePath = <The Device IO Protocol instance device path>

ValidBaseAddress = <The Io address in this Device>

ValidEfiIoWidth = <The valid EFI\_IO\_WIDTH value>

Length = <The Data length to be tested>

[IoWrite\_Func]

DevicePath = <The Device IO Protocol instance device path>

ValidBaseAddress = <The Io address in this Device>

ValidEfiIoWidth = <The valid EFI\_IO\_WIDTH value>

Length = <The Data length to be tested>

[PciRead\_Func]

DevicePath = <The Device IO Protocol instance device path>

ValidBaseAddress = <The PCI address>

ValidEfiIoWidth = <The valid EFI\_IO\_WIDTH value>

Length = <The Data length to be tested>

DataUnits = <The data for this PCI address range>

[PciWrite\_Func]

DevicePath = <The Device IO Protocol instance device path>

ValidBaseAddress = <The PCI address >

ValidEfiIoWidth = <The valid EFI\_IO\_WIDTH value>

Length = <The Data length to be tested>

DataUnits = <The data to be written for this PCI address range>

[MemRead\_Conf]

DevicePath = <The Device IO Protocol instance device path>

ValidBaseAddress = <The Memory address in this device>

InvalidEfiIoWidth = <The EFI\_IO\_WIDTH invalid for this system>

[MemWrite\_Conf]

DevicePath = <The Device IO Protocol instance device path>

ValidBaseAddress = <The Memory address in this device>

InvalidEfiIoWidth = <The EFI\_IO\_WIDTH invalid for this system>

[IoRead\_Conf]

DevicePath = <The Device IO Protocol instance device path>

ValidBaseAddress = <The Io address in this device>

InvalidEfiIoWidth = <The EFI\_IO\_WIDTH invalid for this system>

[IoWrite\_Conf]

DevicePath = <The Device IO Protocol instance device path>

ValidBaseAddress = <The Io address in this device>

InvalidEfiIoWidth = <The EFI\_IO\_WIDTH invalid for this system>

[PciRead\_Conf]

DevicePath = <The Device IO Protocol instance device path>

ValidBaseAddress = <The Valid PCI address >

InvalidEfiIoWidth = <The EFI\_IO\_WIDTH invalid for this system>

[PciWrite\_Conf]

DevicePath = <The Device IO Protocol instance device path>

ValidBaseAddress = <The Valid PCI address >

InvalidEfiIoWidth = <The EFI\_IO\_WIDTH invalid for this system>

[AllocateBuffer\_Conf]

DevicePath = <The Device IO Protocol instance device path>

InvalidBaseAddress= <The memory address invalid for this system>

[PciDevicePath\_Conf]

DevicePath = <The Device IO Protocol instance device path>

InvalidBaseAddress= <The PCI address invalid for this system>

>