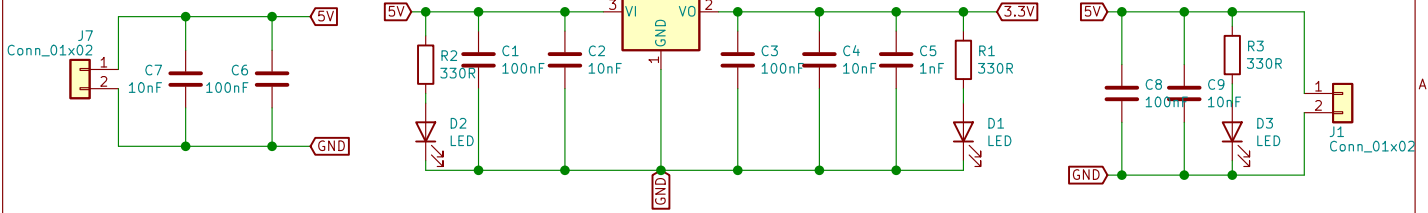
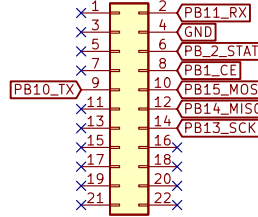


FREQUENCY SYNTH. 3.3V REGULATION

U2
AMS1117-3.3

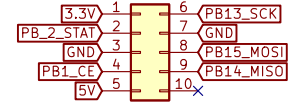


J5
Conn_02x11_Odd_Even



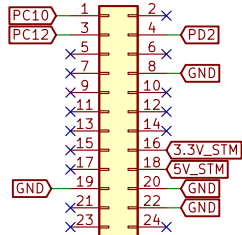
STM32 CN10

J4
Conn_02x05_Top_Bottom



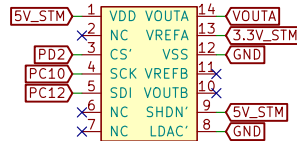
STM32 Output for PLL
IDC Socket

J6
Conn_02x12_Odd_Even

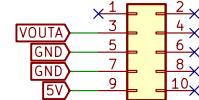


DAC Control SPI
STM32 CN7

U1A
MCP4922_UD



J2
Conn_02x05_Odd_Even



DAC Output for Attenuator
IDC Socket

Efe Kiraz
Dokuz Eylul University Electrical and Electronics Engineering Faculty

Sheet: /
File: stm32_cape.sch

Title: STM32 Cape

Size: User Date: 2021-09-04

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Rev: V1.1

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