Baby-MIND

FRONT END ELECTRONICS

Doc. no.:

Issue : v.2.1

Date : 2016/06/08

Cat

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Spill header 31 30 29 28 27 26 25 24 23 22 21 Spill header ID Board ID	20 19 18 17 16 15 14 13 12 0 SId Spill tag	11 10 9 8 7 6 5 4 3 2	1 0
GTRIG header #1 31 30 29 28 27 26 25 24 23 22 21 GTRIG header ID Global Trigger tag	20 19 18 17 16 15 14 13 12	11 10 9 8 7 6 5 4 3 2	1 0
Event Data: Hit #1 31 30 29 28 27 26 25 24 23 22 21 Hit time ID Channel ID	20 19 18 17 16 15 14 13 12 Hit ID Tag ID EDGE	11 10 9 8 7 6 5 4 3 2 Hit time (2.5ns res.)	1 0
31 30 29 28 27 26 25 24 23 22 21 Hit Amplitude ID Channel ID Event Data: Hit #n	20 19 18 17 16 15 14 13 12 Hit ID Tag ID Amplitude ID	11 10 9 8 7 6 5 4 3 2 Amplitude measurement	1 0
31 30 29 28 27 26 25 24 23 22 21 Hit time ID Channel ID 31 30 29 28 27 26 25 24 23 22 21	20 19 18 17 16 15 14 13 12 Hit ID Tag ID EDGE	11 10 9 8 7 6 5 4 3 2 Hit time (2.5ns res.)	1 0
Hit amplitude ID Channel ID GTRIG trailer #1 31 30 29 28 27 26 25 24 23 22 21		Amplitude measurement	1 0
GTRIG trailer 1 ID Global Trigger tag GTRIG trailer 2 ID Hit counts within gtrig* Spill trailer	Global Trigger time (10us res. % curre	ent spill start)	
31 30 29 28 27 26 25 24 23 22 21 Spill trailer 1 ID Board ID Spill trailer 2 ID Spill time (10ms res.)	20 19 18 17 16 15 14 13 12 0 SId Spill tag 1 SId Temperature	11 10 9 8 7 6 5 4 3 2 Humidity	1 0

^{*:} unused yet

Table 4: data structure for the communication between FEB & VRB

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Amplitude ID:

	<u>HG</u>	<u>LG</u>	TEMP/HV
<u>COMPUTE</u>	0000	<u>0001</u>	<u>0111*</u>
COMPARE	0010	0011	<u>0110</u>
BASELINE	0100	0101	0111*
<u>OTHERS</u>	0111*	0111*	0111*

^{*:} forbidden value (error)

Tag ID: 2 LSB: These bits are used for Tagging synchronization in order to place Timing & Amplitude measurement within the corresponding GTRIG event i.e. between its real Header/Trailer. The 2 LSB bits of the Hit ID must correspond to the current 2 LSB bits of the Global Trigger Tag. Due to the different clock domains and the FIFOs cascade latency used for the readout, the hit or amplitude ID message may be sent on the next GTRIG header/trailer cell if the event occurs just before the GTRIG signal. Moreover the probability of having an amplitude event on the next GTRIG is higher since it takes ~9us to process the analog readout (ASIC LG/HG multiplexed outputs) compared to an hit event which is pushed in the readout flow within 10-20ns due to FIFO to FIFO shorter latencies (2.5ns clocking).

Hit ID: 3 bits: these bits are used to synchronize the Amplitude event with the corresponding Timing event. Indeed, the amplitude measurement takes approximatively ~9s while several timing events can be latched and sent during this lapse. As soon as a Time event occurs on a given channel, the analog stage latches the hit ID of this channel and this latch is enabled during the entire HOLD DELAY duration and the ADC reading is started at the end. In other words, if many channels are hit the time flow, the corresponding analog ones will be associated with the Hit ID bits field since both timing & analog flows tag the same Hit ID. A rollover counter of 8 events (3-bits) is used.

EDGE: used for hit timing identification of event: 0=rising edge of event, 1=falling edge of event. The event duration may be calculated with the difference of the falling and the rising edge time stamps.

Channel ID: the channel IDs are used for the HG/LG identification but also for the TEMP & HV identification. In this particular case, Channel number is equivalent to:

<u>CH</u>	<u>signal</u>
<u>0</u>	TEMP ASIC 2
<u>1</u>	<u>NC</u>
<u>2</u>	TEMP ASIC 1
<u>3</u>	TEMP ASIC 0
4	<u>HV</u>

SiD: Sub Board ID for a vertical/horizontal identification (by hardware dip switch)

R: Reserved bit (=0)