GOVT. COLLEGE OF ENGINEERING, AMRAVATI Department of Computer Science and Engineering

CLASS TEST-II(Winter 2015) B. Tech. Final Year

Course: Microprocessor and Interfacing Code: CSU702 Time: 1 hrs. Marks: 15

Q.1 Draw pin configuration of USART 8251 with methods of data communication?[5 Marks]

Q.2 Explain control word format for Synchronous and Asynchronous modes of operation of USART 8251.[5 m]

Q.3 (a) Explain internal architecture of programmable DMA interface 8237 with transfer modes of 8237. [5 m]

OR

Q.3 (b)Explain programmable keyboard/display controller 8279 with block diagram?[5 m]