

Software Safety Requirements and Architecture

Lane Assistance

**Document Version: [Version]**

**Template Version 1.0, Released on 2017-06-21**



# Document history

**[Instructions: Fill in the date, version and description fields. You can fill out the Editor field with your name if you want to do so. Keep track of your editing as if this were a real world project.**

**For example, if this were your first draft or first submission, you might say version 1.0. If this is a second submission attempt, then you'd add a second line with a new date and version 2.0]**

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| Date | Version | Editor | Description |
| 18/08/2019 | 1.0 | Patrick Klie | Initial Version |
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# Purpose

**[Instructions: Answer what is the purpose of this document?]**

The purpose of the Software Requirements and Architecture Document is to develop requirements and metrics against which the item can be verified, that will ensure its functional safety.

# Inputs to the Software Requirements and Architecture Document

**[Instructions:**

**REQUIRED:**

**You are only required to develop this document for the LDW (lane departure warning) amplitude malfunction. So here, provide the technical safety requirements for the LDW amplitude malfunction as well as the refined system architecture diagram from the technical safety concept.**

**OPTIONAL:**

**Expand this document to include software safety requirements for the LDW frequency malfunction as well. Go even further and document software safety requirements for the Lane Keeping Assistance (LKA) function as well.**

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## Technical safety requirements

Technical Safety Requirements related to Functional Safety Requirement 01-01 are:

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| **ID** | **Technical Safety Requirement** | **ASIL** | **Fault Tolerant Time Interval** | **Architecture Allocation** | **Safe State** |
| Technical  Safety  Requirement  01 | The LDW Safety | C | 50 ms | LDW Safety | LDW shall set the oscillating torque to 0. |
| Technical  Safety  Requirement  02 |  |  |  |  |  |
| Technical  Safety  Requirement  03 |  |  |  |  |  |
| Technical  Safety  Requirement  04 |  |  |  |  |  |
| Technical  Safety  Requirement  05 |  |  |  |  |  |

## Refined Architecture Diagram from the Technical Safety Concept

**[Instructions:**

**REQUIRED: Provide the refined system architecture diagram from the technical safety concept**

**]**

# Software Requirements

**Lane Departure Warning (LDW) Amplitude Malfunction Software Requirements:**

**[Instructions: Fill in the software safety requirements for the LDW amplitude malfunction technical safety requirements. We have provided the associated technical safety requirements. Hint: The software safety requirements were discussed in the text from the software and hardware lesson.**

**OPTIONAL:**

**CHALLENGE ONE**

**Develop software safety requirements for the Lane Departure Warning (LDW) frequency function and modify the system architecture as needed.**

**CHALLENGE TWO**

**Develop software safety requirements for the Lane Keeping Assistance (LKA) function and modify the system architecture as needed.**

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| **ID** | **Technical Safety Requirement** | **ASIL** | **Fault Tolerant Time Interval** | **Allocation to Architecture** | **Safe State** |
| Technical  Safety  Requirement  01 | The LDW safety component shall ensure that the amplitude of the LDW\_Torque\_Request sent to the Final Electronic Power Steering Torque component is below Max\_Torque\_Amplitude |  |  |  |  |

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| ID | Software Safety Requirement | ASIL | Allocation Software Elements | Safe State |
| Software  Safety  Requirement  01-01 |  |  |  |  |
| Software Safety Requirement 01-02 |  |  |  |  |
| Software Safety Requirement 01-03 |  |  |  |  |

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| **ID** | **Technical Safety Requirement** | **ASIL** | **Fault Tolerant Time Interval** | **Allocation to Architecture** | **Safe State** |
| Technical  Safety  Requirement  02 | The validity and integrity of the data transmission for LDW\_Torque\_Request signal shall be ensured |  |  |  |  |

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| **ID** | **Software Safety Requirement** | **ASIL** | **Allocation Software Elements** | **Safe State** |
| Software Safety Requirement 02-01 |  |  |  |  |
| Software Safety Requirement 02-02 |  |  |  |  |

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| **ID** | **Technical Safety Requirement** | **ASIL** | **Fault Tolerant Time Interval** | **Allocation to Architecture** | **Safe State** |
| Technical  Safety  Requirement  03 | As soon as a failure is detected by the LDW function, it shall deactivate the LDW feature and the LDW\_Torque\_Request shall be set to zero |  |  |  |  |

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| **ID** | **Software Safety Requirement** | **ASIL** | **Allocation Software Elements** | **Safe State** |
| Software Safety Requirement03-01 |  |  |  |  |
| Software Safety Requirement03-02 |  |  |  |  |
| Software Safety Requirement03-03 |  |  |  |  |
| Software Safety Requirement03-04 |  |  |  |  |
| Software Safety Requirement03-05 |  |  |  |  |

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| **ID** | **Technical Safety Requirement** | **ASIL** | **Fault Tolerant Time Interval** | **Allocation to Architecture** | **Safe State** |
| Technical  Safety  Requirement  04 | As soon as the LDW function deactivates the LDW feature, the LDW Safety software block shall send a signal to the car display ECU to turn on a warning light |  |  |  |  |

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| **ID** | **Software Safety Requirement** | **ASIL** | **Allocation Software Elements** | **Safe State** |
| Software Safety Requirement 04-01 |  |  |  |  |

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| **ID** | **Technical Safety Requirement** | **ASIL** | **Fault Tolerant Time Interval** | **Allocation to Architecture** | **Safe State** |
| Technical  Safety  Requirement  05 | Memory test shall be conducted at start up of the EPS ECU to check for any faults in memory |  |  |  |  |

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| **ID** | **Software Safety Requirement** | **ASIL** | **Allocation Software Elements** | **Safe State** |
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| Software Safety Requirement 05-02 |  |  |  |  |
| Software Safety Requirement 05-03 |  |  |  |  |
| Software Safety Requirement 05-04 |  |  |  |  |

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# Refined Architecture Diagram

**[Instructions: Include the refined system architecture. Hint: The refined system architecture should include the system architecture from the end of the software and hardware lesson, including all of the ASIL labels.]**