Processes the basic instructions that drive a Computer.

Micro Processor: - Incorposates the functions of a Computer's Central Processes Onet (CPU) on single IC (81) at most a few Il's.

The Micsopsocensor is a Multipuspose, programmable device that accepts dightal data as 9/p processes it according to instructions that accepts dightal data as 9/p processes it according to instructions should in its memory and provides subolts as olp. (&: Sequential logic should in its memory and provides subolts as olp. (&: Sequential logic design).

History: - The First 4-1-1 missofrocensor 4004 from Intel Conformation in 1971. Which has been a silent revolution in the domain of degetal system design.

(10,000) transistors where Component density increased more than those fold in less than a decade's time.

(x) -> After 4004 MP (4-1817), Immeadiatly Intel Conforation has antooduced first (8-191) up 8008 an 1972 (which one not successful).

(x) -> In 1974 Intel Indeased the first general Purpose 8-1514 micsopsoceus 81 8080 in which "CPU" was not functionally Complete.

(x) -> later on the first 8-Bit functionally Complete "CPU" 8085 MP was Introduced in 1977.

The 8085 CPU is still the most Popular one amongst all 8-1917 CPU'S Which Provides good Performance utilizing an optimum Set of Progristers and an searonably Powerful ALV, where 8085 houses on - chip clock generater.

(x) -> The Major 19mstations of 8-18t up's other their (2) 19mited memory addressing safacity, slow speed of execution, limited scootchpad segisters and non-availability complex instruction set, and oddwersing model.

(x) -> The first 16-184 Mp's from Intelacous 8086 in 1978 results mose powerful systems Computing machine. In 8086 Contains 16-184 general perpose Jugisters, 16-181 ALU'S , a 81ch instruction set and Provides a

reducented mountaged gos apprendid schame.

(x) The Perepheral chips designed sarlier for 8085 were compatiable with misopso-cesses sosse with slight (si) no modifications. Thorough there is considerable techniques difference the addressing memby. :11. 0808 pro 2808 n;

* Register: - Is a type of storage device which are used to Store large amount of digital data. Memories are mode up of stagistions. Each stagistion on memory is one storage location. Each boation is identified by an address. Each location is accompliate one (21) mose 1978. The Capacity is specified in teams of bytes (815:45 = 1 byte). Each glest Stores one 18th of dota. A storage Element is alled all. The data stood on momony by Poacess collect working and Ose retained from memory by Bosens is alled "Reading".

Refer to DICA MOST pools.

1) Misso Bosewas: - The MP is a Bogfammable Ic device that has

Computing and decision making apability.

The is a Program Controlled device, which fetcher, deads, and executes sustanctions.

-> The MP reads instructions from memory Communicates with all speciphosal (memory of I/o ph) using the system bus.

-> The MP Controls the timing of information flow Postlems the Computing tosks specified in a program.

(2) Engrison of wholestons:-

(*) The first UP. Intel 4004 (4-1817) was introduced in 1971.

(x) Intel developed an improved 4-154 4P 4040, Enchanced version of Intellegent Many other Companies also Introduced 4-1817 Mp such as P15-4 by Rock well international, 73472 by Toshiba Etc: ")

(x) In 1972 Intel first (8 481 MP) Intel 8008 MP developed so Jas used Paros technology these technology of Paros used is 8 bio, and not Compatible with TIL, ciocate.

(*) In 1973 Intel antoduced more Powerful 8-1614 Mp Intel 8080. used n-mos technology, and was faster, and Compatiable with TIL ciscosts. The Nmos Process also glass higher density than

-> The drawbacks of 8080 was that 9th sequisted 3-Power Suplin

(*) In 1975 Intel Introduced 8-134 MP 8085, which sugrissed only one 451 fower supply.

(8) 8-184 pps are Motosola Mc6800, Mc6809; 78-69/s 7800;

(x) In 1978 Intel antoabled first 16-124 Mp 8086. Some other 16-18H MP one Intel 30186, Intel 8088, Intel 80186, Intel 80188, Intel 80886, Motosolas MC6800, 68010, 7 68012; Fairchild 9440, £log's. £800; National sentional sentional sentionals PACE of INS8900. These used HMOS technology (High density Mos). The HMOS technology offers better speed power Bodict (Spp) and Higher Packing density than N-Mos.

Speed Power Product (Spp) = Speed X jower - nous selonds x mallematt

= PiGo Zoules.

-> Spp of 14Mos is 4 times better than 10mos.

-> Ciscust density Bosided by HMOS. (4198 gates/um²) are approximately turice those of N-mos (1852-5 gates [mm²).

-) Intel 8088 was very popular and wholly used in cheaper Pelisonal Computed.

-) Intel 80886 was also very Popular and was used in Costlies and more powerful personal computers.

-> Intel 80186 and 80188 were not Popular for grenolal Perpose Computeds but very used for industrial Control.

The 6800 was very popular of was used an Minic Computeds.

-) In 1985' Intel introduced more Powerful 32 bit up Intel 80386.

became very Popular and wridely used the desktop Computers.

-> Same other 32 BH UP are Intel 80486, Pertium, Rentium Bo, Pertium II, Pertium IV, Advand mico desircis (AMD's) K5, K6, K7, National remi conductors 32032, 32332, and 32 C532. Intel 486 was very BPold and widely wed John desktop Computed.

-> Motosolais 68020, 68030, and 680210 are why Popdal and were way the best min Compatels.

-> Now Pentium III and advanted working of up are sted in desistants and sequels. These are Jahos Goted using the low Power version of HMOS technology called HCMOS.

-> Recently 64 bit micro Bolerious tour also been dourbled some Examples are Sun's USRA SPARC, Power PC 620, MSPS 7400, R500, R10000 and 1200.11.

8-bit Missorprocemosis

- 1) Sample Asolatectuse
- a) Slow speed of execution
- 3) Low Memory addressing Capability
- 29) Less Powerful Prostauction set
- 5) fobsitated using P-MOS & NMOS Technology

16-194 M9080 Procemors

- 1.) Advanced Aachstectoose
- 3) Mosse Boconsod Codopying
- 3) Losaper memory addressing Grability
- 4) Mose Powerful Prostoction Set
- 5) Fabriated using Amos technology.

	6.) Applications										A) Applications:					
Data acquirstation system, Numerical Control,									Control Rystems, Traffer Control Rystems, Traffer Control Rystems, Traffer Controls of Instru					oothoo)	ntegg,	
Automatic testing system.)						
T) Limited not of General Perpose T) Mosse not of GPR's.																
		+ ecy (i	TIENCY													
Adressable. Memoray.	640 भिर्मेख	16 kbyte	64 Kbyte	64 K byte	IMB	1 mB	16 maga	2 (Algo	Je Graga	L. Gega	64 Ciga	62e Giga	Gle aga	67 (aga		
जिसकारिक १९५५ ति	+	o O	6 0	Φ	9)	∞ 0	9	39	2	33 (8) 64	S2 (81) 64	3	9	79		
Act 2008, 500 1849	9	3	<u>و</u>	9	30	90	700	33	33	33	39	36	36	£		
Jotial clock	168	900	E B	S MHZ	E C	S mHz	S MH Z	(6 M/H/2	25 MHz	SO MIN SO	190 milis	930 MH3	MW CSO MAN	1.4 GH3		
1865, 8000 to 1000	2300	35500	0009	9	39000	39000	4,34,000	2,75,000	ing mega	3.1 maga	3.5 mega	g. g. mega	Joseph S. P	1.3 Maga.		
Years of the Introduction	1461	1972	(97th	9461	1978	(a H	1687	. 1986		(993	506	464	36.	9000 ·		
Historical Pacessor	7007	3008	000 0000 0000 0000 0000 0000 0000 0000 0000	2008 Sabes	980g	2088	20,386	98800	98702	Pentrum	Fertium 20	Partium 2	Perthin 3	Perthum 2		