

electronic circuit design laboratory tests (third edition)



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Reviews

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(Jody Veum)

ELECTRONIC CIRCUIT DESIGN LABORATORY TESTS (THIRD EDITION)







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paperback. Condition: New. Ship out in 2 business day, And Fast shipping, Free Tracking number will be provided after the shipment. Paperback. Pages Number: 421 Publisher: Huazhong Pub. Date :2008-03-01. This book is the general higher education 15 national planning materials. is based on the second revised edition of the complete. The second edition of 95 four-level key materials. and in 2002 won the national second prize of outstanding teaching universities and colleges. The book is 11 chapters. 1 to 4 for the basic experiment. Chapter 1 introduces the basic theory of measurement error and experimental data processing of the basic method; Chapter 2 describes the crystal diodes. transistors. MOS field effect transistor and the integrated operational amplifier device performance parameters of the test methods. and basic application circuit; first Chapter 3 describes the integration of logic gates. integrated flip-flop. IC 555 and the combination of small and medium-scale logic circuits and sequential logic circuits and other performance parameters. the basic logic test and application circuit design methods. Chapter 4 describes the OrCAD9.2 version of the operating methods of teaching software and 12 low-frequency circuits. digital logic circuits. high-frequency circuits. mixed analog-digital circuit simulation examples. 5 to 7 were low-frequency circuits. high-frequency circuits. digital logic circuit design of experiments. in which low-frequency. high frequency circuit design issues 15. digital circuit design issues 7. These design issues. we introduce a quantitative estimate and test electronic circuits based on the traditional design methods and testing technology designed to enhance students basic training in engineering design. Chapter 8 Hardware Description Language. describes the basic syntax rules VerilogHDL. the basic structure of the program. and the gate level. behavioral modeling approach. gives 12 examples of program design. Chapter 9 describes two programmable logic devices. namely. the basic structure of CPLD and FPGA and programming methods....

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