



The schematic diagram shows the internal connections of the AMS1117-3.3 voltage regulator. The input pin (VIN) is connected to a 12V source through a 100 Ohms resistor at 100 MHz. A 3V3 feedback network (FB1) is connected to the feedback pin (VOUT). The output pin (VOUT) is connected to a 3V3 output. The regulator is labeled U4, AMS1117-3.3. Capacitors C19 (1uF), C20 (22uF), and C21 (22uF) are connected to the input, feedback, and output pins, respectively. The ground pin is connected to ground.

[illegible]

Diagram of the ACJS-IH component showing pin connections. Pin 1 is labeled 'S', pin 2 is 'R', and pin 3 is 'T'. Pin 1 is connected to 'R\_HEADPHONE\_OUT', pin 2 to 'L\_HEADPHONE\_OUT', and pin 3 to ground. Pins 4, 5, and 6 are marked with red X's and are not connected.

Title <b>BeatNTwae-Phemius-I Input/Output Selector</b>			
Size <b>A3</b>	Number <b>1</b>		Revision <b>1</b>
Date <b>2/27/2024</b>	Sheet of <b>1</b>		Drawn By <b>Eda Sooglou P.</b>
File: <b>C:\Users\...BeatNTwae-Phemius-InputOutputSelector</b>			