
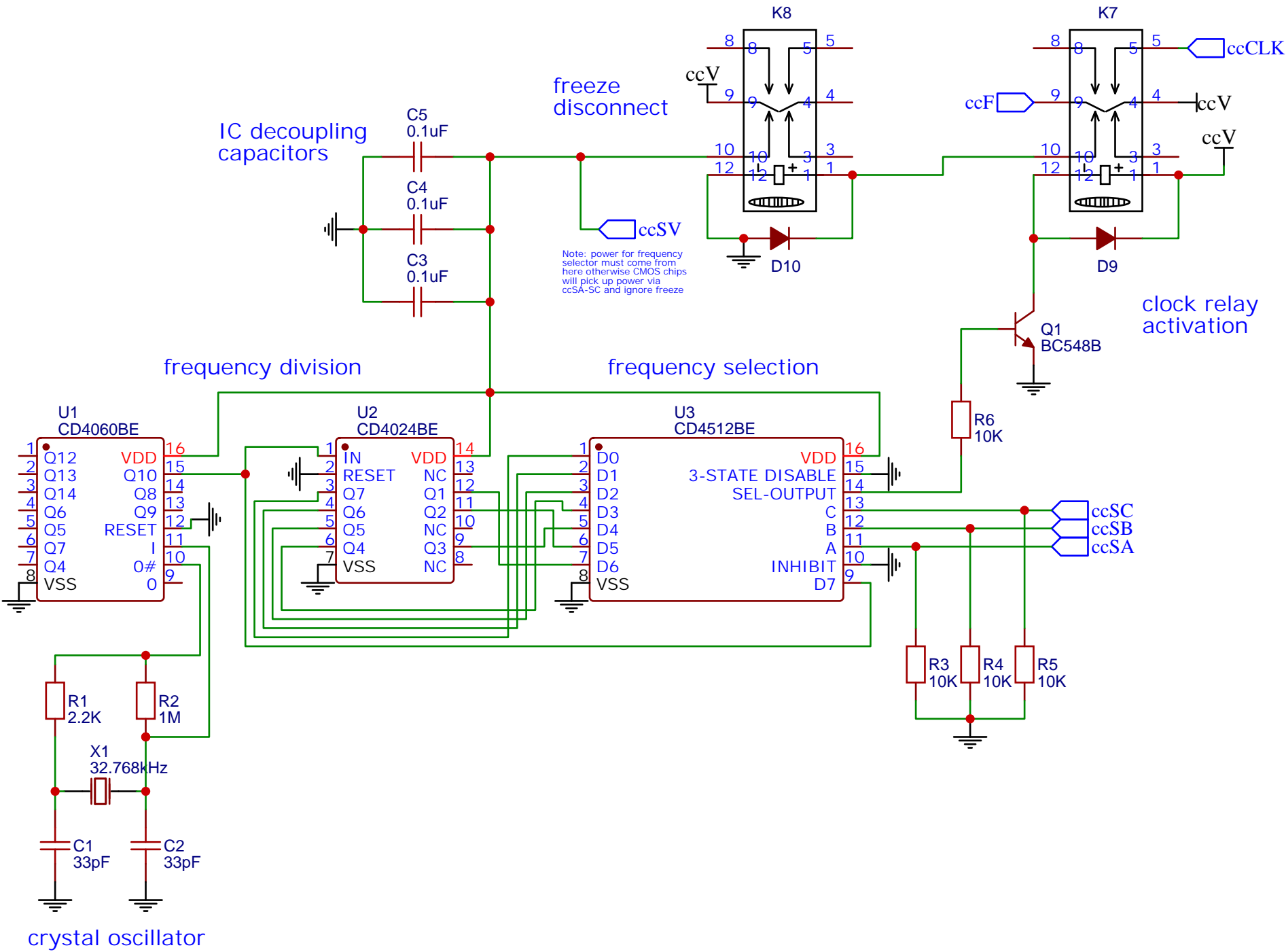


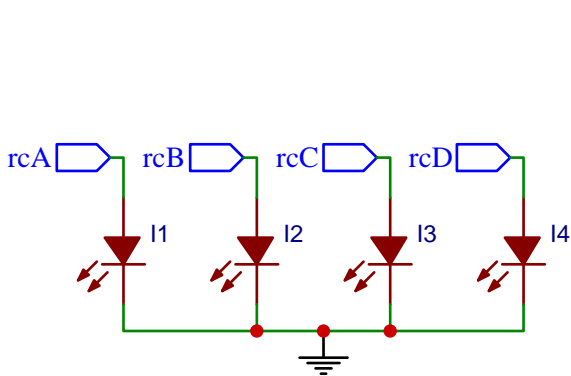
rcA-D, rcCLK and rcF connect to UI (sheet 3)  
rcV is fed from UI (sheet 3)

TITLE: Relay Computer : Clock Card		REV: 1.0
	Company: Paul Law	Sheet: 1/4
	Date: 2022-10-15 Drawn By: Paul Law	

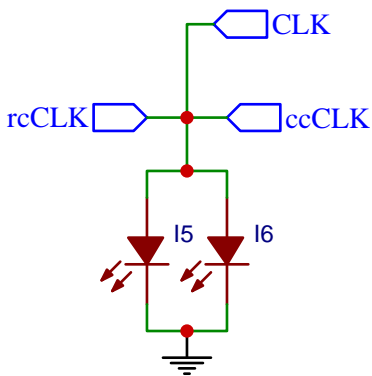


ccSA-SC, ccSV, ccCLK and ccF connect to UI (sheet 3)  
ccV is fed from UI (sheet 3)

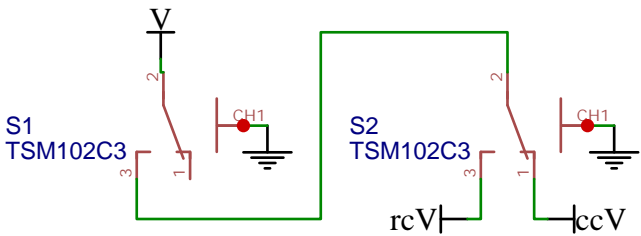
TITLE: Relay Computer : Clock Card		REV: 1.0
	Company: Paul Law	Sheet: 2/4
	Date: 2022-10-15 Drawn By: Paul Law	



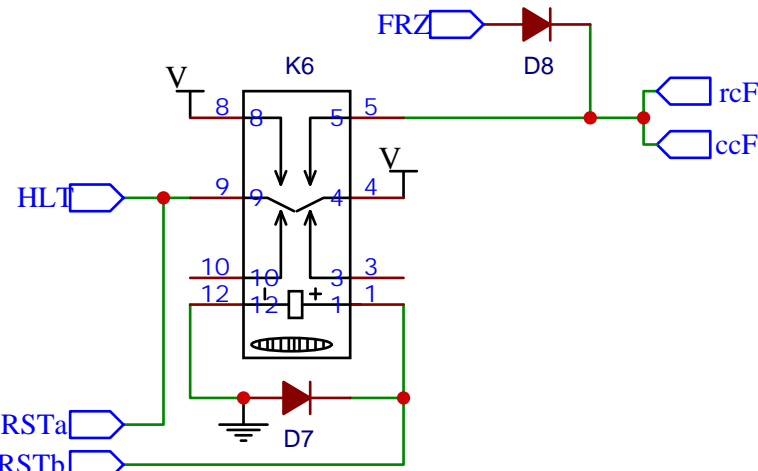
relay clock stages



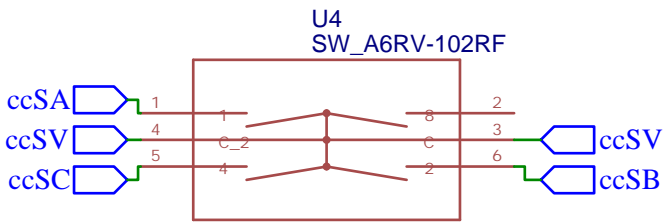
clock output



clock power & relay/crystal switch



halt latch



frequency selection  
(crystal clock)

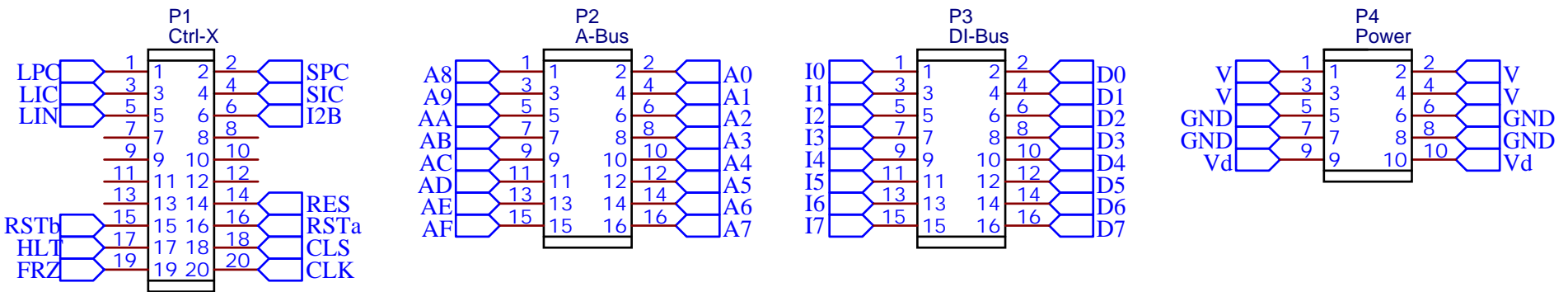
Important Note: All LEDs shown are 12V type (with integral resistor)

HLT, FRZ and CLK connect to backplane (sheet 4)

rcA-D, rcCLK and rcF connect to Relay Clock (sheet 1)

ccSA-SC, ccSV, ccCLK and ccF connect to Crystal Clock (sheet 2)

rcV and ccV feeds to Relay and Crystal clock respectively (sheet 1 and 2)



HLT, FRZ and CLK connect to UI (sheet 3)