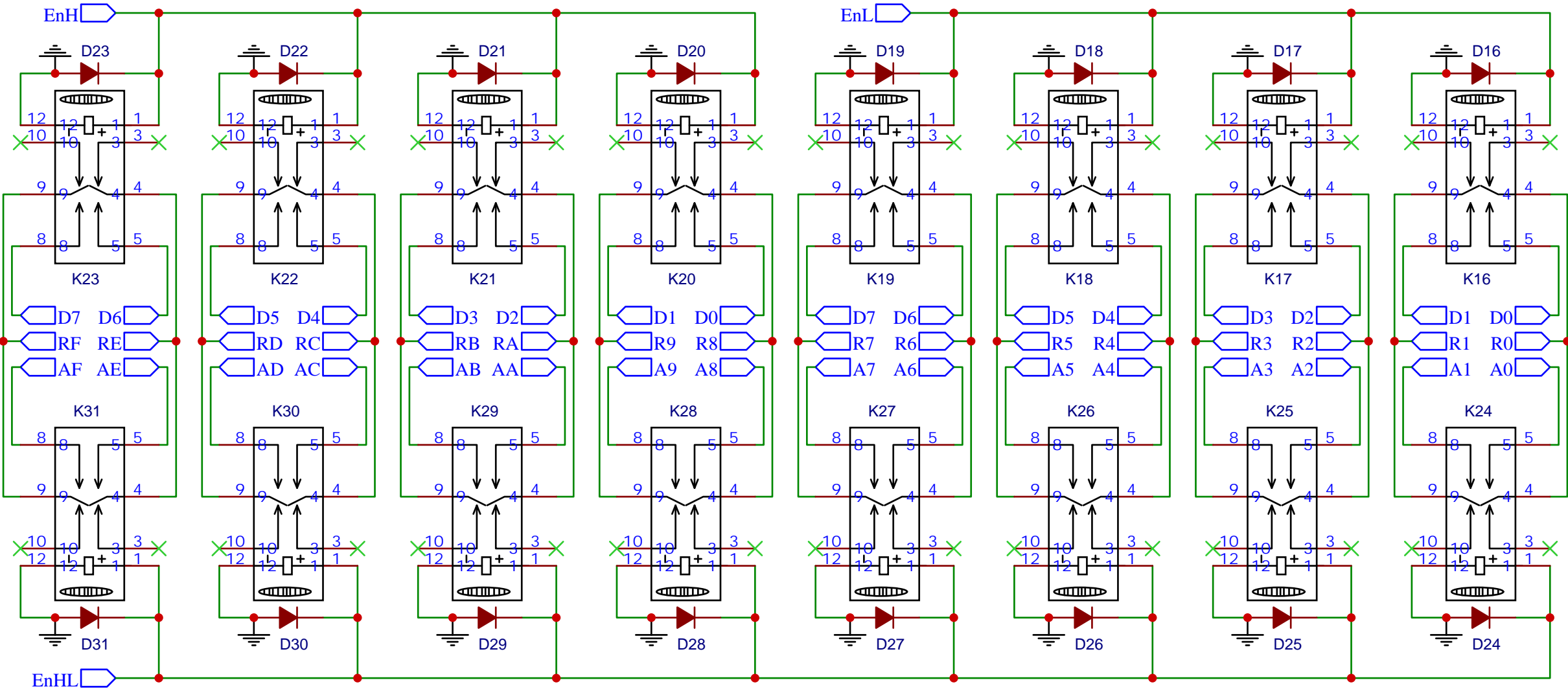Sheet 1: 1-bit register stores (8-bit X + 8-bit Y)

Important Note: All LEDs shown are 12V type (with integral resistor)

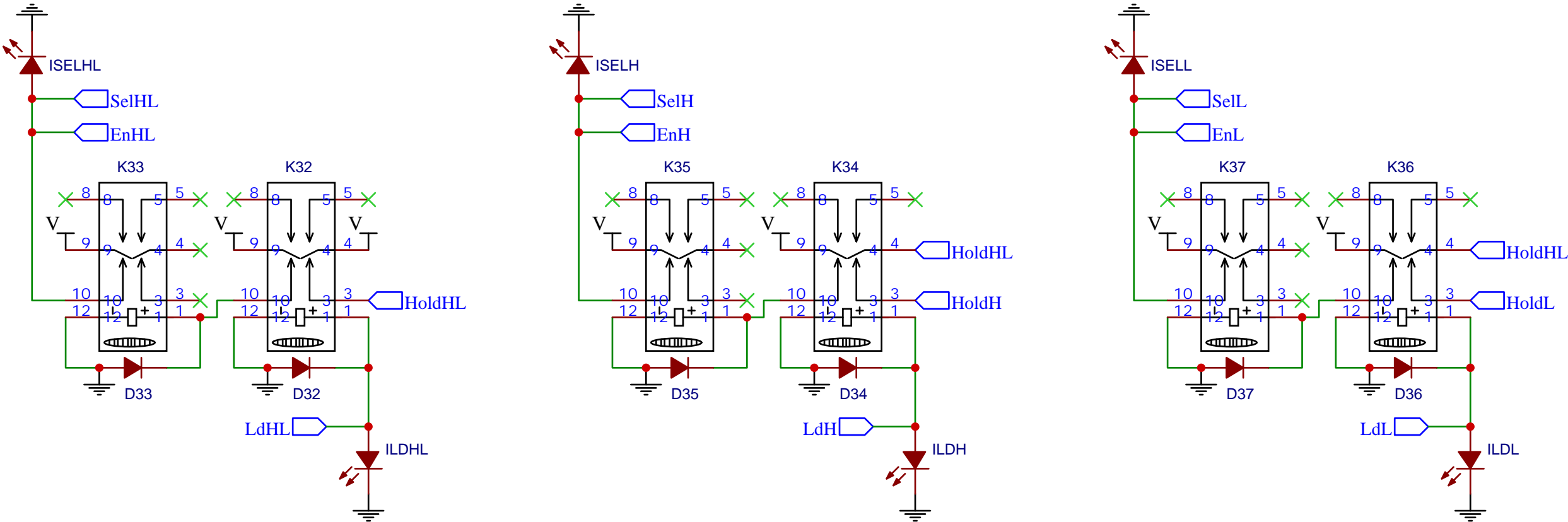HoldH and HoldL signals connect to control relays (sheet 3)

R0-RF signals connect to gating relays (sheet 2)

TITLE:
Relay Computer : Y Backplane Registers

REV: 1.1

Company: Paul Law

Sheet: 1/4

Date: 2019-02-10   Drawn By: paul_6392

Sheet 2: Data/Address bus gating

EnX, ExY and EnXY signals connect to control relays (sheet 3)
A0-AF signals connect to address bus (sheet 4)
D0-D7 signals connect to data bus (sheet 4)
R0-RF signals connect to register relays (sheet 1)

TITLE:
Relay Computer : Y Backplane Registers

REV: 1.1

Company: Paul Law

Sheet: 2/4

Date: 2019-02-10   Drawn By: paul_6392

RC Relay Computer

Sheet 3: Gating control

ISELHL

SelHL

EnHL

K33   K32

8   5   8   5

9   9   4   4   9   9   4

10   10   3   3   10   10   3

12   12   1   12   12   1

HoldHL

D33   D32

LdHL

ILDHL

ISELH

SelH

EnH

K35   K34

8   5   8   5

9   9   4   9   9   4   HoldHL

10   10   3   10   10   3

12   12   1   12   12   1   HoldH

D35   D34

LdH

ILDH

ISELL

SelL

EnL

K37   K36

8   5   8   5

9   9   4   9   9   4   HoldHL

10   10   3   10   10   3

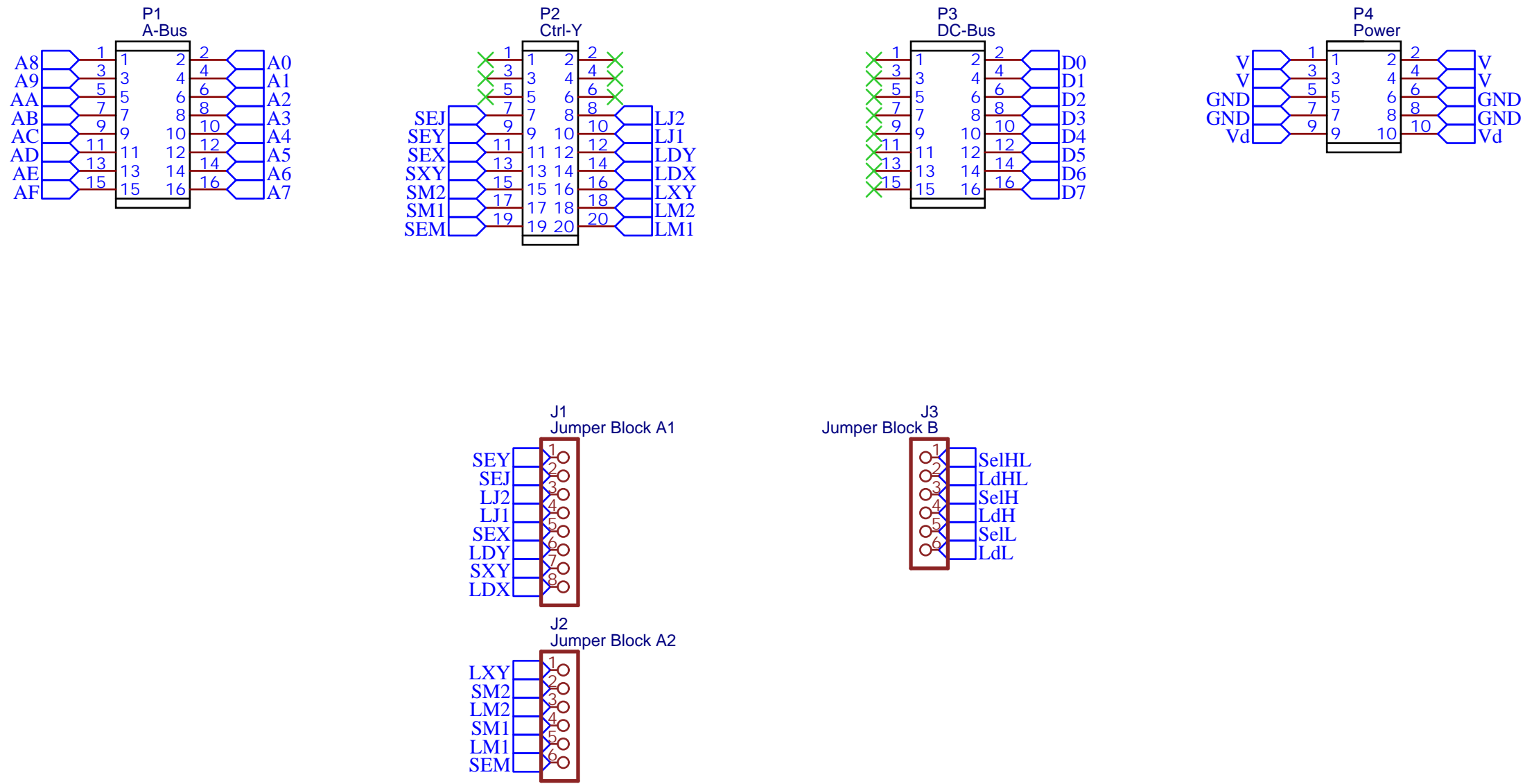12   12   1   12   12   1   HoldL

D37   D36

LdL

ILDL

Important Note: All LEDs shown are 12V type (with integral resistor)

LdH, LdL and LdHL signals connect to control Y bus (sheet 4)

SelH, SelL and SelHL signals connect to control Y bus (sheet 4)

EnH, ExL and EnHL signals connect to gating relays (sheet 2)

HoldHL connects only on this sheet

HoldH and HoldL signals connect to register relays (sheet 1)

P1
A-Bus

A8	1	2	A0
A9	3	4	A1
AA	5	6	A2
AB	7	8	A3
AC	9	10	A4
AD	11	12	A5
AE	13	14	A6
AF	15	16	A7

P2
Ctrl-Y

	1	2	
	3	4	
	5	6	
	7	8	
SEJ	9	10	LJ2
SEY	11	12	LJ1
SEX	13	14	LDY
SXY	15	16	LDX
SM2	17	18	LXY
SM1	19	20	LM2
SEM			LM1

P3
DC-Bus

	1	2	D0
	3	4	D1
	5	6	D2
	7	8	D3
	9	10	D4
	11	12	D5
	13	14	D6
	15	16	D7

P4
Power

V	1	2	V
V	3	4	V
GND	5	6	GND
GND	7	8	GND
Vd	9	10	Vd

J1
Jumper Block A1

SEY	1
SEJ	2
LJ2	3
LJ1	4
SEX	5
LDY	6
SXY	7
LDX	8

J3
Jumper Block B

1	SelHL
2	LdHL
3	SelH
4	LdH
5	SelL
6	LdL

J2
Jumper Block A2

LXY	1
SM2	2
LM2	3
SM1	4
LM1	5
SEM	6

Jumper block A and B is used to map control signals from backplane Ctrl-Y bus

A0-AF signals connect to gating relays (sheet 2)

D0-D7 signals connect to gating relays (sheet 2)

LdH, LdL and LdHL signals connect to control relays (sheet 3)

SelH, SelL and SelHL signals connect to control relays (sheet 3)

TITLE:
Relay Computer : Y Backplane Registers

REV: 1.1

RC Relay Computer

Company: Paul Law

Sheet: 4/4

Date: 2019-02-10   Drawn By: paul_6392