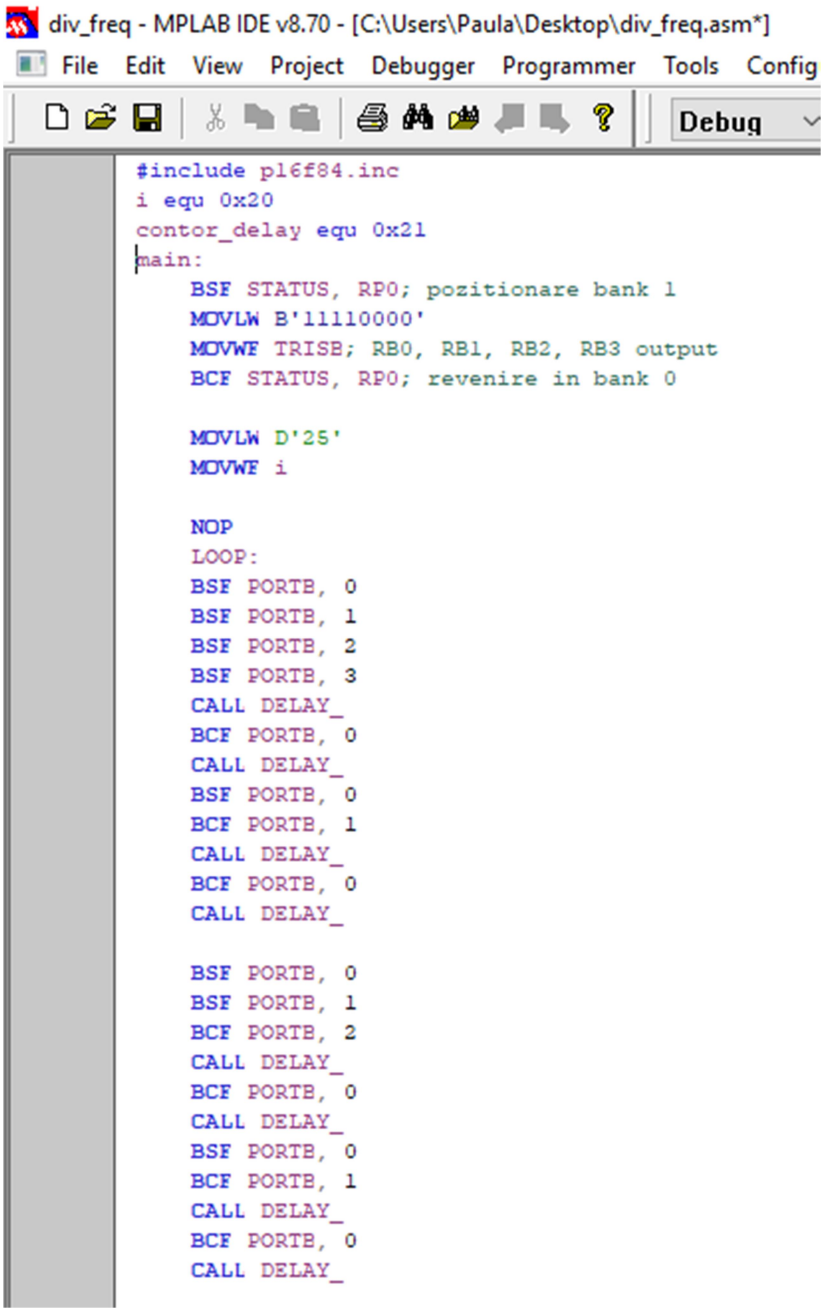
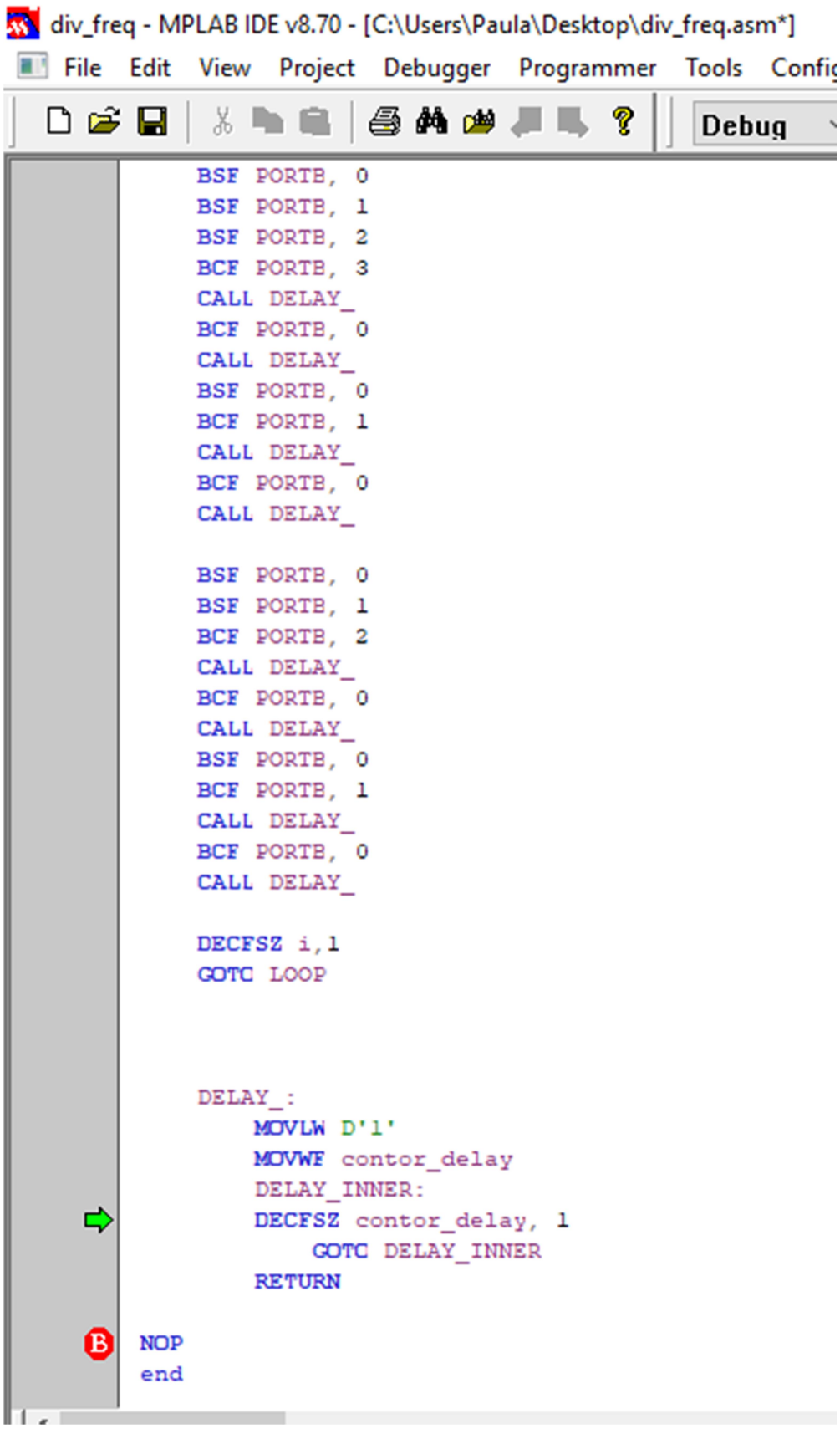
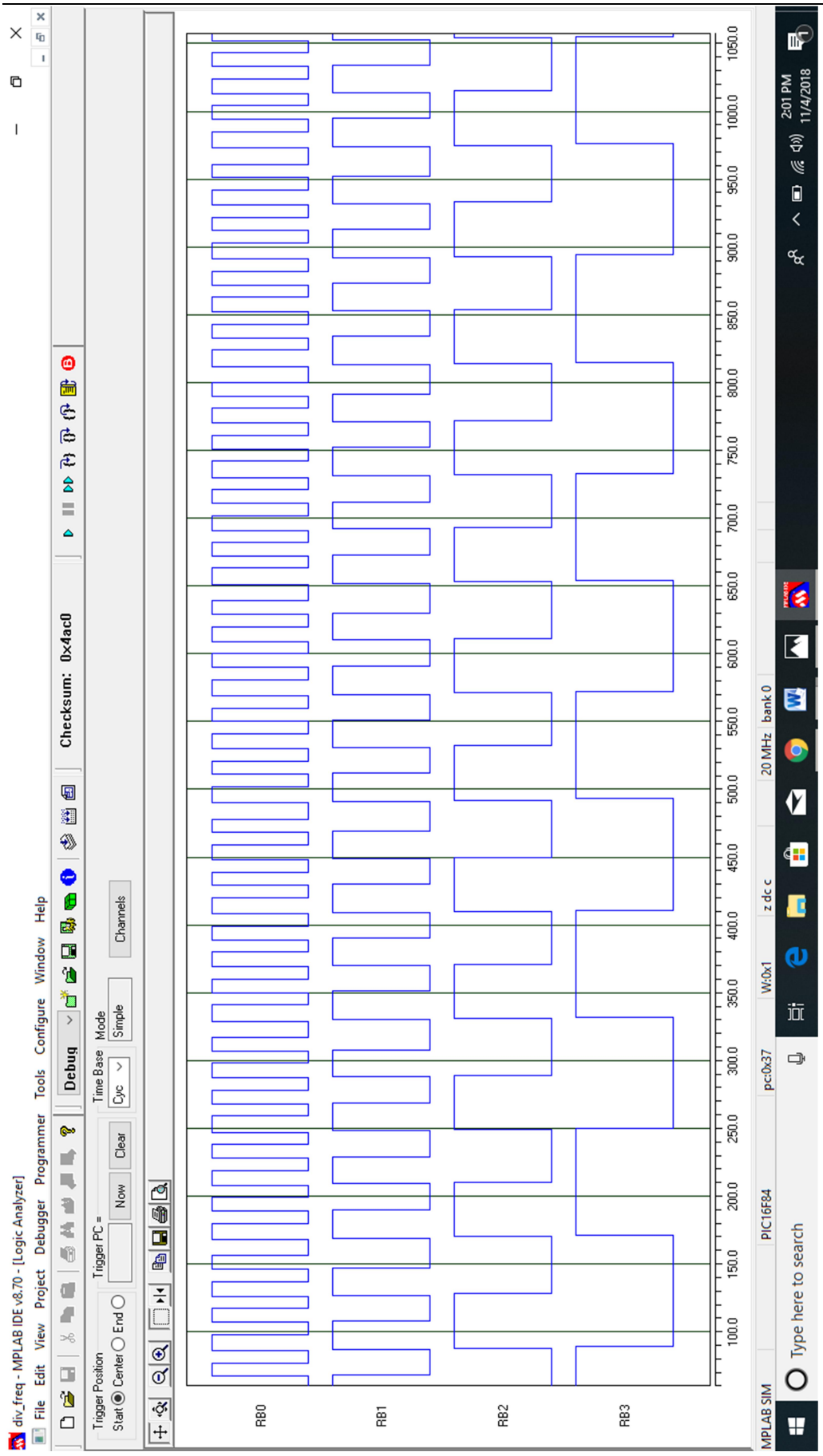
Sa se genereze un divizor de frecventa




```
#include p16f84.inc
i equ 0x20
contor_delay equ 0x21
main:
    BSF STATUS, RP0; pozitionare bank 1
    MOVLW B'11110000'
    MOVWF TRISB; RB0, RB1, RB2, RB3 output
    BCF STATUS, RP0; revenire in bank 0

    MOVLW D'25'
    MOVWF i

    NOP
    LOOP:
    BSF PORTB, 0
    BSF PORTB, 1
    BSF PORTB, 2
    BSF PORTB, 3
    CALL DELAY_
    BCF PORTB, 0
    CALL DELAY_
    BSF PORTB, 0
    BCF PORTB, 1
    CALL DELAY_
    BCF PORTB, 0
    CALL DELAY_

    BSF PORTB, 0
    BSF PORTB, 1
    BCF PORTB, 2
    CALL DELAY_
    BCF PORTB, 0
    CALL DELAY_
    BSF PORTB, 0
    BCF PORTB, 1
    CALL DELAY_
    BCF PORTB, 0
    CALL DELAY_
```




```
        BSF PORTB, 0
        BSF PORTB, 1
        BSF PORTB, 2
        BCF PORTB, 3
        CALL DELAY_
        BCF PORTB, 0
        CALL DELAY_
        BSF PORTB, 0
        BCF PORTB, 1
        CALL DELAY_
        BCF PORTB, 0
        CALL DELAY_

        BSF PORTB, 0
        BSF PORTB, 1
        BCF PORTB, 2
        CALL DELAY_
        BCF PORTB, 0
        CALL DELAY_
        BSF PORTB, 0
        BCF PORTB, 1
        CALL DELAY_
        BCF PORTB, 0
        CALL DELAY_

        DECFSZ i,1
        GOTO LOOP



        DELAY_:
            MOVLW D'1'
            MOVWF contor_delay
            DELAY_INNER:
            DECFSZ contor_delay, 1
                GOTO DELAY_INNER
            RETURN

    NOP
    end
```


div_freq - MPLAB IDE v8.70 - [Logic Analyzer]
File Edit View Project Debugger Programmer Tools Configure Window Help
Debug
Checksum: 0x4ac0
Trigger Position
Start Center End
Trigger PC =
Now
Clear
Time Base
Cyc
Mode
Simple
Channels
RB0
RB1
RB2
RB3
100.0 150.0 200.0 250.0 300.0 350.0 400.0 450.0 500.0 550.0 600.0 650.0 700.0 750.0 800.0 850.0 900.0 950.0 1000.0 1050.0
MPLAB SIM
PIC16F84
pc:0x37
W:0x1
20 MHz
bank 0
Type here to search
2:01 PM
11/4/2018

Alta metoda:

C:\Users\Paula\Desktop\t3mm.asm

```
#include p16f84.inc
main:
    BSF STATUS, RP0;
    MOVLW D'0'
    MOVWF TRISB; PB output
    BCF STATUS, RP0;

    MOVLW D'128'
    MOVWF PORTB
    LOOP:
        DECFSZ PORTB
        GOTO LOOP
NOP
end
```