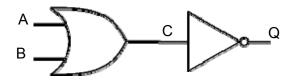
A Logic circuit has two inputs being X and Y fill in all the possible combinations.

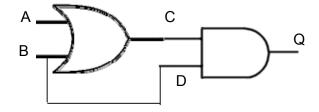
Х	Υ

2. The figure below shows a logic circuit and its incomplete truth table. Complete the below truth table.



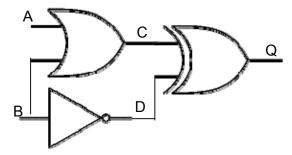
Α	В	С	Q
0	0		
0	1		
1	0		
1	1		

3. The figure below shows a logic circuit and its incomplete truth table. Complete its truth table.



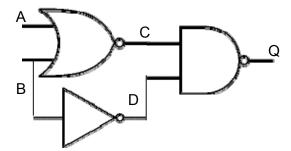
Α	В	С	D	Q
0	0			
0	1			
1	0			
1	1			

4. The figure below shows a logic circuit and its incomplete truth table. Complete the below truth table.



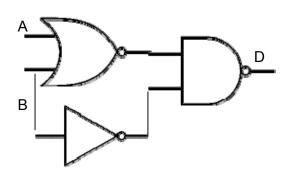
Α	В	С	D	Q
0				
0				
1				
1				

5. The figure below shows a logic circuit and its incomplete truth table. Complete the below truth table.



Α	В	C	D	Q
	0			
	1			
	0			
	1			

6. For the logic circuit below complete the truth table.



Α	В		D
0	0		
0	1		
1	0		
1	1		

## Answers:

1.

Х	Υ
1	0
1	1
0	0
0	1

2

Α	В	С	Q
0	0	0	1
0	1	1	0
1	0	1	0
1	1	1	0

3

Α	В	С	D	Q
0	0	0	0	0
0	1	1	1	1
1	0	1	0	0
1	1	1	1	1

4.

Α	В	С	D	Q
0	0	0	1	1
0	1	1	0	1
1	0	1	1	0
1	1	1	0	1

5

Α	В	С	D	Q
0	0	1	1	0
0	1	0	0	1
1	0	0	1	1
1	1	0	0	1

6.

Α	В	D
0	0	0
0	1	1
1	0	1
1	1	1