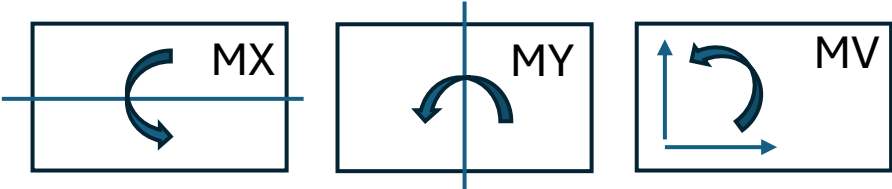
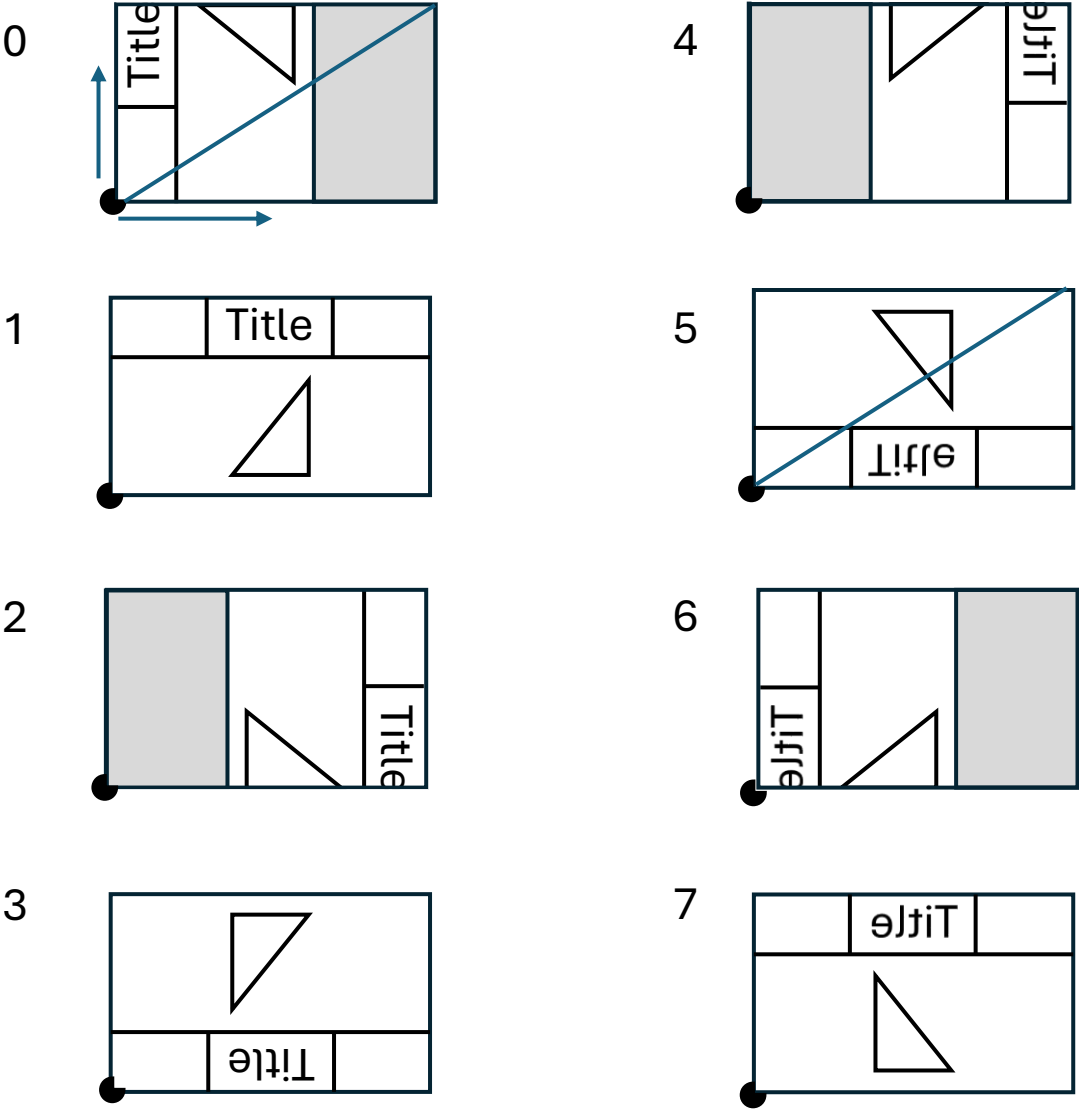


TFT Rotations – ILI9341 (Micro USB) – Lovyan GFX
Panel_ILI9341 - DFUNCTR 0x08,0xC2,0x27

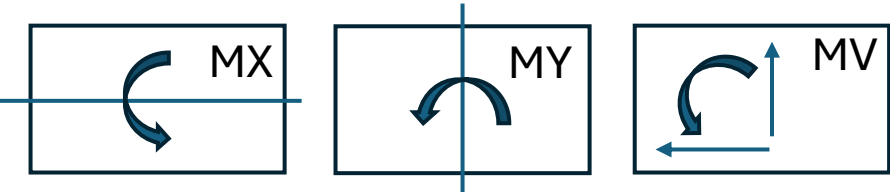
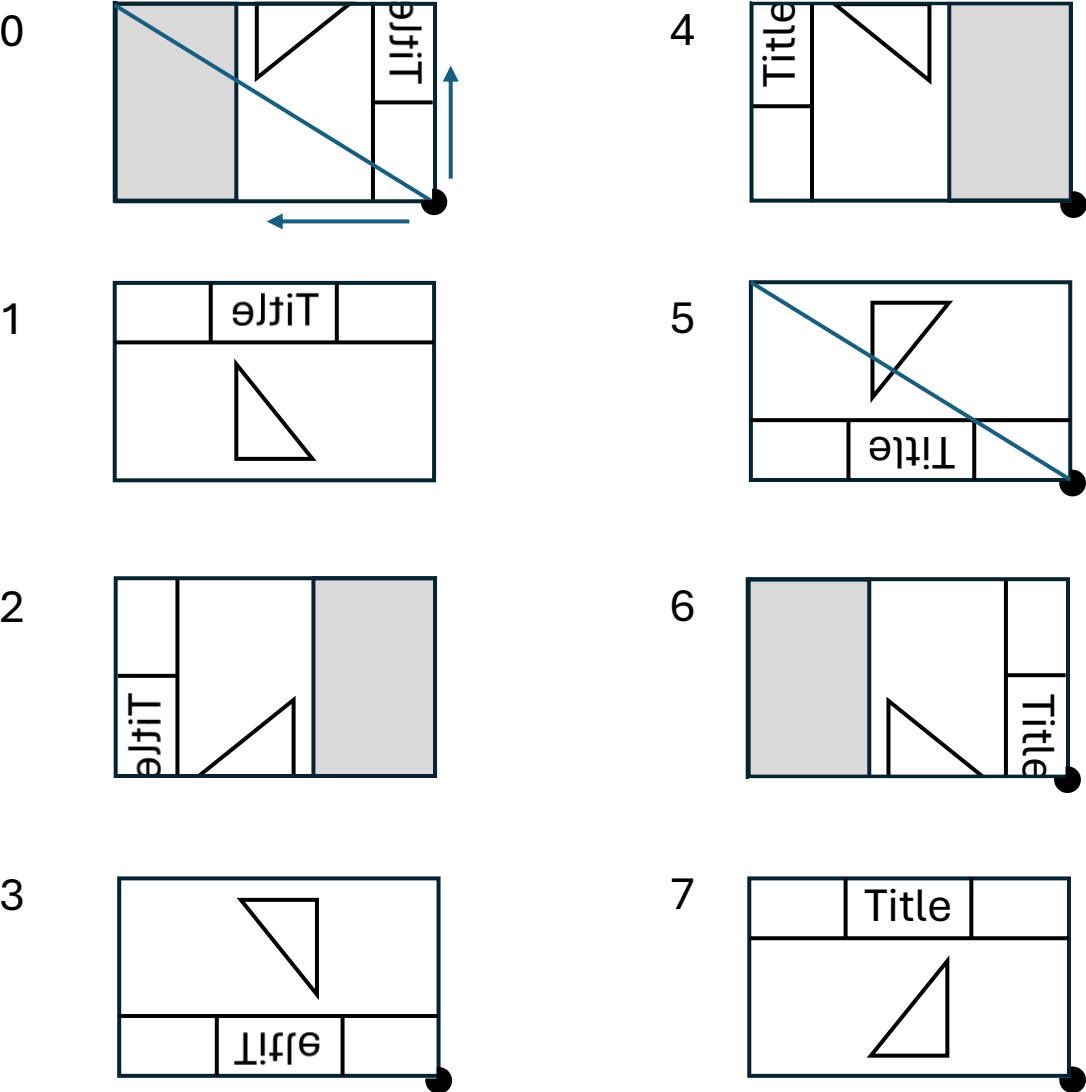
width 240
height 320



	MV	MX	MY
0			
1	MV	MX	
2		MX	MY
3	MV		MY
4			MY
5	MV		
6		MX	
7	MV	MX	MY

TFT Rotations – ILI9341 (Micro USB) – Lovyan GFX
Panel_ILI9341_2 - DFUNCTR 0x08,0x82,0x27

width 240
height 320

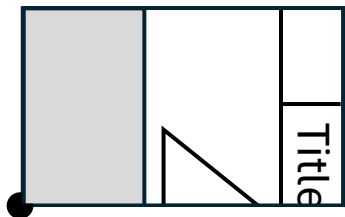


	MV	MX	MY
0			
1	MV	MX	
2		MX	MY
3	MV		MY
4			MY
5	MV		
6		MX	
7	MV	MX	MY

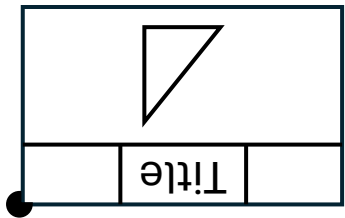
TFT Rotations – ILI9341 (Micro USB) – TFT_eSPI

width 240
height 320

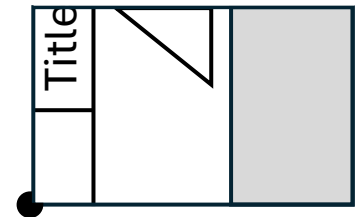
0



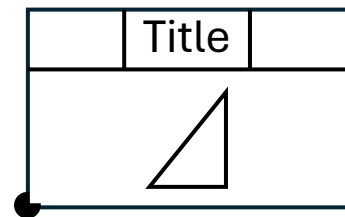
1



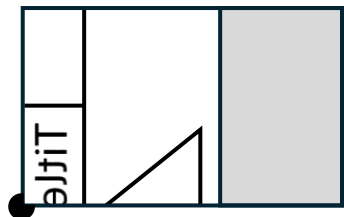
2



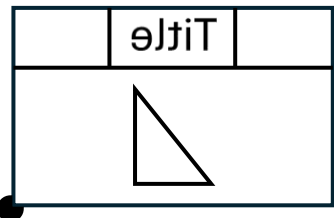
3



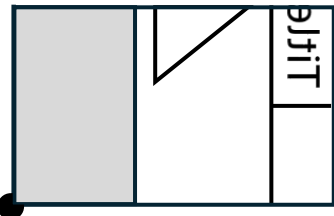
4



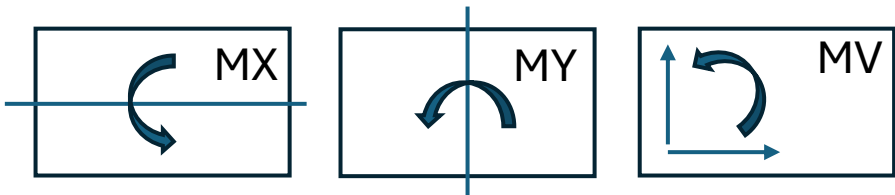
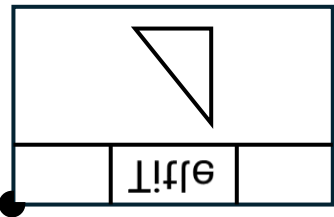
5



6



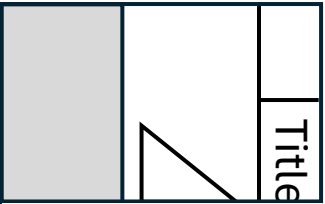
7



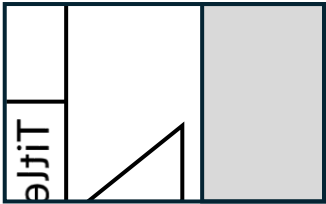
	MV	MX	MY
0		MX	
1	MV		
2			MY
3	MV	MX	MY
4		MX	MY
5	MV	MX	
6			
7	MV		MY

width 240
height 320

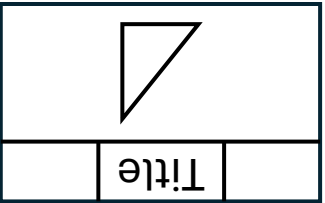
0



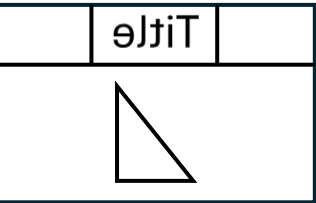
4



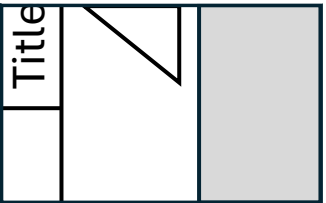
1



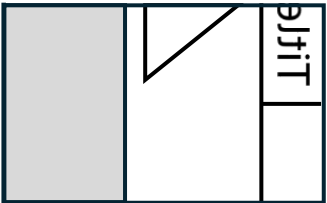
5



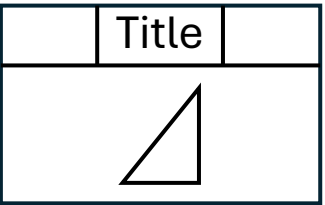
2



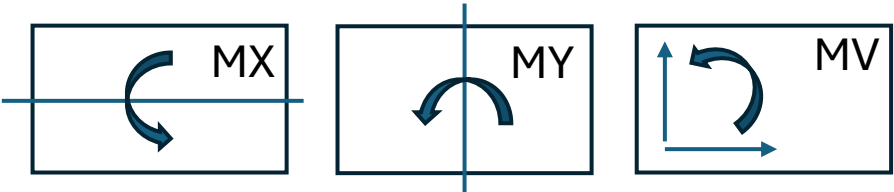
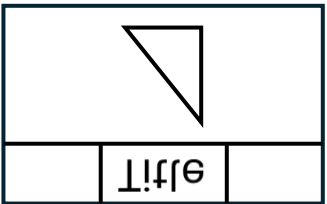
6



3



7

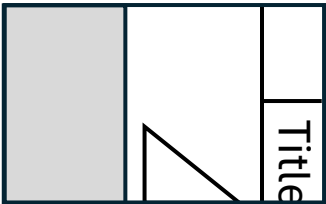


	MV	MX	MY
0			
1	MV	MX	
2		MX	MY
3	MV		MY
4			MY
5	MV		
6		MX	
7	MV	MX	MY

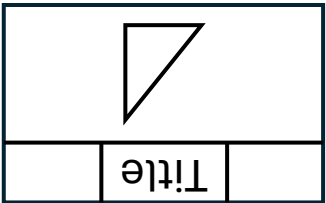
TFT Rotations – ST7789 (2 USB) – TFT_eSPI
setRotation()

width 240
height 320

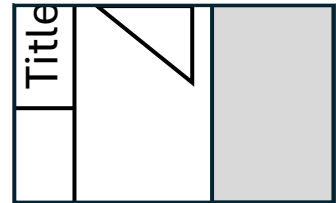
0



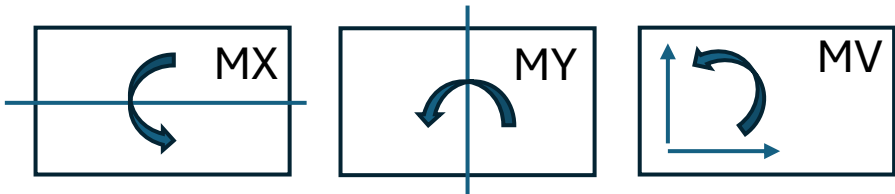
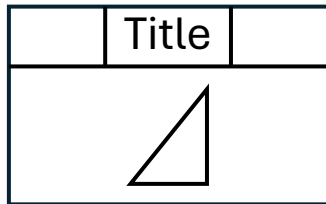
1



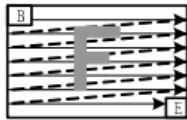
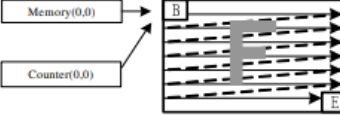
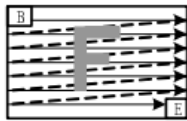
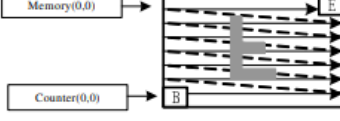
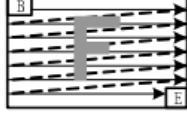
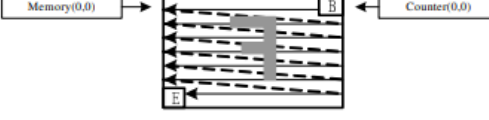
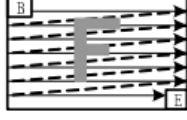
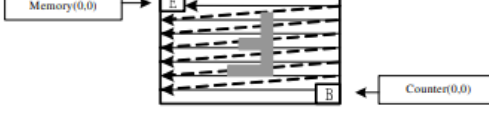
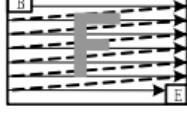
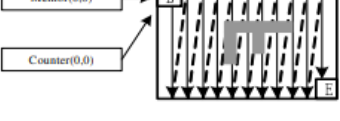

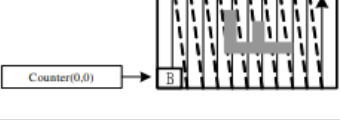




2



3



	MV	MX	MY
0			
1	MV	MX	
2		MX	MY
3	MV		MY

Display Data Direction	MADCTR Parameter			Image in the Memory (MPU)	Image in the Driver (Frame Memory)
	MV	MX	MY		
Normal	0	0	0		
Y-Mirror	0	0	1		
X-Mirror	0	1	0		
X-Mirror Y-Mirror	0	1	1		
X-Y Exchange	1	0	0		
X-Y Exchange Y-Mirror	1	0	1		
XY Exchange X-Mirror	1	1	0		
XY Exchange XY-Mirror	1	1	1		

MY	MX	MV	ML	BGR	MH	0	0
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36h	MADCTL (Memory Access Control)												
	D/CX	RDX	WRX	D17-8	D7	D6	D5	D4	D3	D2	D1	D0	HEX
Command	0	1	↑	XX	0	0	1	1	0	1	1	0	36h
Parameter	1	1	↑	XX	MY	MX	MV	ML	BGR	MH	0	0	00
	This command defines read/write scanning direction of frame memory.												
	This command makes no change on the other driver status.												
	Bit	Name		Description									
	MY	Row Address Order		These 3 bits control MCU to memory write/read direction.									
	MX	Column Address Order											
	MV	Row / Column Exchange											
	ML	Vertical Refresh Order		LCD vertical refresh direction control.									
	BGR	RGB-BGR Order		Color selector switch control (0=RGB color filter panel, 1=BGR color filter panel)									
	MH	Horizontal Refresh ORDER		LCD horizontal refreshing direction control.									
	Note: When BGR bit is changed, the new setting is active immediately without update the content in Frame Memory again.												
X = Don't care.													