Sequential benary multiplication

multiplier 2

Acumulator immult. Deimmult.

declare register: A[7:0] Q[7:0] M[7:0]

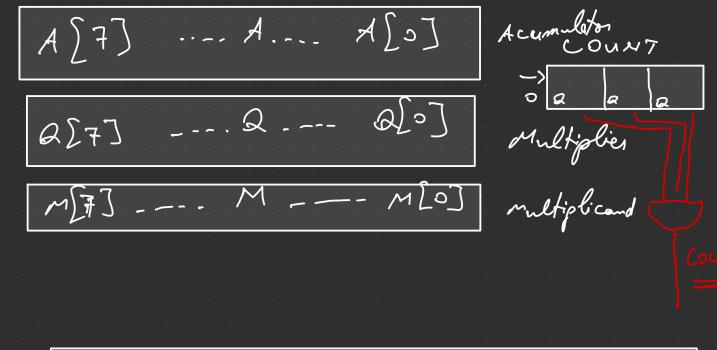
Counter [2:0]

declare BUS INBUS[7:0] DUTBUS[7:0]

A:=0 CQUNT:=0) (Co == tilune M:= inbus;)
Decrential ych in pualet

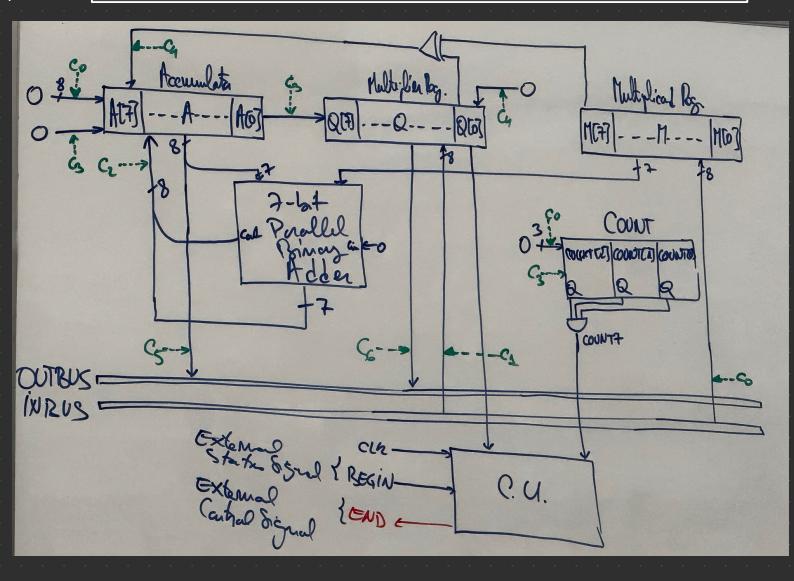
frecon chi BEGIN: INPUT: TESTA: if Q[0] = 0 -goto $2SHiFT_i/P_i := P_i + x_i \cdot y_i$ AAA: A[7:0] := A[6:0] + M[6:0];A[7]:=0, A[6:0]. Q:= A. Q[7:1]) (5)

count := count +1; RSHIFT: INCA: if count 7 for then go to TESTA TESTA: A[7]:= 2[0] exon M[7], Q[0]:= 0; Ou7 Bus:= A; ou7 Bus:= A; -> C6 SiGH : OUT PUT ENY



OUT BUS

in bus



= .793

registery & name buses & width Pseudo language concalenation operator A [6:0]. Q Synchronous exec. -> non- conflicting ->
-> sequentsal -> Assignment - Se loading
Flow Control of go to

SM 5 Lit integer 13. 2 *(-11)2 X= +13= 011010 Y = -11= 1 + 0118 5-143·2 8 P = Pg. Pg. ا : أنها هد Pg , 7 P8 P7 . --01101 10 000 00000 11011 0101 09 1 10 110 00101 012 00010 41011 01101 11101 Ex97 011 00110 Count 4 f 101. 0100011118 1 9 9 9 1 1 9100 91111 = - 143 P = 10.100 0.1111

X = -1.1875Y = -1.8125