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# Energy-Efficient Readout System for Semiconductor Radiation Detectors

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PROJECT REPORT
Submitted in partial fulfillment of the requirements for the MO632 course in Energy-Efficient Computing, as a special student of the Masters program in Computer Science

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# Abstract

Example text.



**Keywords**: Energy Efficient Design, FPGA Readout System, Medipix Collaboration.

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# Abbreviations

$ASIC$ Application Specific Integrated Circuits

$CLI$ Command-line Interface

$CSA$ Charge Sensitive Amplifier

$DAC$ Digital-to-Analog Converter

$OMR$ Operation Mode Register

# 1 Introduction

Example text.

## 1.1 Problem Contextualization

## 1.2 Literature Review

(...)

### 1.2.1 Medipix Collaboration

A complete and interesting review of the Medipix and Timepix ASICs is presented by Ballabriga et al. [2018]. In that review the design of the chips is explained in technical details and a number of other related chip developments are discussed.

# 2 Objectives

Example text.

# 3 Methodology

Example text.

# 4 Results

Example text.

# 5 Conclusions

Example text.

# References


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