K-Maps continue

SIMPLIFY A LOGIC EXPRESSION USING K-MAPS

Simplify an expression using K-maps

Given,

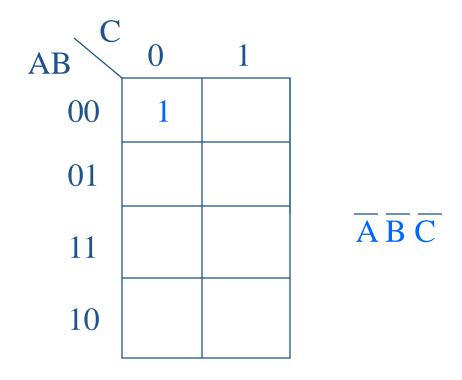
$$X = ABC + AB + BC$$

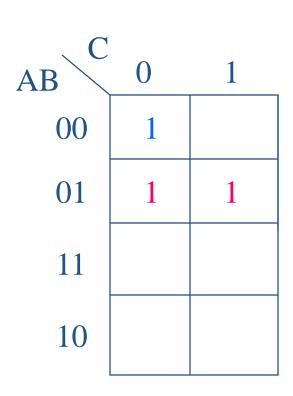
Simplify an expression using K-maps

Given,

$$X = ABC + AB + BC$$

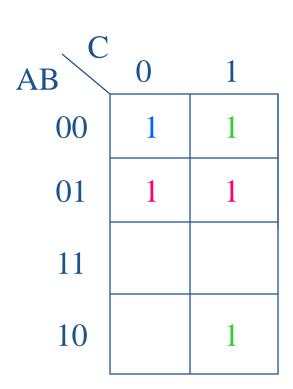
Simplify the above expression, using K-maps.





$$X = \overline{ABC} + \overline{AB+BC}$$

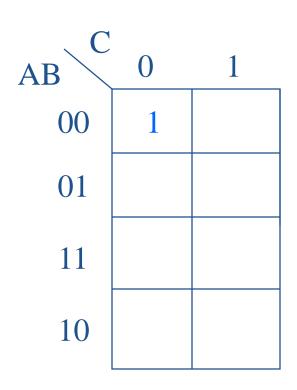
$$\overline{A} \overline{B} \overline{C} + \overline{A} B$$



$$X = \overline{ABC} + \overline{AB+BC}$$

$$\overline{A} \overline{B} \overline{C} + \overline{A} B + \overline{B} C$$

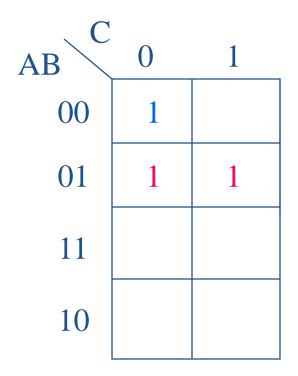
A more systematic way ...



$$\overline{A} \overline{B} \overline{C} + \overline{A} B + \overline{B} C$$

A	В	С	A'B'C'	A'B	В'С
0	0	0	1		
0	0	1			
0	1	0			
0	1	1			
1	0	0			
1	0	1			
1	1	0			
1	1	1			

$$\overline{A} \overline{B} \overline{C} + \overline{A} B + \overline{B} C$$



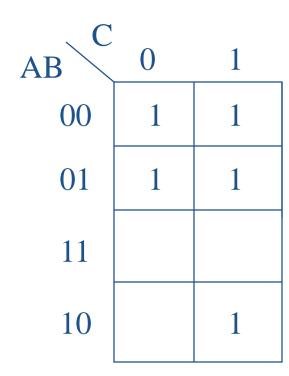
A	В	C	A'B'C'	A'B	В'С
0	0	0	1		
0	0	1			
0	1	0		1	
0	1	1		1	
1	0	0			
1	0	1			
1	1	0			
1	1	1			

$$\overline{A} \overline{B} \overline{C} + \overline{A} B + \overline{B} C$$

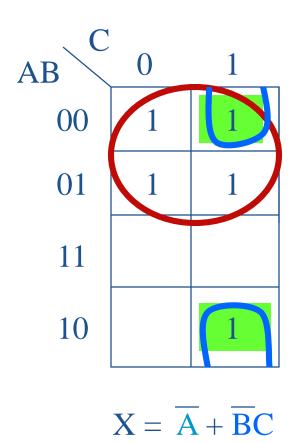
AB	0	1
00	1	1
01	1	1
11		
10		1

A	В	C	A'B'C'	A'B	B'C
0	0	0	1		
0	0	1			1
0	1	0		1	
0	1	1		1	
1	0	0			
1	0	1			1
1	1	0			
1	1	1			

Simplification



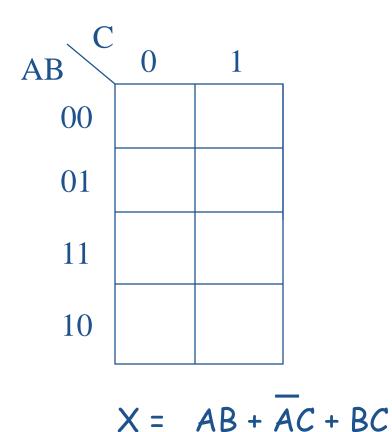
Simplification



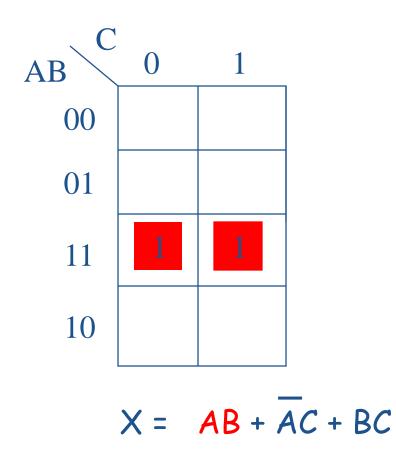
Another useful example...

$$X = AB + \overline{AC} + BC$$

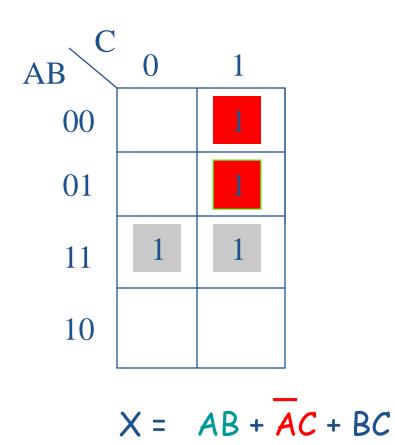
Another useful example...



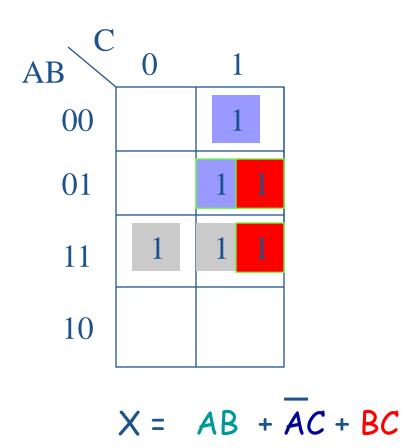
AB



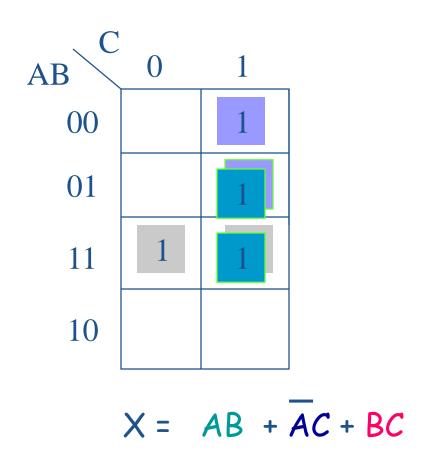
A'C



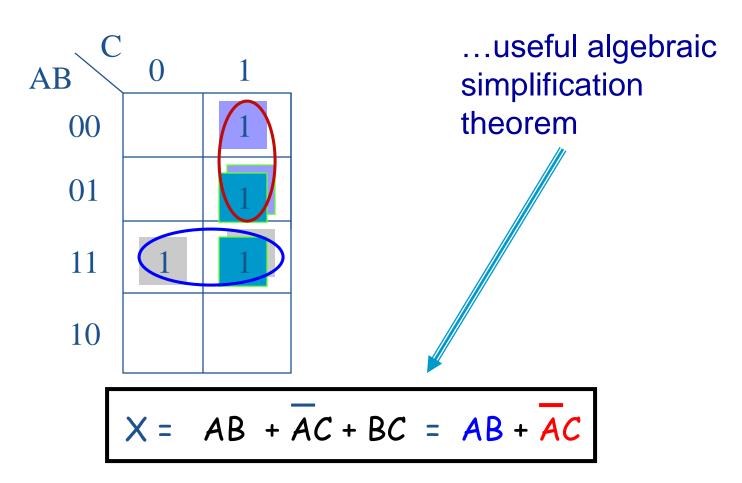
BC



Consensus theorem



Consensus theorem



Using the Table

A	В	С	AB	A'C	ВС
0	0	0			
0	0	1		1	
0	1	0			
0	1	1		1	1
1	0	0			
1	0	1			
1	1	0	1		
1	1	1	1		1

- How can we simplify the table?
- · Why it is: AB + AC'
- (project or independent study = 3 credits research)

Consensus Theorem

- AB + A'C + BC
- AB + A'C + 1 BC
- AB + A'C + (A + A')BC
- AB + A'C + ABC + A'BC
- AB(1+C) + A'C(1+B)
- AB + A'C

Which is ... AB+A'C+BC = AB + A'C

K-Map simplification technique

- > Good only for small circuits
- > Excellent academic method
- > There are better computer-based techniques

WWW-based simplification method

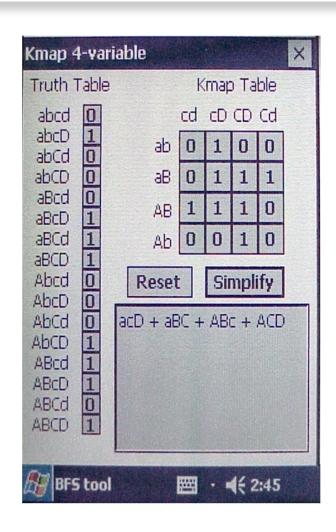
Tomaszewski, S.P, I.U. Ilgaz and Antoniou, G.E. (2003). WWW-Based Boolean function simplification, *International Journal of Applied Mathematics and Computer Science*, 13 (4), 577-583.

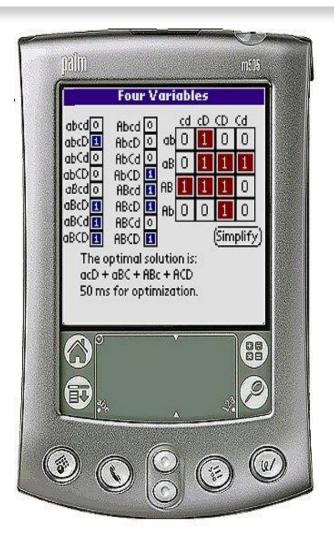
Quine W.V. (1952). The problem of simplifying truth tables, *American. Math. Monthly*, 59(8), 521-531.

McCluskey E.J. (1956). Minimization of Boolean functions, Bell System Technical Journal, 35(5), 1417-1444.

Sebastian Tomaszewski and I.U. Ilgaz are MSU/CS Alumni (2002)

PDA-based simplification





PDA-based simplification

Ledion Bitincka, George E. Antoniou, (2004), PDA-Based Boolean Function Simplification: A Useful Educational Tool, International Journal INFORMATICA, Vol. 15, No. 3, pp. 329-336.

Ledion Bitincka, George E. Antoniou, (2005), Pocket-PC Boolean Function Simplification, *International Journal in Electrical Engineering*, Vol. 56, No. 7-8, pp. 1-4.

BFSTool 1.0 (Softpedia)



BFSTool 1.0 (Freeware Palm OS)

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 Summary: A useful educational tool is presented, for students in Computer Science and Electrical and Computer Engineering, for minimizing low order Boolean expressions.

Requirements:

- Arrived: 2005-10-13
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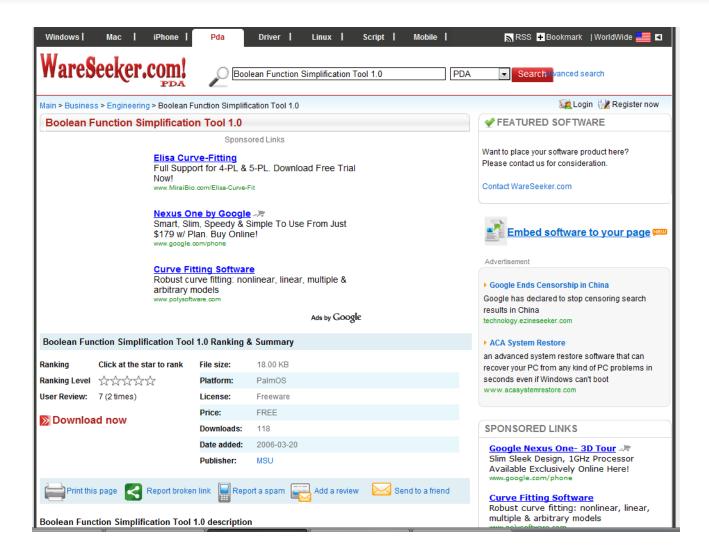
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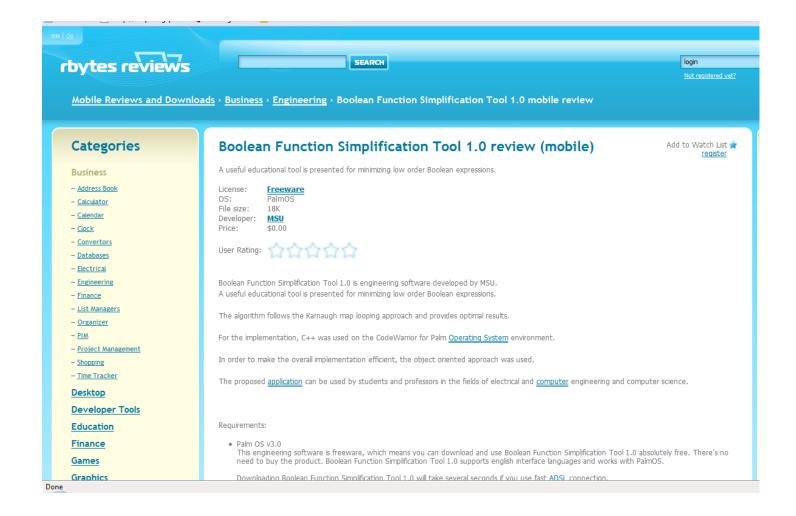
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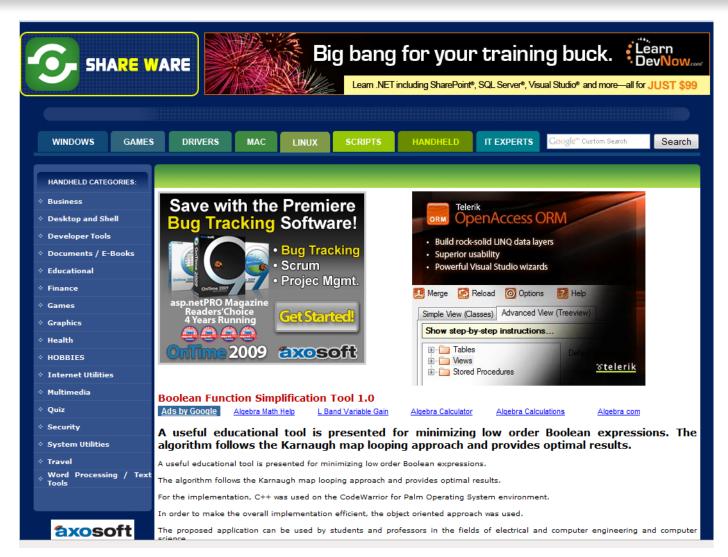
BFSTool 1.0 (WareSeeker)



BFSTool 1.0 (rbytes reviews)



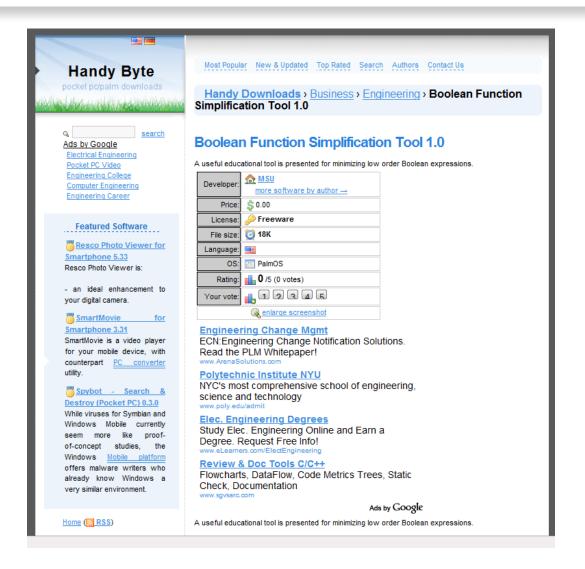
BFSTool 1.0 (Shareware)



BFSTool 1.0 (Mobile fun.net)



BFSTool 1.0 (Handy Byte)



Incompletely specified functions*

• In some design problems a number of the inputs never occur, so there is no specified output. Such an output is denoted by (X) and is called Don't Care Condition.



ISF-Example

The output (Z) of a three-input F(A,B,C) digital circuit is:

- \triangleright 0 if $F(A,B,C) \leq 2_{10}$
- \gt 1 if $F(A,B,C) \geq 5_{10}$
- > x otherwise

Set-up the Truth Table

Truth table

The output (Z) of a three-input F(A,B,C) digital circuit is:

$$\triangleright$$
 0 if $F(A,B,C) \leftarrow 2_{10}$

$$>$$
 1 if $F(A,B,C) >= 5_{10}$

> x otherwise

	Α	В	С	Z
0	0	0	0	
1	0	0	1	
2	0	1	0	
3	0	1	1	
4	1	0	0	
5	1	0	1	
6	1	1	0	
1	1	1	1	

Truth table

The output (Z) of a three-input F(A,B,C) digital circuit is:

$$>$$
 0 if $F(A,B,C) <= 2_{10}$

$$>$$
 1 if $F(A,B,C) >= 5_{10}$

> x otherwise

6

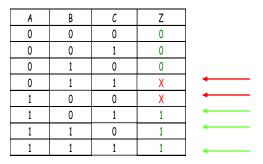
Α	В	С	Z
0	0	0	0
0	0	1	0
0	1	0	0
0	1	1	X
1	0	0	X
1	0	1	1
1	1	0	1
1	1	1	1

Output and don't care equations

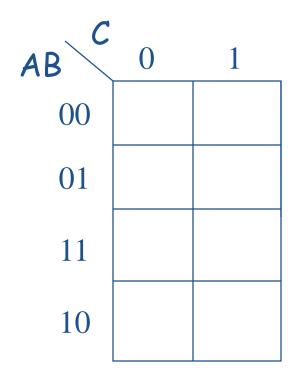
```
Z = and X =
```

Output and don't care equations

$$Z = A \overline{B} C + A B \overline{C} + ABC$$
and
$$X = \overline{A} B C + A \overline{B} \overline{C}$$

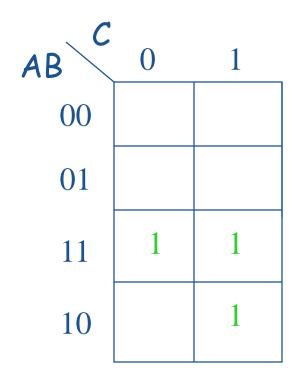


Set-Up K-map table - Z

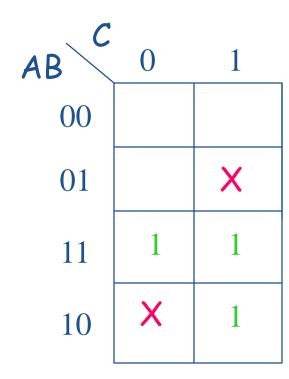


Α	В	С	Z
0	0	0	0
0	0	1	0
0	1	0	0
0	1	1	X
1	0	0	X
1	0	1	1
1	1	0	1
1	1	1	1

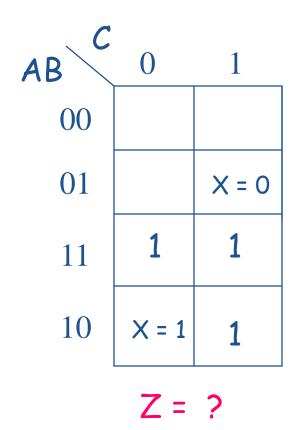
Set-Up K-map table - Z



Α	В	С	Z
0	0	0	0
0	0	1	0
0	1	0	0
0	1	1	Χ
1	0	0	X
1	0	1	1
1	1	0	1
1	1	1	1



Optimal values





Result: Z = A

