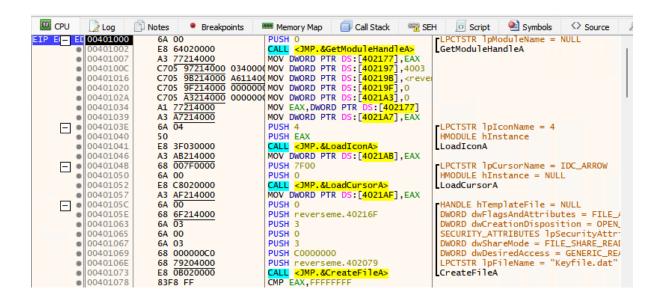
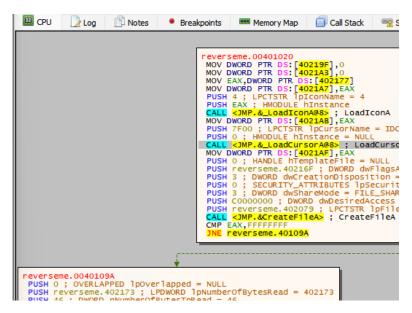
CPU - Main Screen:



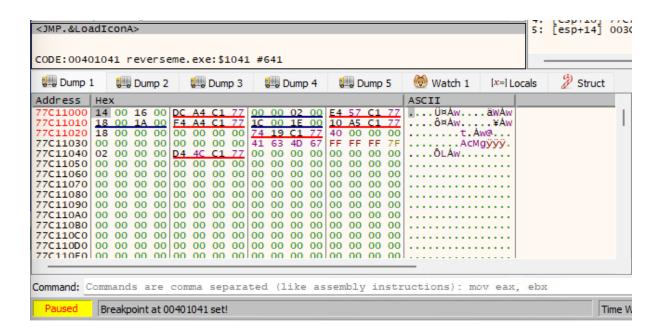
Left – Right (Sections):

(<u>Breakpoint/Registers Location</u>) | (<u>VAS – Virtual Address space</u>) | (<u>Opcode</u>) | (<u>Disassembly</u>) | (Comments / API's (<u>xAnalyzer</u>))

CPU - Main Screen (Graph Mode):



CPU - Bottom Screen:



Top - Bottom (Sections):

(Execution Window)

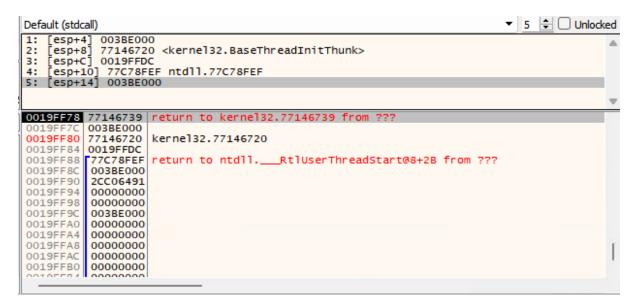
(5 Hex Dumps) | (Watch 1) | (Locals) | (Struct)

(Command Bar)

(Status Bar/Info Bar/Time Debugging Bar)

Notes: Struct is use to import header file Structs (Check Link Above). Parse header (Select File) + Visit Type (Struct name) + (nan) Function

CPU - Stack Screen:



Top – Bottom (Sections):

(<u>Stack Trace</u> - Menu bar) | (No. of <u>Stack Traces</u> (Default: 5)) | (Stack Tracer Status: (Unlocked/Calls/Locked))

(SP/ESP/RSP: Stack pointer for top address of the stack) | (<u>VAS – Virtual Address space</u>) | (Comments)

CPU - Registers Screen:

X32Dbg / X64Dbg

		Show FPU			Show FPU	
EAX	0019FFCC		RAX	0000000000937B30	<hxd.entrypoi< th=""></hxd.entrypoi<>	
EBX	00284000		RBX	0000000000000000	KIIXUI EIICI YI OII	
ECX	00401000	<pre><reverseme.entrypoint></reverseme.entrypoint></pre>	RCX	00000000003AE000		
EDX	00401000	<pre><reverseme.entrypoint></reverseme.entrypoint></pre>	RDX	0000000000937B30	<hxd.entrypoi< th=""></hxd.entrypoi<>	
EBP	0019FF84		RBP	0000000000000000		
ESP	0019FF78		RSP	000000000014FF28		
ESI	00401000	<pre><reverseme.entrypoint></reverseme.entrypoint></pre>	RSI	0000000000000000		
EDI	00401000	<reverseme.entrypoint></reverseme.entrypoint>	RDI	0000000000000000		
EIP	00401000	<reverseme.entrypoint></reverseme.entrypoint>	R8	0000000003AE000		
			R9	0000000000937B30	<hxd.entrypoi< th=""></hxd.entrypoi<>	
EFLAG			R10	00007FFE5F737C90	kernel32.0000	
	PF 1 AF 0		R11	0000000000000000		
	SF 0 DF 0		R12	0000000000000000		
CF 0	TF 0 IF 1		R13	0000000000000000		
			R14	0000000000000000		
LastError 0000012A (ERROR_TOO_MANY_POSTS LastStatus C0000047 (STATUS_SEMAPHORE_LIM			R15	0000000000000000		
GS 00	2B FS 0053		RIP	0000000000937B30	<hxd.entrypoi< th=""></hxd.entrypoi<>	
ES 002B DS 002B			RFLAGS 000000000000244			
CS 0023 SS 002B			ZF 1 PF 1 AF 0			
			OF O			
	0000000		CF 0			
	0000000		Last	Error 00000000 (ERRO	R_SUCCESS)	
DR3 00000000			Lasts	Status C0000023 (STAT	US_BUFFER_TOO_SI	
DR6 0	0000000					
DR7 0	0000000		GS 00	02B FS 0053		
				02B DS 002B		
			CS 00	033 <u>SS</u> 002B		
				DRO 000000000000000		
				DR1 000000000000000		
			DR2 000000000000000			
			DR3 000000000000000			
				DR6 000000000000000		
			DR7 (000000000000000		

General Purpose Registers:

- AL/AH/AX/EAX/RAX: Accumulator
- BL/BH/BX/EBX/RBX: Base index (for use with arrays)
- CL/CH/CX/ECX/RCX: Counter (for use with loops and strings)
- DL/DH/DX/EDX/RDX: Extend the precision of the accumulator (e.g. combine 32-bit EAX and EDX for 64-bit integer operations in 32-bit code)
- BP/EBP/RBP: Stack base pointer for holding the address of the current stack frame.
- SP/ESP/RSP: Stack pointer for top address of the stack.
- SI/ESI/RSI: Source index for string operations.
- DI/EDI/RDI: *Destination index* for string operations.
- R8 R15(x64 only): Eight additional 64-bit general registers.
- IP/EIP/RIP: Instruction pointer. Holds the <u>program counter</u>, the address of next instruction.

Control Registers:

- ZF Zero Flag
- OF Overflow Flag
- CF Carry Flag
- PF Parity Flag
- SF Sign Flag
- TF Trap Flag
- AF Adjust Flag
- <u>DF Direction Flag</u>
- IF Interrupt Enable Flag

Segment Registers:

- CS: Code
- DS: Data
- SS: Stack
- ES: Extra data
- FS: Extra data #2
- GS: Extra data #3

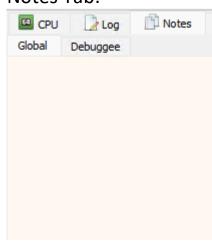
Debug Registers:

- DR0 -> DR3: Breakpoints
- DR6: Debug Status
- DR7: Debug Control

Log Tab:



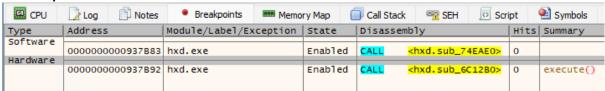
Notes Tab:



GUI Manual - Notes

GUI Manual - Log

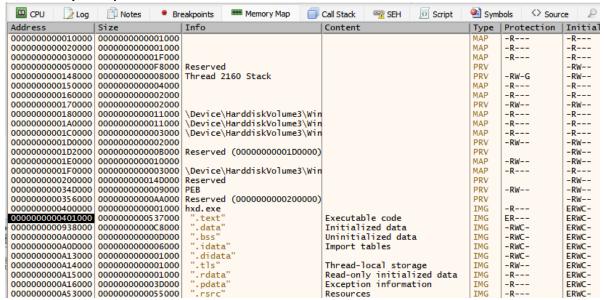
Breakpoints Tab:



There are 2 types of breakpoints: (Software / Hardware). Hardware only has (4 breakpoint Slots).

GUI Manual – Conditional Breakpoints

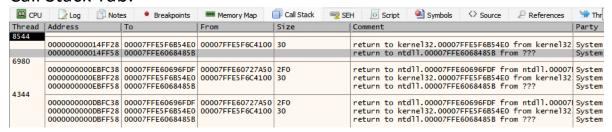
Memory Map Tab:



Memory Map:

In <u>computer science</u>, a **memory map** is a structure of data (which usually resides in memory itself) that indicates how <u>memory</u> is laid out.

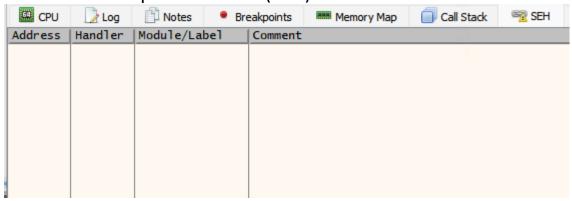
Call Stack Tab:



A **call stack** is a <u>stack data structure</u> that stores information about the active subroutines of a computer program

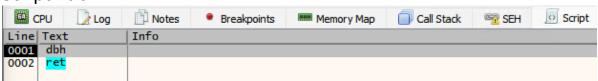
GUI Manual - Call Stack

Structured Exception Handler (SEH) Chain Tab:

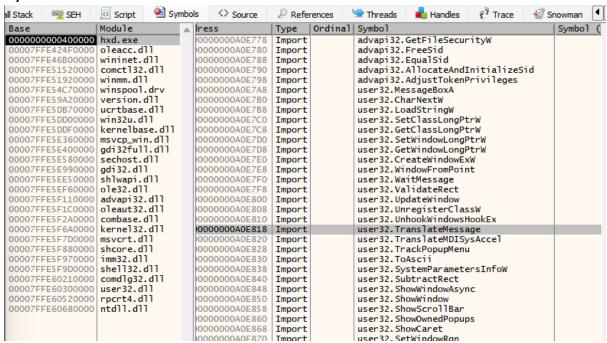


Wikipedia – SEH mechanism

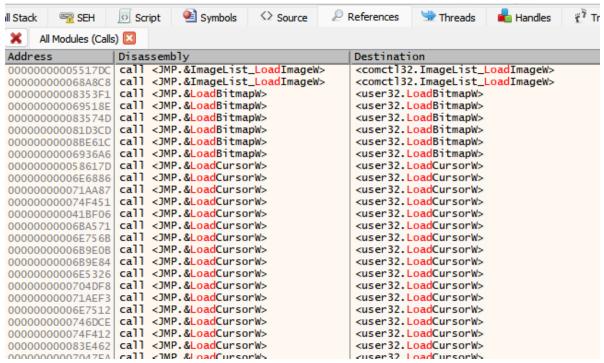
Script Tab:



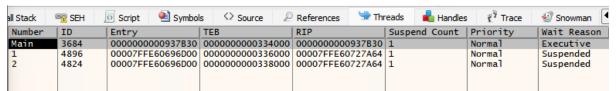
Symbols Tab:



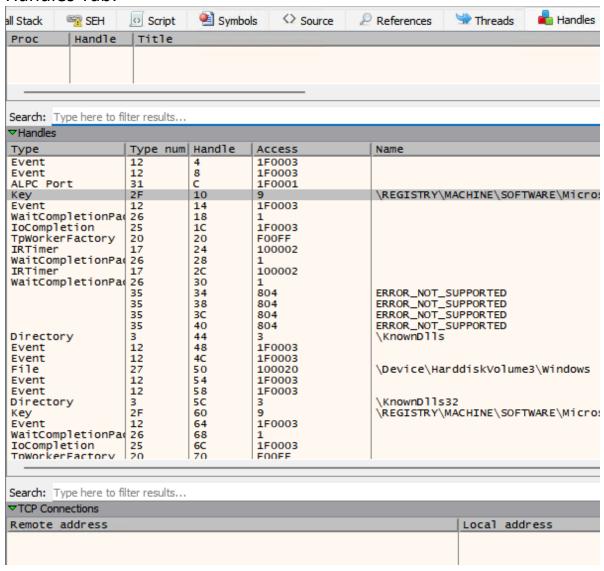
References Tab:



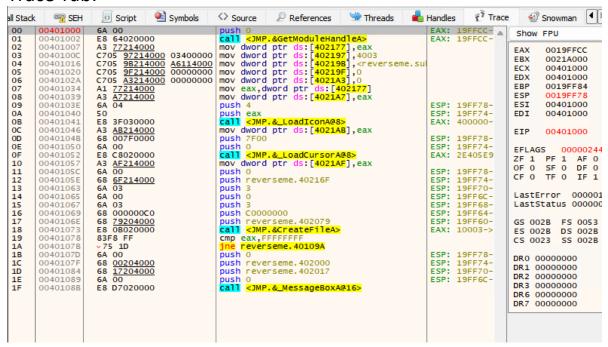
Threads Tab:



Handles Tab:



Trace Tab:

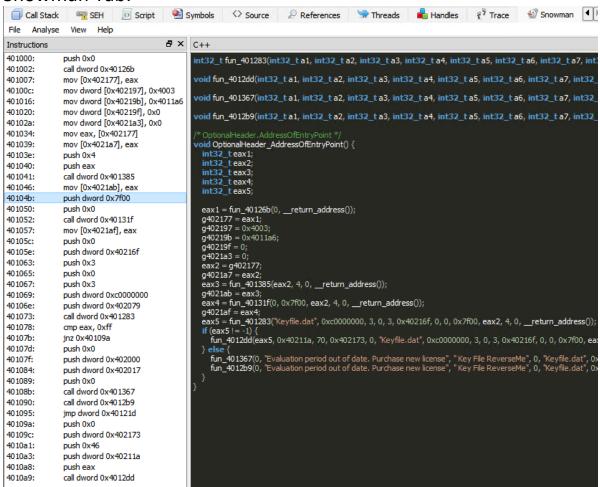


Left - Right (Sections):

(Steps) | (<u>VAS – Virtual Address space</u>) | (<u>Opcode</u>) | (<u>Disassembly</u>) | (Comments / API's (<u>xAnalyzer</u>))

GUI Manual - Trace

Snowman Tab:



Snowman - C++ Decomplier (Plugin)

https://github.com/x64dbg/snowman/releases/tag/plugin-v1