Projet Minilucy

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idée générale

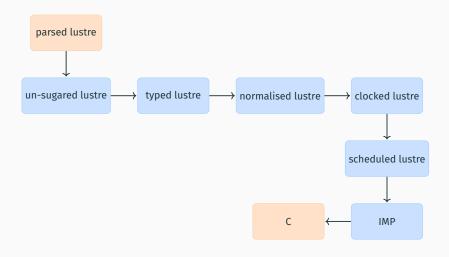
when

automate

merge

mémoire

Schéma de compilation



Features réalisées

- when
- merge
- reset
- automates (en surface uniquement)

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Vérification des horloges

```
node clock_error (c: bool) returns (o: int);
  var x: int;
let
  x = 0 when True(c);
  o = x;
tel

Clocking error: The expected clock is Base, got Base on True(c)
```

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Traduction de l'automate

Traduction de l'automate

```
type t = Even | Odd
node syracuse (i: int) returns (o: int);
    var state: t; cond__4: bool; cond__3: bool;
let.
  cond_4 = o mod 2 = 0;
  cond 3 = 0 \mod 2 = 1;
  state = Even ->
           pre (merge state
                   (Even -> if cond__3 then Odd else Even)
                   (Odd -> if cond 4 then Even else Odd));
  o = merge state
         (Even \rightarrow i \rightarrow pre o / 2)
         (0dd \rightarrow i \rightarrow pre o * 3 + 1);
tel
```

Automate (une slide)

```
node syracuse (i: int)
  returns (o: int);
let
  automaton
  | Even ->
        o = i -> pre o / 2;
        until (o mod 2 = 1)
        continue Odd
  | Odd ->
        o = i -> pre o * 3 + 1;
        until (o mod 2 = 0)
        continue Even
  end
tel
```

```
type t = Even | Odd
node syracuse(i: int)
  returns (o: int):
 var state: t:
      cond__4, cond__3: bool;
let
  cond 4 = 0 \mod 2 = 0:
cond_3 = o mod 2 = 1;
  state = Even ->
           pre (merge state
                (Even ->
                   if cond__3 then Odd else Even)
                 (Odd ->
                  if cond__4 then Even else Odd));
  o = merge state
        (Even -> i -> pre o / 2)
        (Odd -> i -> pre o * 3 + 1):
tel
```

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Compilation des « merge »

Compilation des « merge »

```
enum t { A, B };
enum t main0(...) {
   switch (o) {
       case A: {
         res = B;
           break;
       case B: {
         res = A;
           break;
   return res;
```

Compilation des « merge »

```
enum t { A, B };
enum t main0(...) {
    switch (o) {
        case A: {
            res = B:
            break;
        case B: {
            res = A:
            break;
    return res;
```

idée générale

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merge

mémoire

on créé une mémoire pour chaque nœud si :

- présence d'un fby dans le nœud
- appel d'un nœud qui a une mémoire
- présence d'un automate

```
node f () returns (o:int);
let
    o = 1;
tel

node main0 () returns (o:int);
let
    o = f();
tel
```

```
node f () returns (o:int);
let
    o = 1 fby 2;
tel

node main0 () returns (o:int);
let
    o = f();
tel
```

```
struct f_mem {
  int o;
};
struct main0_mem {
  struct f_mem f_next;
};
void f_init (struct f_mem* mem) {
  mem -> o = 1;
void main0_init (struct main0_mem* mem) {
 f_init(&(mem->f_next));
```

```
node f () returns (o:int);
let
    o = 1 fby 2;
tel

node main0 () returns (o:int);
let
    o = f();
tel
```

```
struct f_mem {
   int o;
};
struct main0_mem {
    struct f_mem f_next;
};
//...
void f_init (struct f_mem* mem) {
   mem->o = 1;
}
//...
void main0_init (struct main0_mem* mem) {
   f_init(&(mem->f_next));
}
//...
```

idée générale

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Compilation des « reset »

```
node incr () returns (cpt: int);
let
 cpt = 0 fby cpt + 1;
t.el
node main0 (i: bool) returns (o: int);
let
 o = reset incr() every i;
tel
→ ajouter un appel à incr_init() quand i est vrai
```

