

## LECTURE 2

Friday, September 8, 2017 8:37 PM

### RECAP

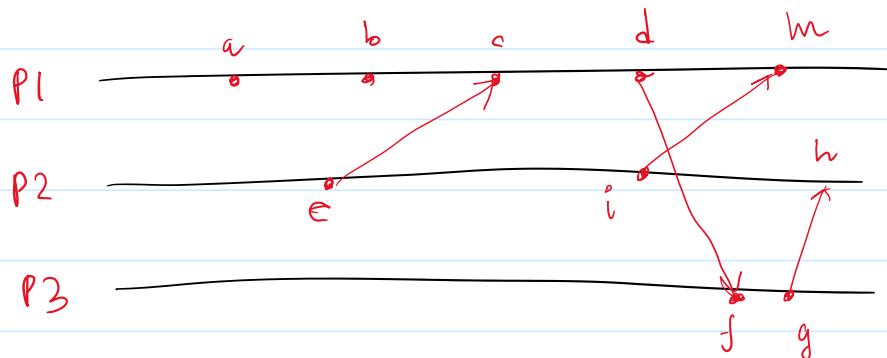
### HAPPENS BEFORE RELATION

$e \rightarrow f$

- Ⓘ If  $a$  and  $b$  takes place on same process &  $a$  happens first,  $a \rightarrow b$
- Ⓜ If  $a$  is send( $m$ ) and  $b$  is receive( $m$ ),  $a \rightarrow b$
- Ⓢ If  $a \rightarrow b$  and  $b \rightarrow c$ , by transitivity  $a \rightarrow c$

THIS IS NOT A TOTAL ORDERING

$a \parallel b$  iff  $\neg(a \rightarrow b)$  and  $\neg(b \rightarrow a)$



All events happening before  $f$ :  $a, b, c, e, d$

All events concurrent to  $i$ :  $a, b, c, d, f, g$

### LOGICAL CLOCK

Each process  $P_i$  has own logical clock  $C_i$

- assign a number to each event that occurs at  $p_i$

-  $c_i(e) \Rightarrow$  time of event  $e$

System of clocks = global function  $C$  : assigns time to every event.

$C(e) = c_i(e)$  if  $e$  takes place on  $p_i$

if  $a \rightarrow b$  then  $C(a) < C(b) \Rightarrow$  LAMPORTS' CLOCK CONDITION.

Clocks need to satisfy 2 conditions :

(i) if  $a$  and  $b$  takes place on same process  $p_i$  :  
and  $a \rightarrow b$ , then  $C(a) < C(b)$ .

(ii) If  $a = \text{send}(m)$ ,  $b = \text{receive}(m)$  then,  
 $C(a) < C(b)$

① and ② are gonna implement Lamports' clock condition.  
because both  $\rightarrow$  and  $<$  are transitive relations.

CLOCK IMPLEMENTATION (from perspective of a single process)

Initially  $C_i = 0$

Increment  $C_i$  between any 2 successive events (tick) by 1

If event is send of message :

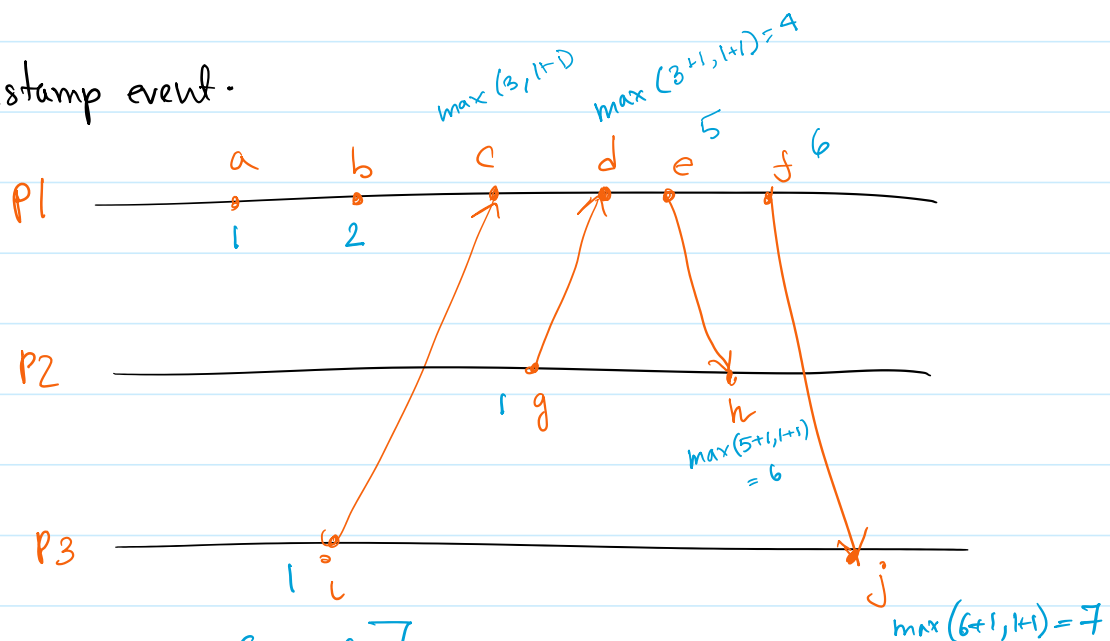
timestamp message with  $T_m = C_i$

timestamp message with  $T_m = C_i$

If event  $i$  receive of message with timestamp  $T_m$ :

$$C_i = \max(C_i, T_{m+1})$$

Timestamp event.



$$\begin{aligned} \text{now } C(h) &= 6 \\ C(f) &= 6 \end{aligned}$$

that means, we can have collisions  
 $\therefore C$  is a partial order

$\therefore$  If we timestamp only using Lamport's algorithm, we cannot Res

## TOTALLY ORDERED LAMPORT TIMESTAMPS

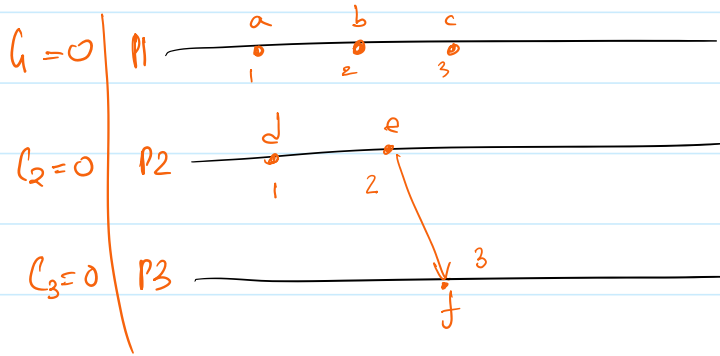
$C(a) = (i, C_i(a))$  event takes place at  $p_i$   
 $\downarrow$   
 process id

$C(a) < C(b)$  if either  $\begin{cases} (i) C_i(a) < C_i(b) \\ (ii) C_i(a) = C_i(b) \text{ and } i < j \end{cases}$

In previous figure, now,  $C(h) = (2, 6) \therefore C(h) < C(f)$

$$c(f) = (3, 6)$$

$$\Rightarrow h \rightarrow f$$



If  $a \rightarrow b$ ,  $c(a) < c(b)$

in this case,

$$\neg (a \rightarrow e)$$

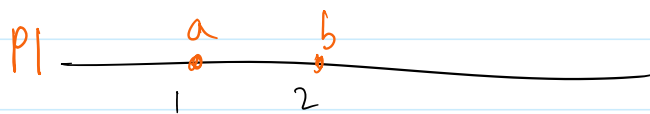
they are concurrent

### WEAK CLOCK CONDITION

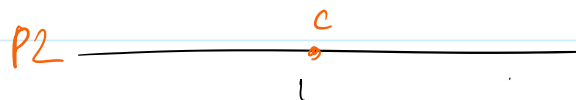
So it happens that I can't just look at the timestamps and determine a causal relationship. So this condition is not strong.

### $\Rightarrow$ STRONG CLOCK CONDITION (maybe what we want?)

$$c(e) < c(f) \text{ iff } e \rightarrow f$$



$$c(a) < c(b)$$



$$\text{but } \neg (c \rightarrow b)$$

$$\left. \begin{array}{l} a \rightarrow b \Rightarrow c(a) < c(b) \\ a \parallel c \Rightarrow c(a) = c(b) \\ b \parallel c \Rightarrow c(b) = c(c) \end{array} \right\}$$

Not possible using integer  
Lamport Timestamps

Does totally ordered Lamport's Algorithm solve this?

No, Process id does not break causality, it's just for breaking ties.

## STRONG CLOCK CONDITIONS

$$c(e) < c(f) \text{ iff } e \rightarrow f$$

VECTOR CLOCKS : (Mattern 1989, Fidge 1991)

So now, we have  $N$  processes in the system,

↳ each process has a vector clock : array of  $N$  integers

↳ timestamp all events.

↳ piggyback on all messages.

ALGORITHM (at  $P_i$ ) (they all run the same thing)

INITIALLY :  $my\_VT = (0, 0, 0, \dots, 0) \in \mathbb{R}^N$

On event  $e$  :

$$my\_VT(i) = my\_VT(i) + 1 \quad (\text{tick})$$

If  $e$  is send of message ( $m$ ) :

$$m.VT = my\_VT \quad (\text{piggyback})$$

If  $e$  is receive of message ( $m$ ) :

$$my\_VT(j) = \max(m.VT(j), my\_VT(j))$$

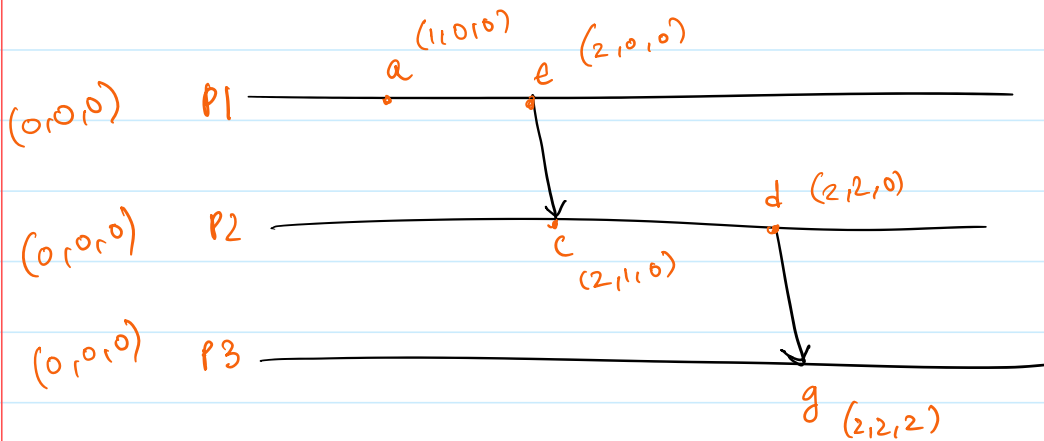
for  $j = 1, 2, 3, \dots, N$

(pairwise max of elements)

for eg.  $\max[(0, 1, 2), (2, 2, 0)] = (2, 2, 2)$

for eg.  $\max \left[ (0,1,2), (2,2,0) \right] = (2,2,2)$

\* timestamp  $e$

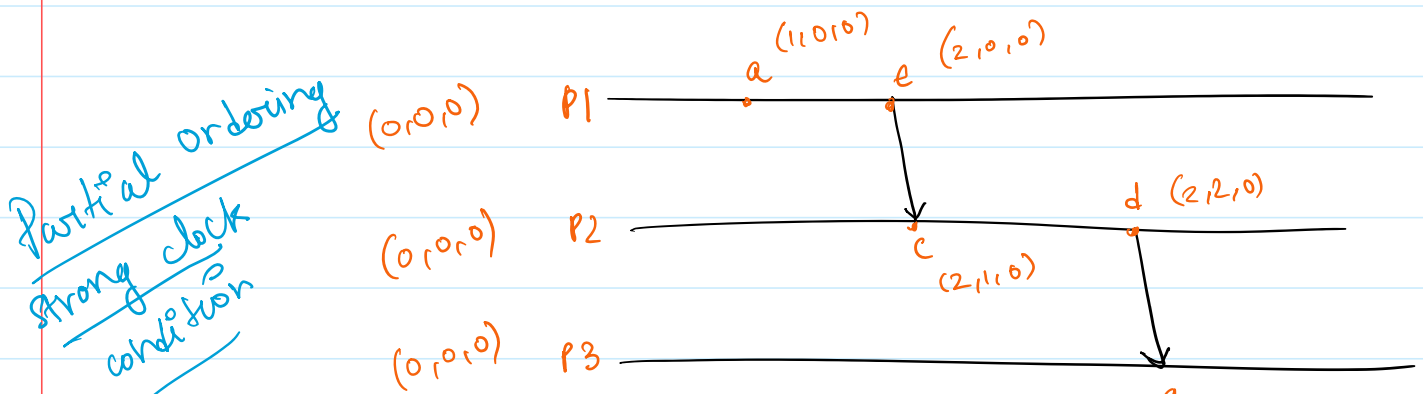


at  $p_i$ ,  $my\_VT(i) = \#$  of events that  $p_i$  have time stamped  
 $my\_VT(j) = \#$  of events that occurred at  $p_j$  that  
 $j \neq i$  causally precede  $e$

How do you compare these VT now?  $e.VT$  for event  $e$   
 $f.VT$  for event  $f$

we will say,

- (i)  $e.VT = f.VT$  iff  $e.VT(i) = f.VT(i) \forall i \in \{1, N\}$
- (ii)  $e.VT \leq f.VT$  iff  $e.VT(i) \leq f.VT(i) \forall i \in \{1, N\}$
- (iii)  $e.VT < f.VT$  iff  $e.VT \leq f.VT$  &  $e.VT \neq f.VT$



Partial ordering  
 Strong clock  
 condition

condition

$(0,0,0)$  p3

$\downarrow$   
g  $(2,2,2)$

$$\begin{aligned} \textcircled{1} \quad VT(e) &= (2,0,0) & \therefore VT(e) < VT(g) \\ VT(g) &= (2,2,2) & \Rightarrow e \rightarrow g \end{aligned}$$

$$\begin{aligned} \textcircled{2} \quad VT(f) &= (0,0,1) & \therefore VT(e) < VT(f) ? \text{ No} \\ VT(e) &= (2,0,0) & VT(f) < VT(e) ? \text{ No} \end{aligned} \left. \vphantom{\begin{aligned} \textcircled{2} \quad VT(f) &= (0,0,1) \\ VT(e) &= (2,0,0) \end{aligned}} \right\} \begin{array}{l} \text{Concurrent} \\ \text{e/f} \end{array}$$

**PROOF** **THEOREM**

Vector clock satisfies the strong clock condition  $e \rightarrow f$  iff  $VT(e) < VT(f)$

SKETCH

$$(a) \quad e \rightarrow f \Rightarrow VT(e) < VT(f)$$

(i) If they are events on the same process,  $e$  &  $f$  and  $e \rightarrow f$ , we  $P_i$  increments  $P_i$ 's component in the VT strictly from  $e$  to  $f$ .  $P_i$ 's gonna tick  $i^{th}$  component before each event in my-VT & no other component. (So its own component in the  $\mathbb{R}^N$  vector is strictly greater in  $f$  in compared to  $e$ )

(ii) Send & Receive : If  $e$  is send by  $P_i$  and  $S$  is received by  $P_j$  because it added 1 to its own component & the rest is pairwise max, therefore,

$P_j$  ticks component  $j$  of its my-VT and then does pairwise max with m-VT. So because we ticked

we have at least **1** element which is greater.

(i) and (ii)  $\Rightarrow$  other cases by transitivity

(b) if  $VT(e) < VT(f) \Rightarrow e \rightarrow f$

We are going to prove by contrapositive  $\neg$

$$\neg (e \rightarrow f) \Rightarrow \neg (e.VT < f.VT)$$

CASE I : assume  $f \rightarrow e$ , then  $f.VT < e.VT$  } weak clock condition

CASE II : assume  $f \parallel e$  :

CLAIM :  $e$  &  $f$  must take place on different processes

say  $e$  is on  $p_i$  and  $f$  is on  $p_j$

when  $e$  occurs

$$e.VT(i) > p_j.VT(i)$$

$\downarrow$   
 $p_j$  has no idea  $e$  is happening

$$e.VT(i) > f.VT(i)$$

$\hookrightarrow$  how many events having occurred at  $p_i$  that process  $f$  knows about

Same argument

$$e.VT(j) < f.VT(j). \text{ Only because } e \parallel f$$

$p_i$  knows about  $e$ , but  $p_j$  doesn't

$p_j$  knows about  $f$ , but  $p_i$  doesn't



$p_j$  knows about  $f$ , but  $p_i$  does not

$\rightarrow \neg (e \cdot VT < f \cdot VT)$   
and  $\neg (f \cdot VT < e \cdot VT)$

Scalability??

Not good for  
 $N > 1000$   
you don't want to  
send a  $x \in \mathbb{R}^N$   
with each message.