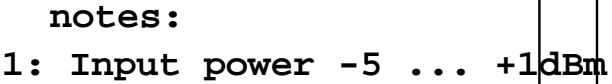


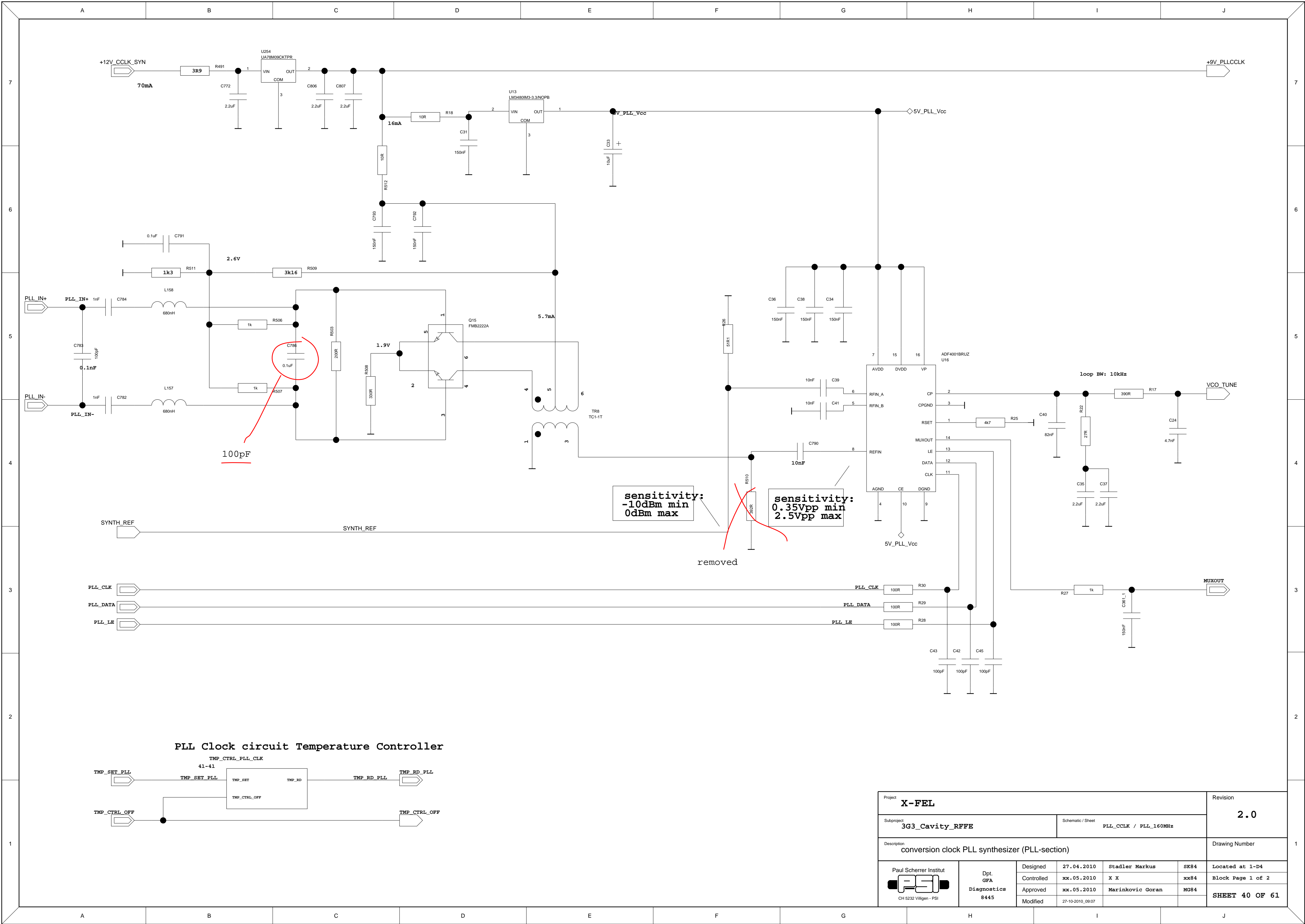
Global signals:


GND, BGND, DGND
+12V, +3.3V_D

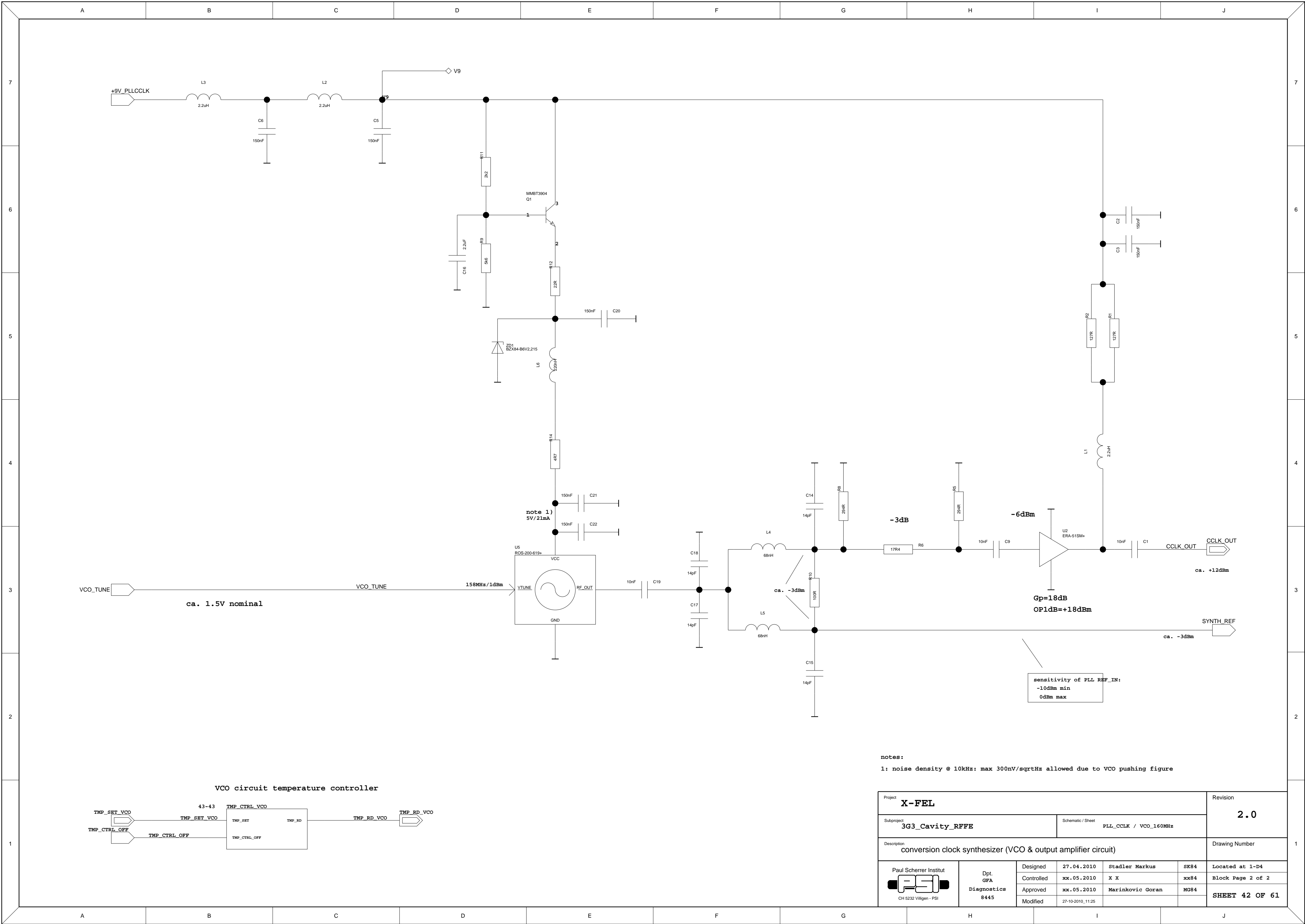
Project		Revision	
X-FEL		2.0	
Subproject		Drawing Number	
3G3 Cavity_RFFE		SHEET 1 OF 61	
Description		Block Page 1 of 1	
Top Level Schematic		Located at Top-Top	
	Designed	27.04.2010	Stadler Markus
	Controlled	xx.05.2010	X X
	Approved	xx.05.2010	Marinkovic Goran
	Modified	16-10-2010_09:50	NG84
Dpt. GFA		8445	
Diagnostische		8445	

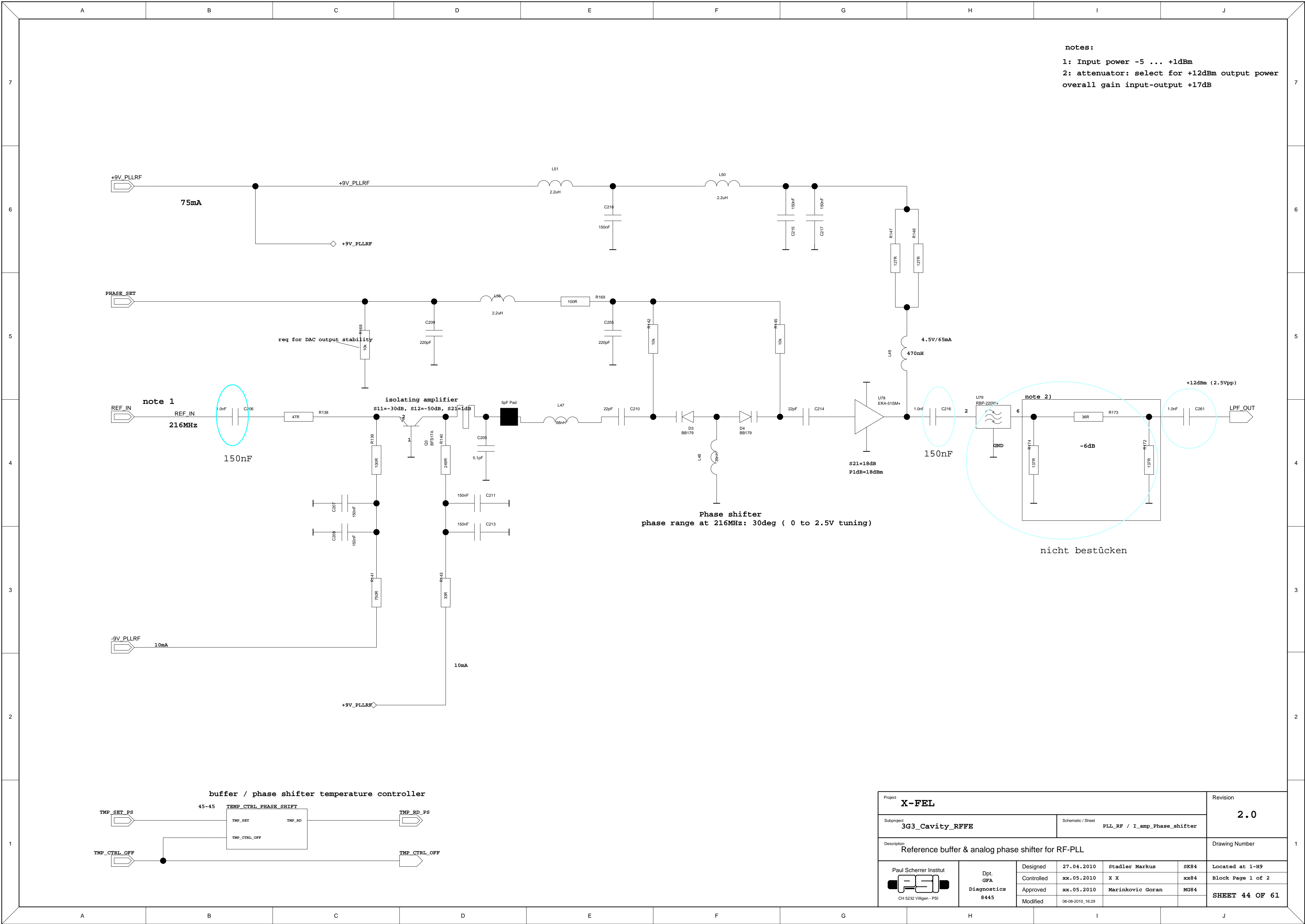


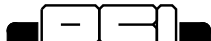
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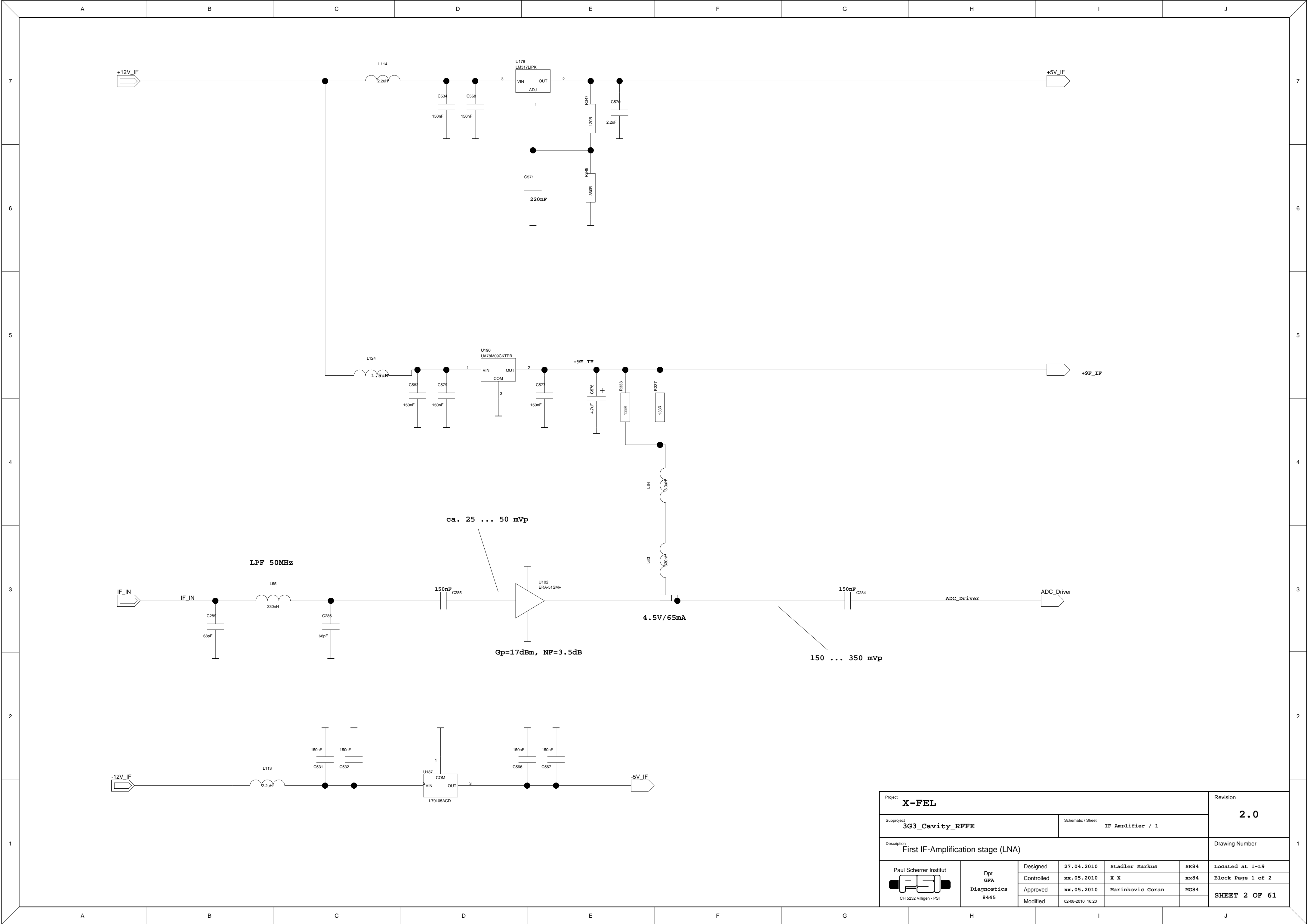


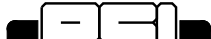
Project X-FEL						Revision 2.0	
Subproject 3G3_Cavity_RFPE			Schematic / Sheet PLL_CCLK / PLL_160MHz				
Description conversion clock PLL synthesizer (PLL-section)						Drawing Number	
<div>Paul Scherrer Institut</div> <div></div> <div>CH 5232 Villigen - PSI</div>	<div>Dpt. GFA</div> <div>Diagnostics</div> <div>8445</div>	Designed	27.04.2010	Stadler Markus	SK84	Located at 1-D4	
		Controlled	xx.05.2010	X X	xx84	Block Page 1 of 2	
		Approved	xx.05.2010	Marinkovic Goran	MG84	SHEET 40 OF 61	
		Modified	27-10-2010_09:07				

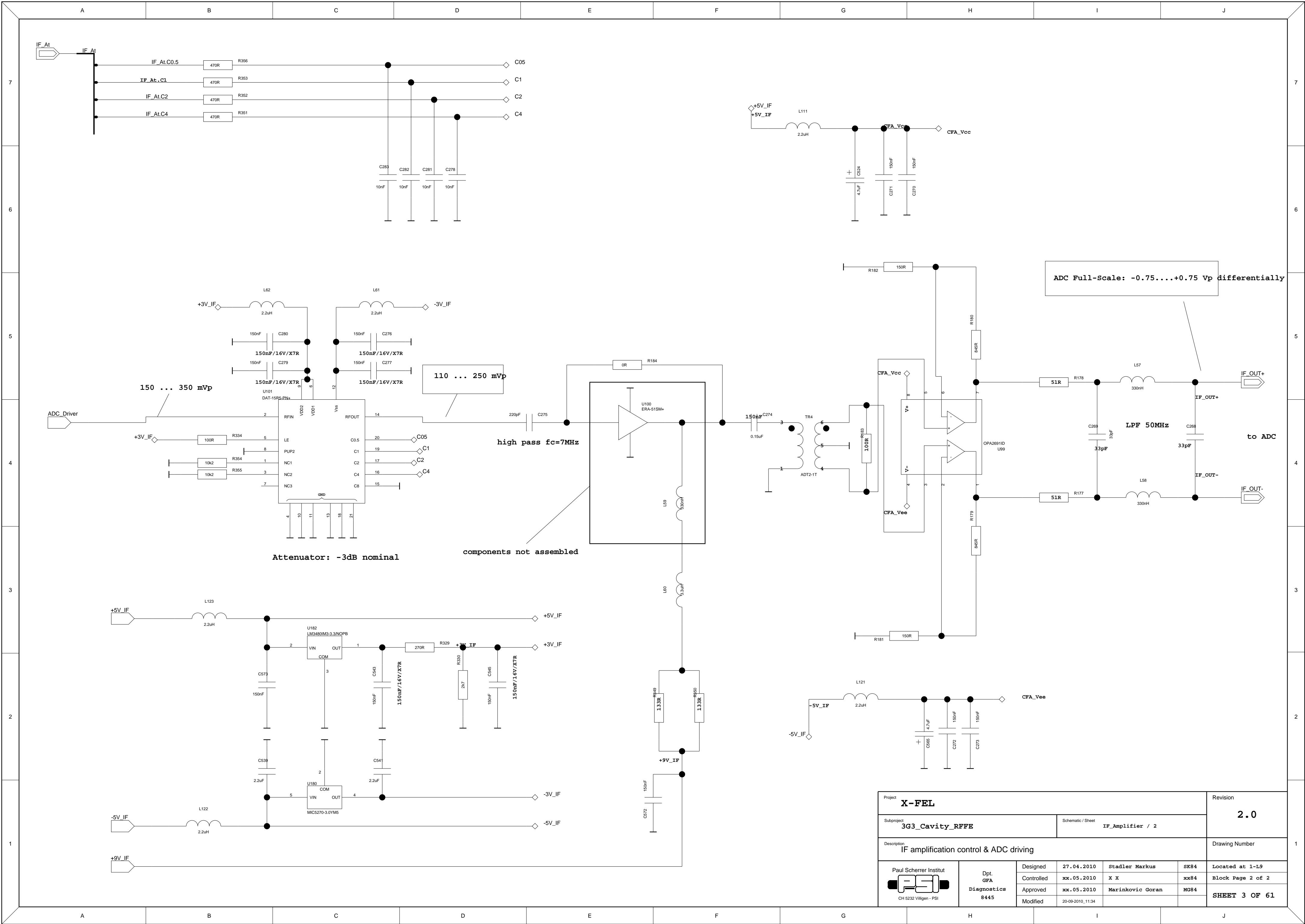


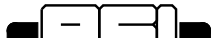


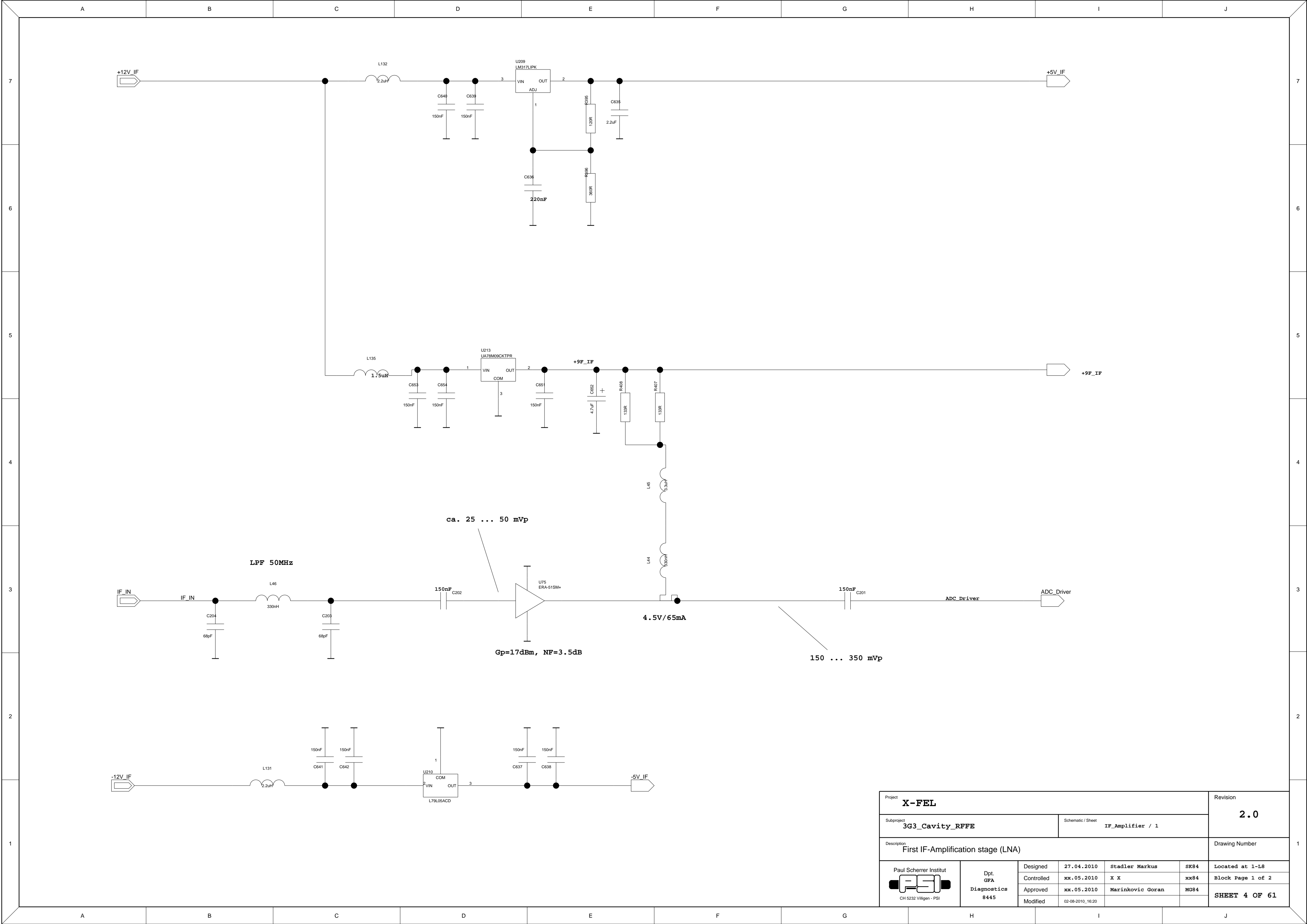
Project X-FEL						Revision 2.0		
Subproject 3G3_Cavity_RFFE			Schematic / Sheet PLL_RF / I_amp_Phase_shifter			Drawing Number		
Description Reference buffer & analog phase shifter for RF-PLL								
<div>Paul Scherrer Institut</div> <div></div> <div>CH 5232 Villigen - PSI</div>		<div>Dpt. GFA</div> <div>Diagnostics</div> <div>8445</div>		Designed	27.04.2010	Stadler Markus	SK84	Located at 1-H9
				Controlled	xx.05.2010	X X	xx84	Block Page 1 of 2
				Approved	xx.05.2010	Marinkovic Goran	MG84	SHEET 44 OF 61
				Modified	06-08-2010, 16:29			

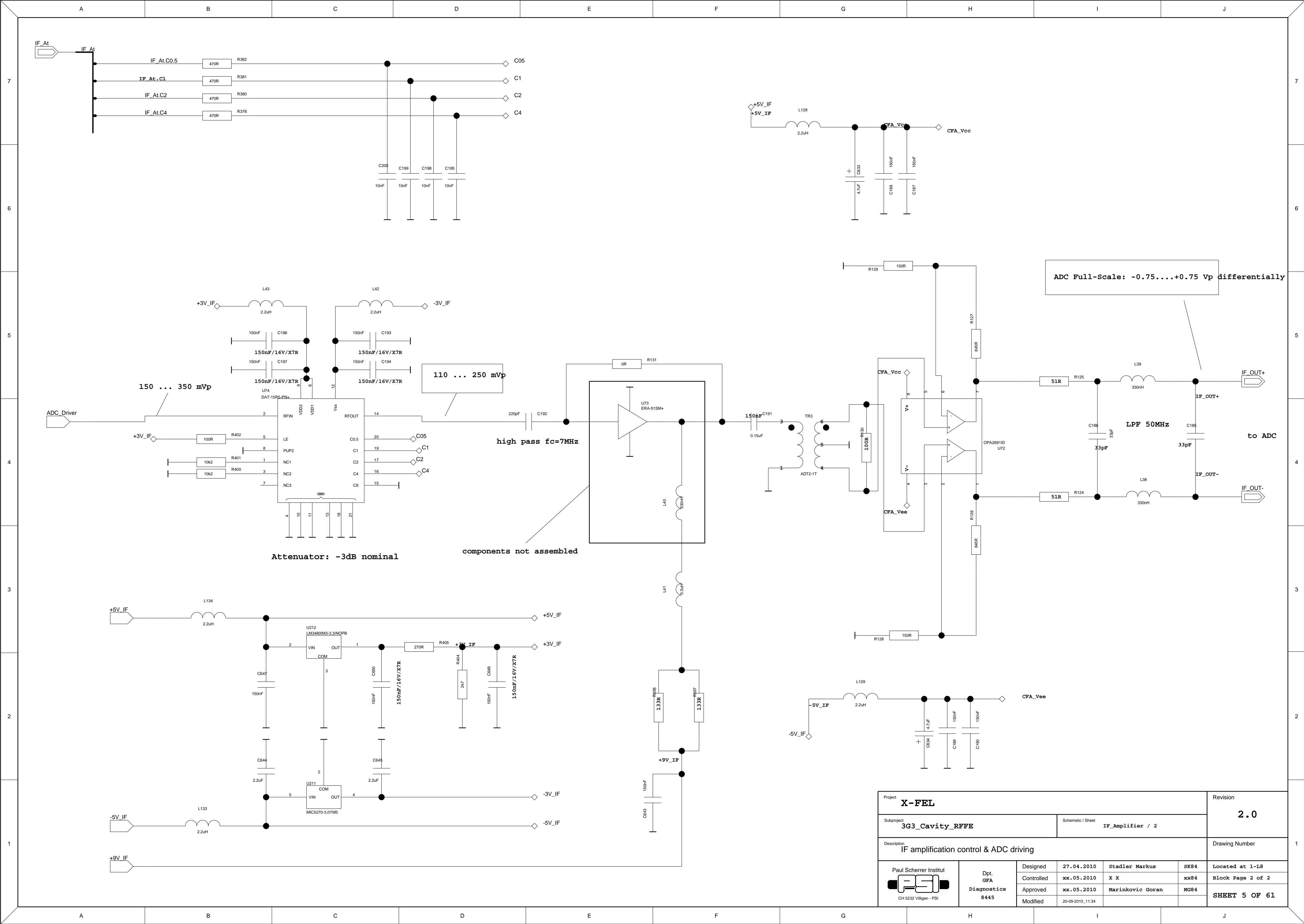


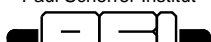
Project X-FEL						Revision 2.0
Subproject 3G3_Cavity_RFFE			Schematic / Sheet IF_Amplifier / 1			
Description First IF-Amplification stage (LNA)						Drawing Number
<div>Paul Scherrer Institut</div> <div></div> <div>CH 5232 Villigen - PSI</div>	<div>Dpt. GFA</div> <div>Diagnostics</div> <div>8445</div>	Designed	27.04.2010	Stadler Markus	SK84	Located at 1-L9
		Controlled	xx.05.2010	X X	xx84	Block Page 1 of 2
		Approved	xx.05.2010	Marinkovic Goran	MG84	SHEET 2 OF 61
		Modified	02-08-2010_16:20			

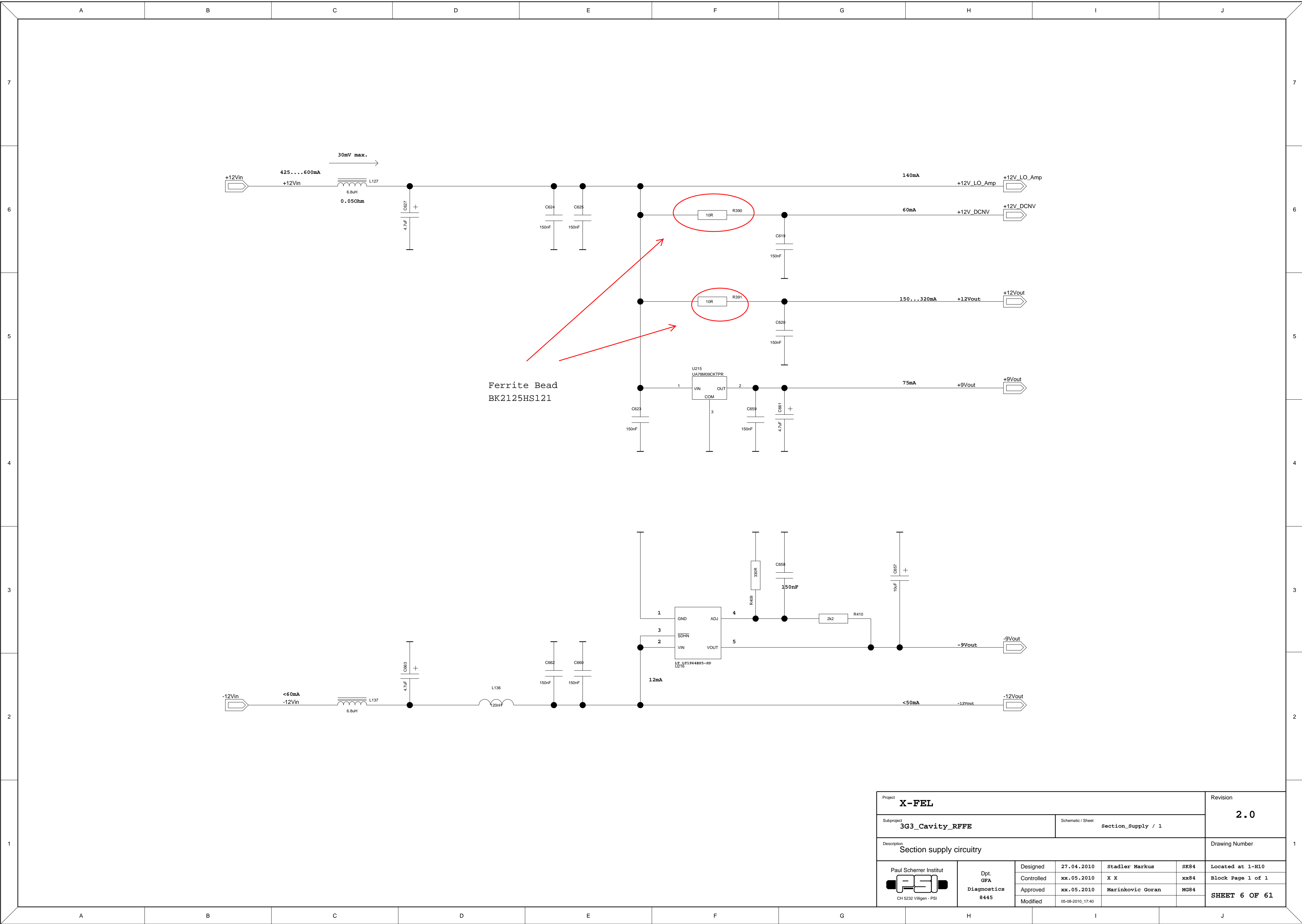



Project X-FEL						Revision 2.0		
Subproject 3G3_Cavity_RFFE			Schematic / Sheet IF_Amplifier / 2					
Description IF amplification control & ADC driving						Drawing Number		
<div>Paul Scherrer Institut</div> <div></div> <div>CH 5232 Villigen - PSI</div>		<div>Dpt. GFA</div> <div>Diagnostics</div> <div>8445</div>		Designed	27.04.2010	Stadler Markus	SK84	Located at 1-L9
				Controlled	xx.05.2010	X X	xx84	Block Page 2 of 2
				Approved	xx.05.2010	Marinkovic Goran	MG84	SHEET 3 OF 61
				Modified	20-09-2010_11:34			

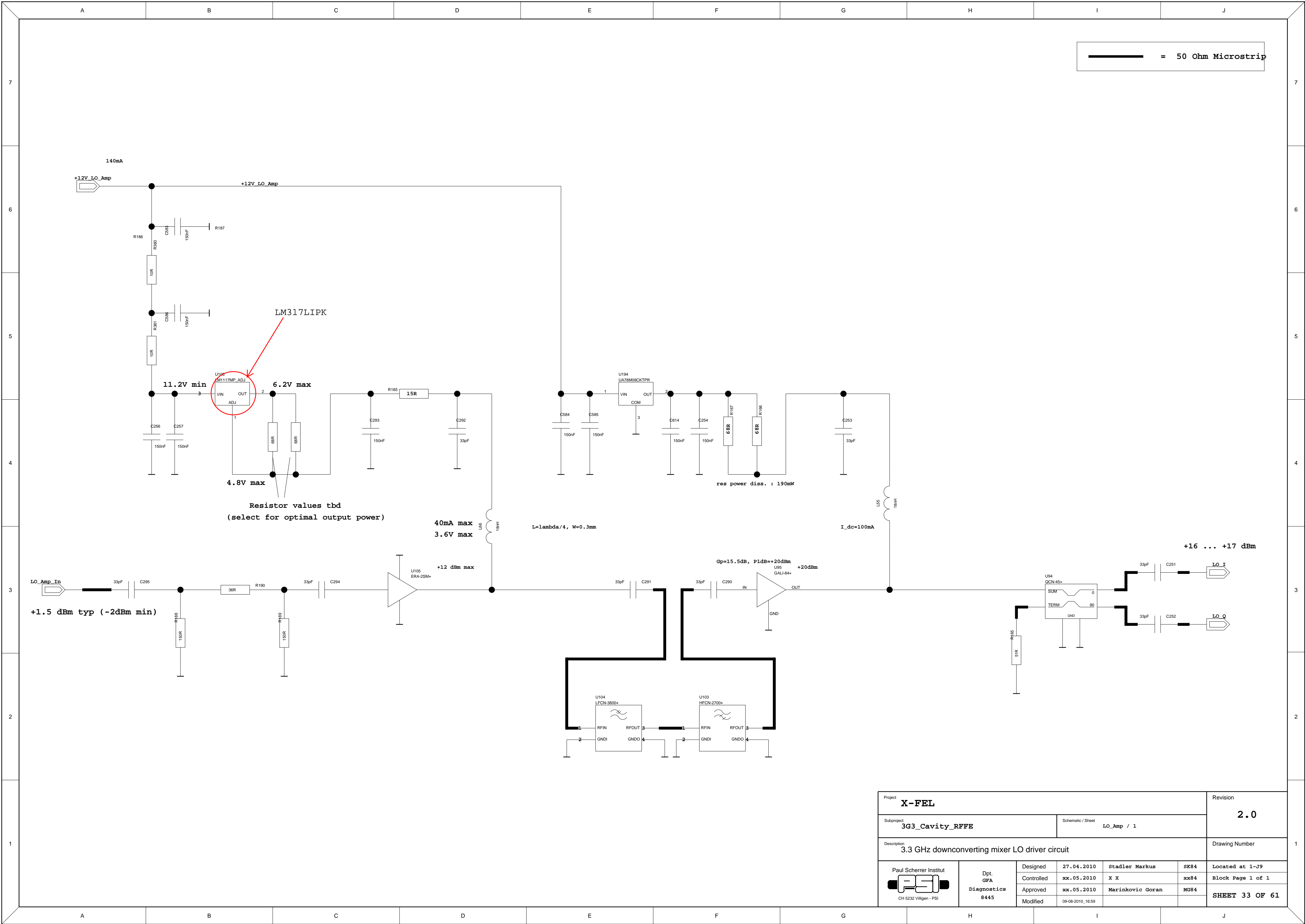


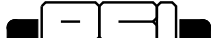


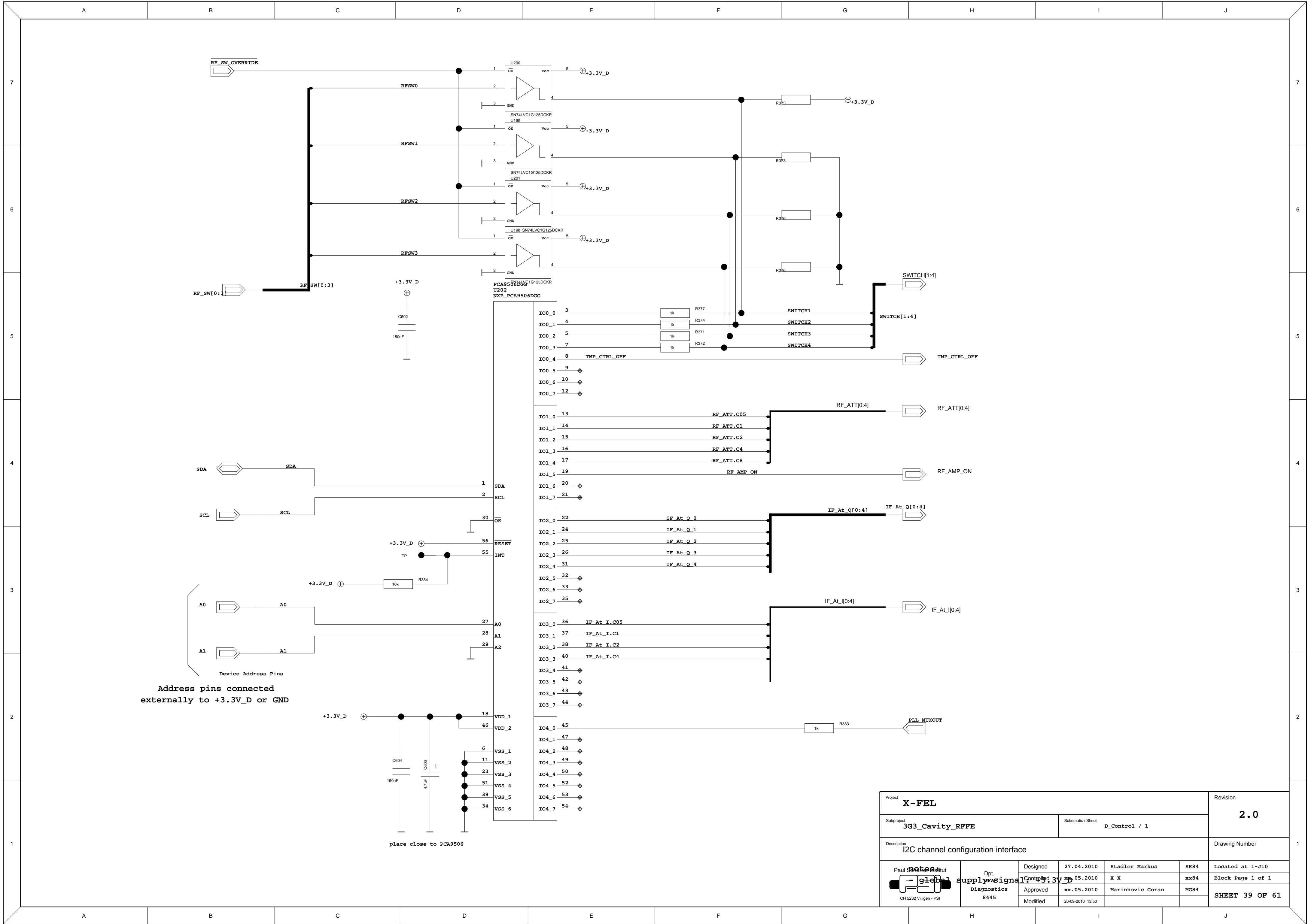
Project X-FEL						Revision 2.0		
Subproject 3G3_Cavity_RFFE			Schematic / Sheet IF_Amplifier / 2					
Description IF amplification control & ADC driving						Drawing Number		
<div>Paul Scherrer Institut</div> <div></div> <div>CH 5232 Villigen - PSI</div>		<div>Dpt. GFA</div> <div>Diagnostics</div> <div>8445</div>		Designed	27.04.2010	Stadler Markus	SK84	Located at 1-L8
				Controlled	xx.05.2010	X X	xx84	Block Page 2 of 2
				Approved	xx.05.2010	Marinkovic Goran	MG84	SHEET 5 OF 61
				Modified	20-09-2010_11:34			

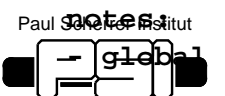


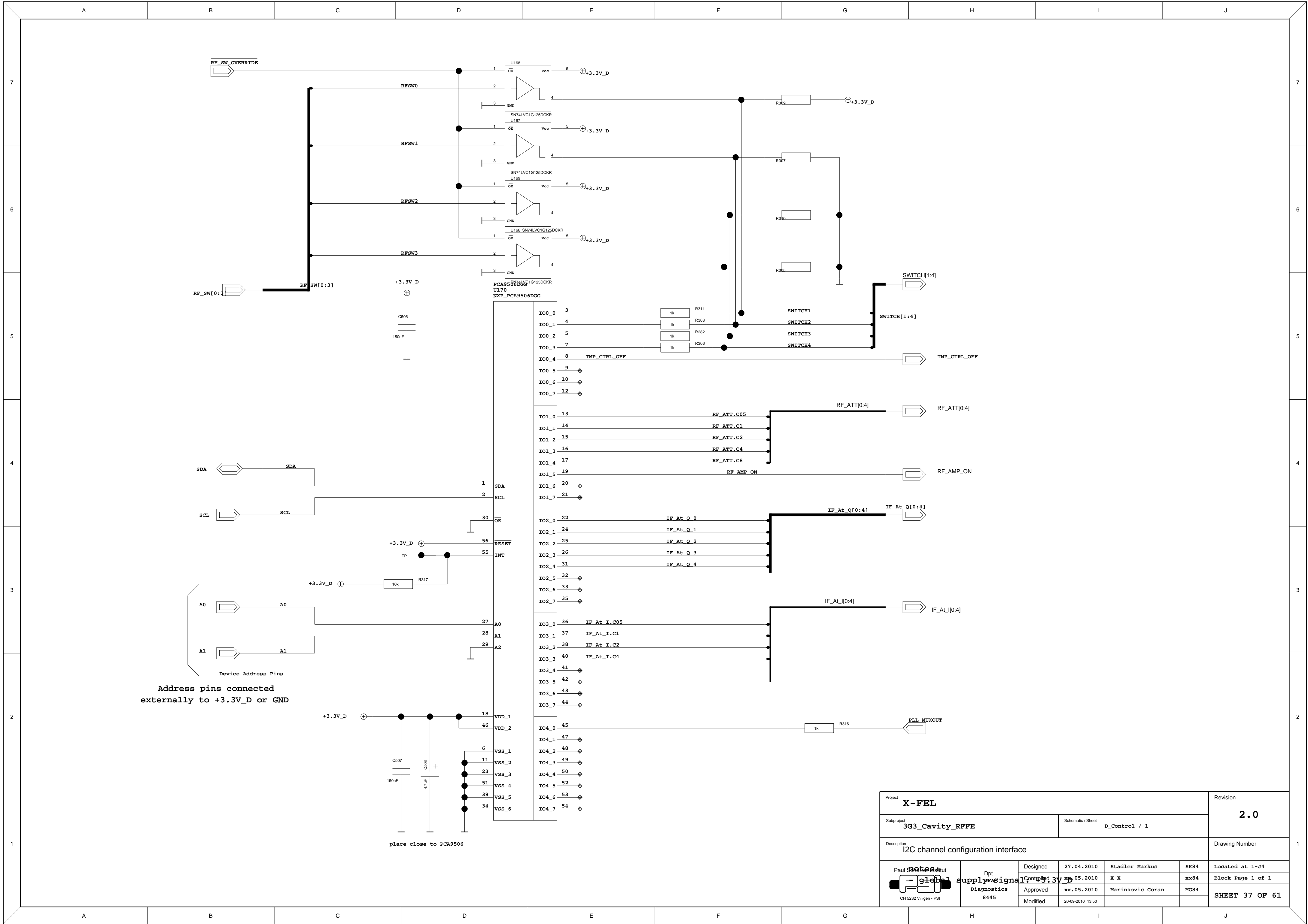
Project X-FEL						Revision 2.0
Subproject 3G3_Cavity_RFFE			Schematic / Sheet Section_Supply / 1			
Description Section supply circuitry						Drawing Number
<div>Paul Scherrer Institut</div> <div></div> <div>CH 5232 Villigen - PSI</div>	Dpt. GFA Diagnostics 8445	Designed	27.04.2010	Stadler Markus	SK84	Located at 1-H10
		Controlled	xx.05.2010	X X	xx84	Block Page 1 of 1
		Approved	xx.05.2010	Marinkovic Goran	MG84	SHEET 6 OF 61
		Modified	05-08-2010_17:40			



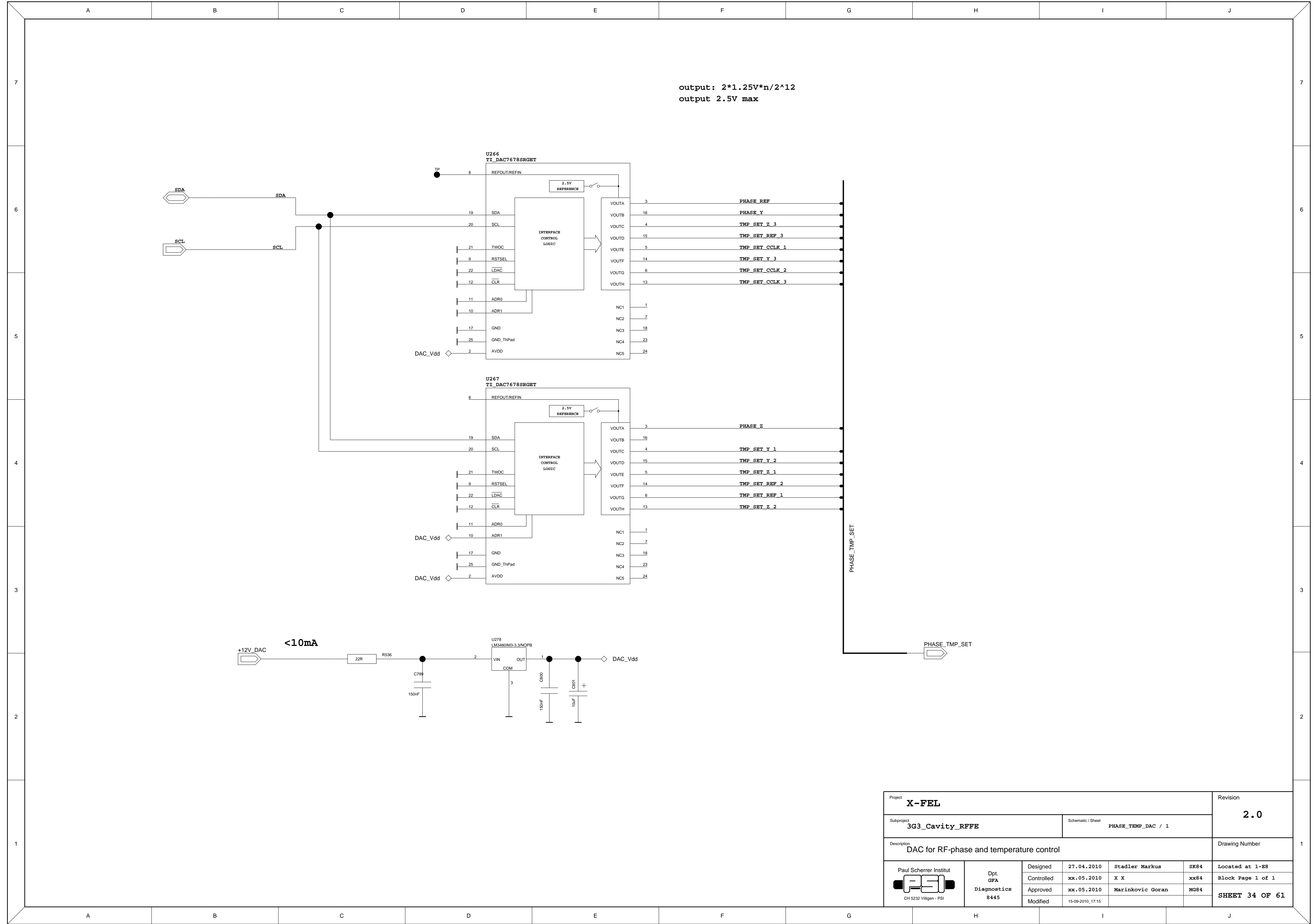
Project X-FEL						Revision 2.0
Subproject 3G3_Cavity_RFFE			Schematic / Sheet LO_Amp / 1			
Description 3.3 GHz downconverting mixer LO driver circuit						Drawing Number
<div>Paul Scherrer Institut</div> <div></div> <div>CH 5232 Villigen - PSI</div>	<div>Dpt. GFA</div> <div>Diagnostics</div> <div>8445</div>	Designed	27.04.2010	Stadler Markus	SK84	Located at 1-J9
		Controlled	xx.05.2010	X X	xx84	Block Page 1 of 1
		Approved	xx.05.2010	Marinkovic Goran	MG84	SHEET 33 OF 61
		Modified	09-08-2010_16:59			




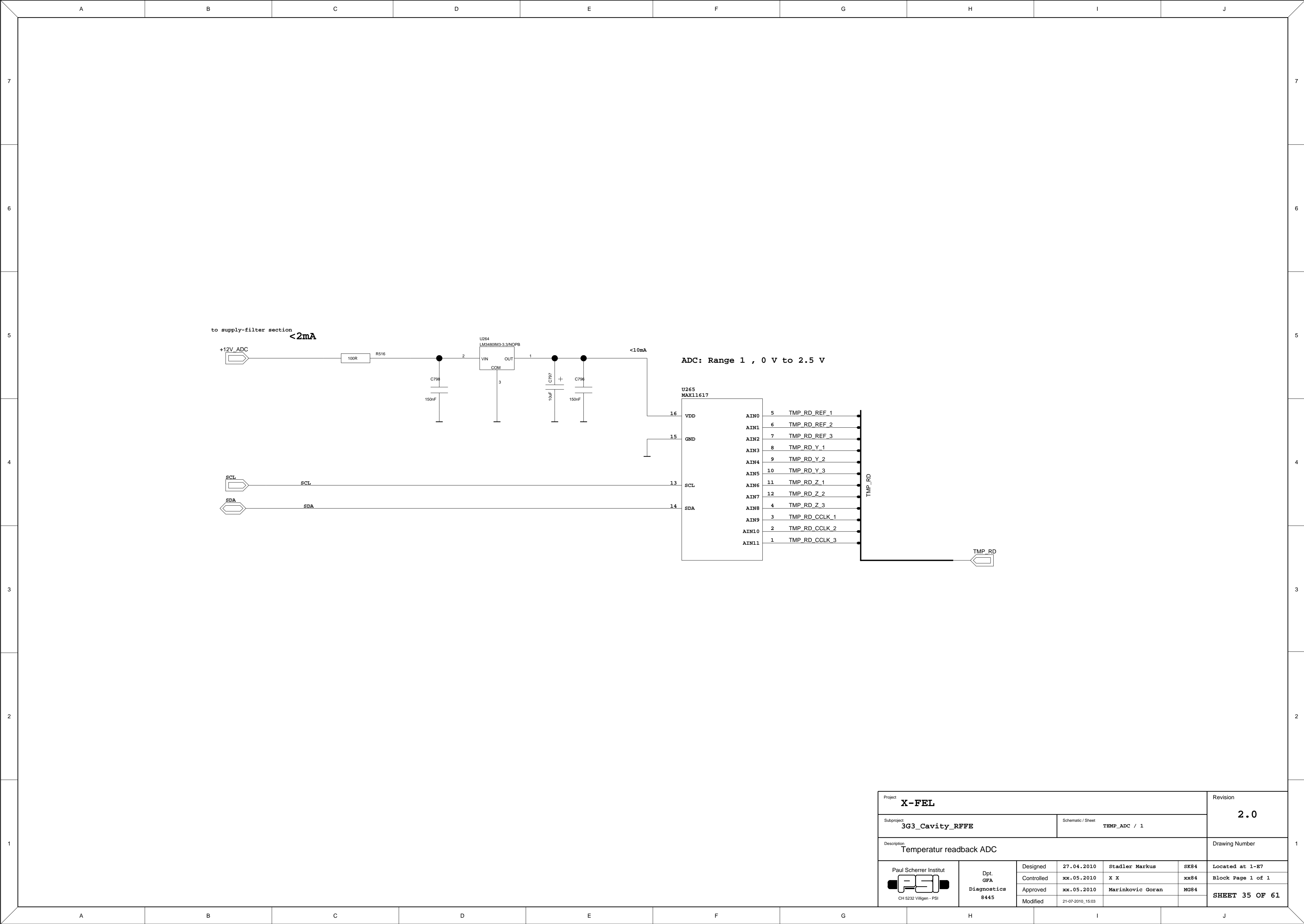
Project X-FEL						Revision 2.0	
Subproject 3G3_Cavity_RFPE			Schematic / Sheet D_Control / 1				
Description I2C channel configuration interface						Drawing Number	
 CH 5232 Villigen - PSI		Dpt. Diagnostics 8445	Designed	27.04.2010	Stadler Markus	SK84	Located at 1-J10
			Controlled	xx.05.2010	X X	xx84	Block Page 1 of 1
			Approved	xx.05.2010	Marinkovic Goran	MG84	SHEET 39 OF 61
			Modified		20-09-2010_13:50		




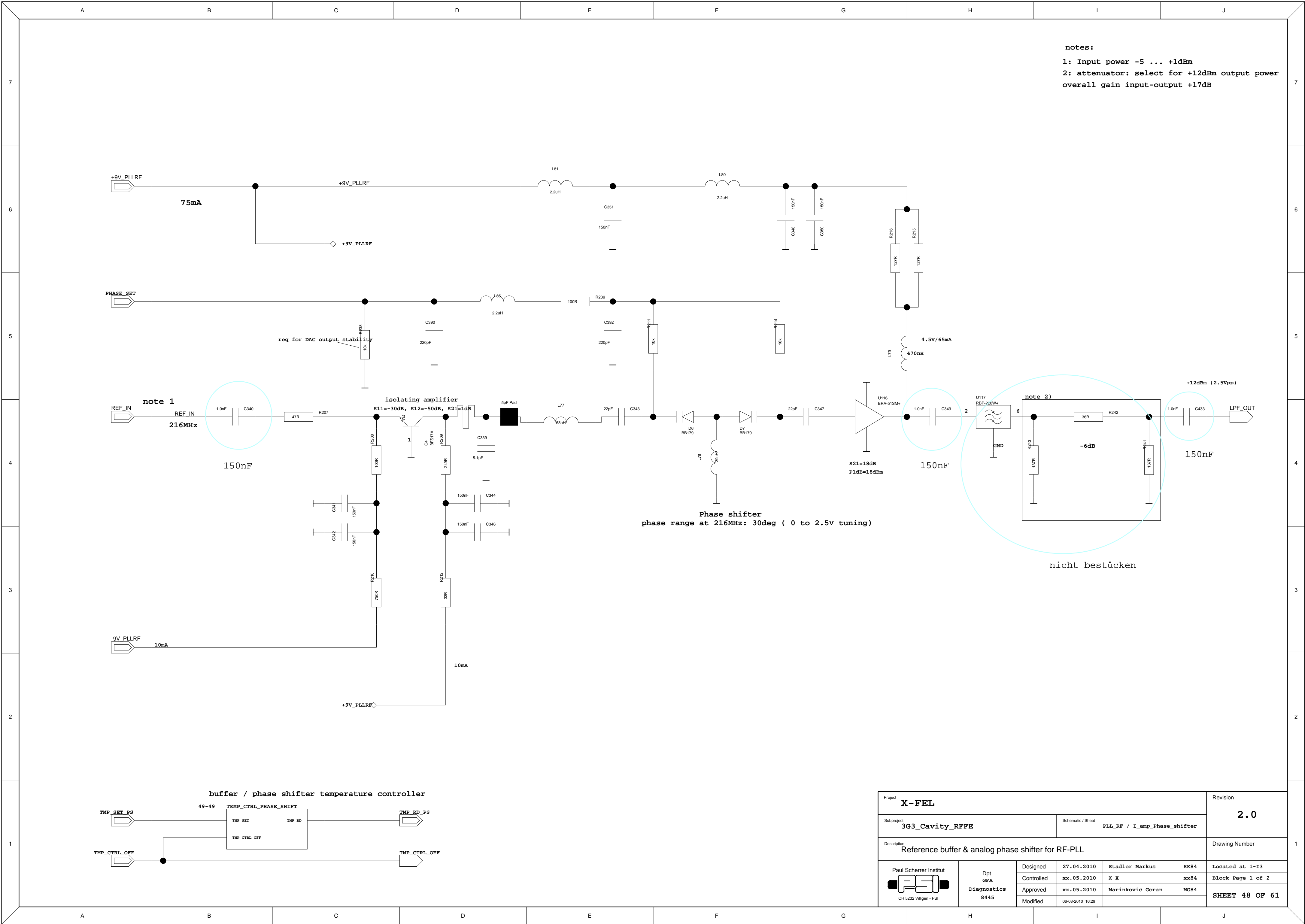
Project X-FEL						Revision 2.0	
Subproject 3G3_Cavity_RFPE				Schematic / Sheet D_Control / 1			
Description I2C channel configuration interface						Drawing Number	
Designed 27.04.2010 Stadler Markus SK84		Controlled xx.05.2010 X X		Approved xx.05.2010 Marinkovic Goran MG84		Located at 1-J4 Block Page 1 of 1 SHEET 37 OF 61	
Modified 20-09-2010_13:50							

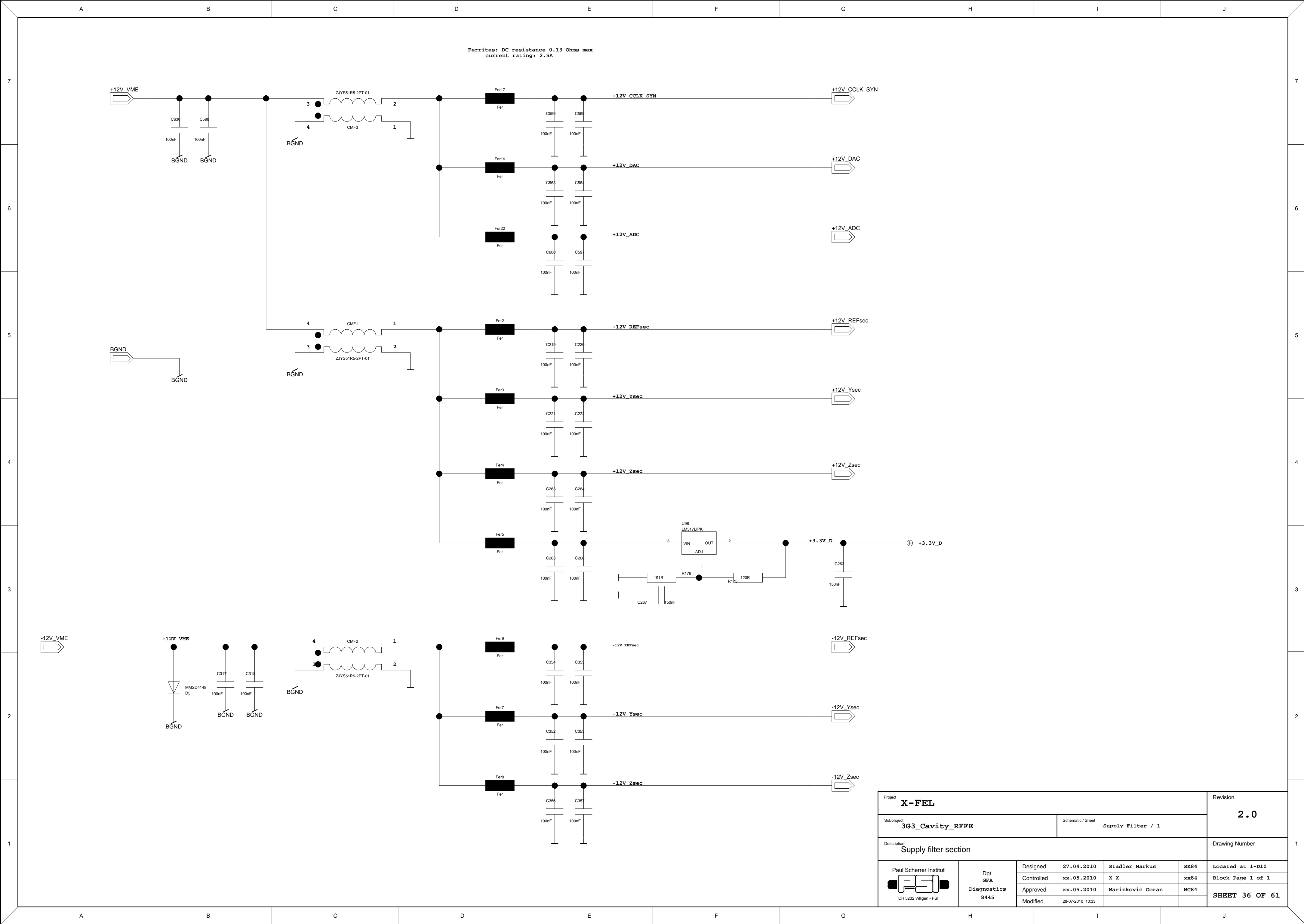



Project X-FEL						Revision 2.0
Subproject 3G3_Cavity_RFFE			Schematic / Sheet PHASE_TEMP_DAC / 1			
Description DAC for RF-phase and temperature control						Drawing Number
<div>Paul Scherrer Institut</div> <div></div> <div>CH 5232 Villigen - PSI</div>	Dpt. GFA Diagnostics 8445	Designed	27.04.2010	Stadler Markus	SK84	Located at 1-E8
		Controlled	xx.05.2010	X X	xx84	Block Page 1 of 1
		Approved	xx.05.2010	Marinkovic Goran	MG84	SHEET 34 OF 61
		Modified	15-09-2010_17:15			

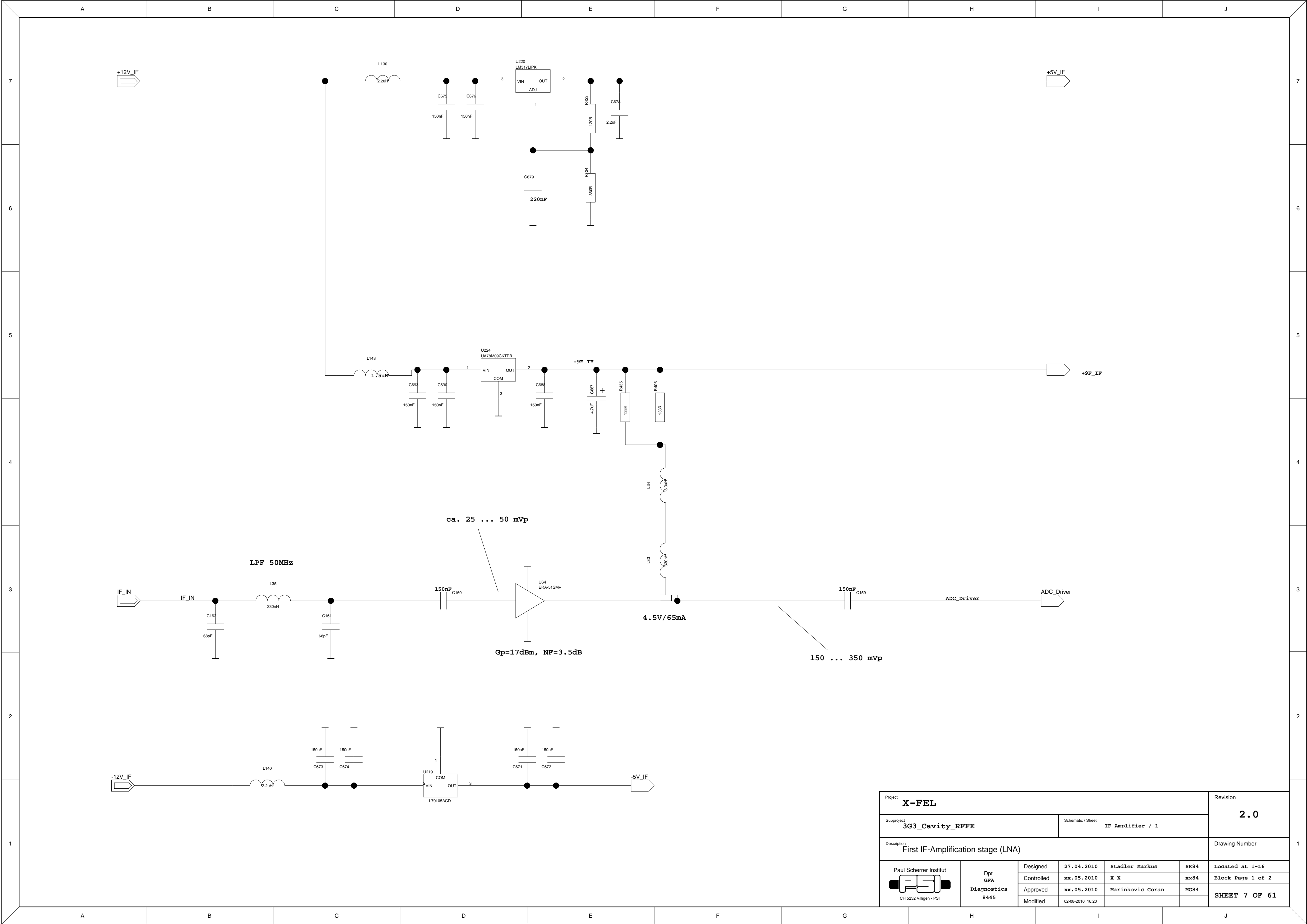



Project X-FEL						Revision 2.0
Subproject 3G3_Cavity_RFFE			Schematic / Sheet TEMP_ADC / 1			
Description Temperatur readback ADC						Drawing Number
<div>Paul Scherrer Institut</div> <div></div> <div>CH 5232 Villigen - PSI</div>	Dpt. GFA Diagnostics 8445	Designed	27.04.2010	Stadler Markus	SK84	Located at 1-E7
		Controlled	xx.05.2010	X X	xx84	Block Page 1 of 1
		Approved	xx.05.2010	Marinkovic Goran	MG84	SHEET 35 OF 61
		Modified	21-07-2010_15:03			

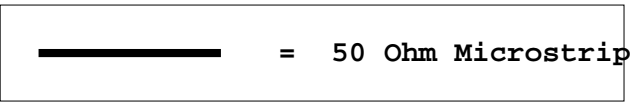





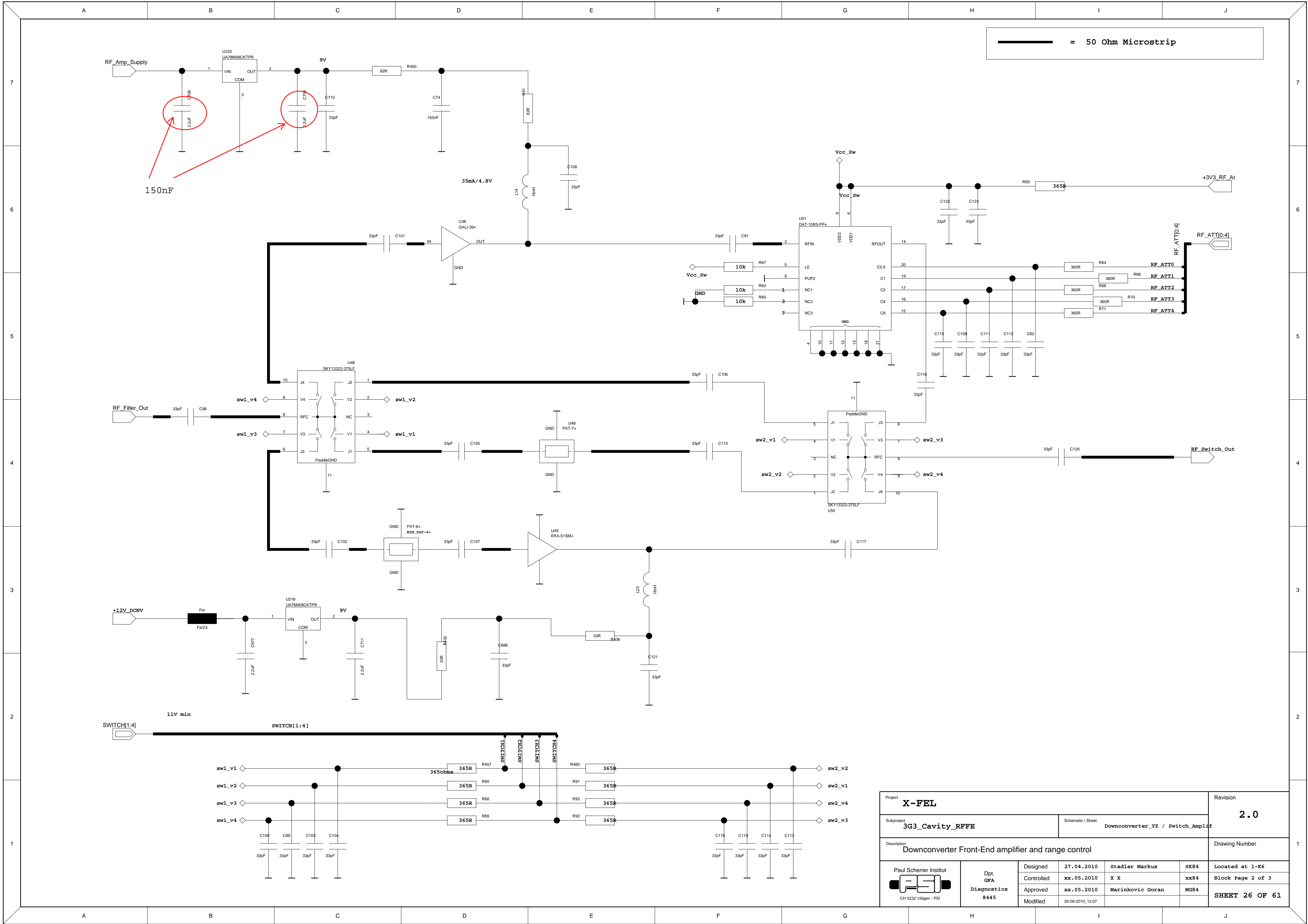
Project X-FEL						Revision 2.0		
Subproject 3G3_Cavity_RFFE				Schematic / Sheet Supply_Filter / 1				
Description Supply filter section						Drawing Number		
<div>Paul Scherrer Institut</div> <div></div> <div>CH 5232 Villigen - PSI</div>		<div>Dpt. GFA</div> <div>Diagnostics</div> <div>8445</div>		Designed	27.04.2010	Stadler Markus	SK84	Located at 1-D10
				Controlled	xx.05.2010	X X	xx84	Block Page 1 of 1
				Approved	xx.05.2010	Marinkovic Goran	MG84	SHEET 36 OF 61
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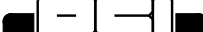


Project X-FEL						Revision 2.0	
Subproject 3G3_Cavity_RFFE				Schematic / Sheet IF_Amplifier / 1			
Description First IF-Amplification stage (LNA)						Drawing Number	
<div>Paul Scherrer Institut</div> <div></div> <div>CH 5232 Villigen - PSI</div>		<div>Dpt. GFA</div> <div>Diagnostics</div> <div>8445</div>	Designed	27.04.2010	Stadler Markus	SK84	Located at 1-L6
			Controlled	xx.05.2010	X X	xx84	Block Page 1 of 2
			Approved	xx.05.2010	Marinkovic Goran	MG84	SHEET 7 OF 61
			Modified		02-08-2010_16:20		




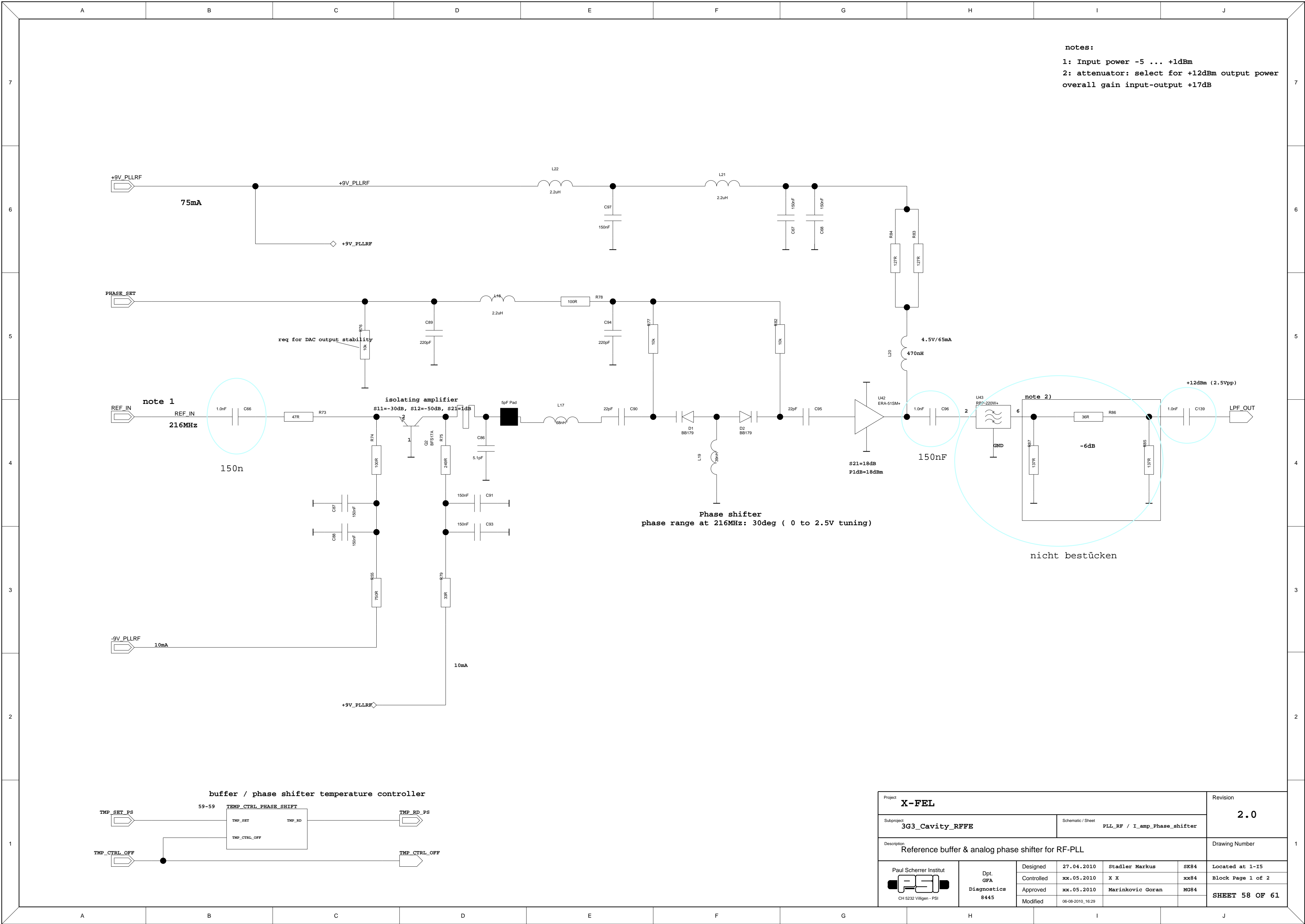
Project X-FEL					Revision 2.0			
Subproject 3G3_Cavity_RFFE			Schematic / Sheet Downconverter_YZ / Input_BPF					
Description Bandpass filter (fo=3.3GHz)					Drawing Number			
<div>Paul Scherrer Institut</div> <div></div> <div>CH 5232 Villigen - PSI</div>		<div>Dpt. GFA</div> <div>Diagnostics 8445</div>		Designed	27.04.2010	Stadler Markus	SK84	Located at 1-K6
				Controlled	xx.05.2010	X X	xx84	Block Page 1 of 3
				Approved	xx.05.2010	Marinkovic Goran	MG84	SHEET 25 OF 61
				Modified	23-09-2010, 14:21			

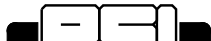


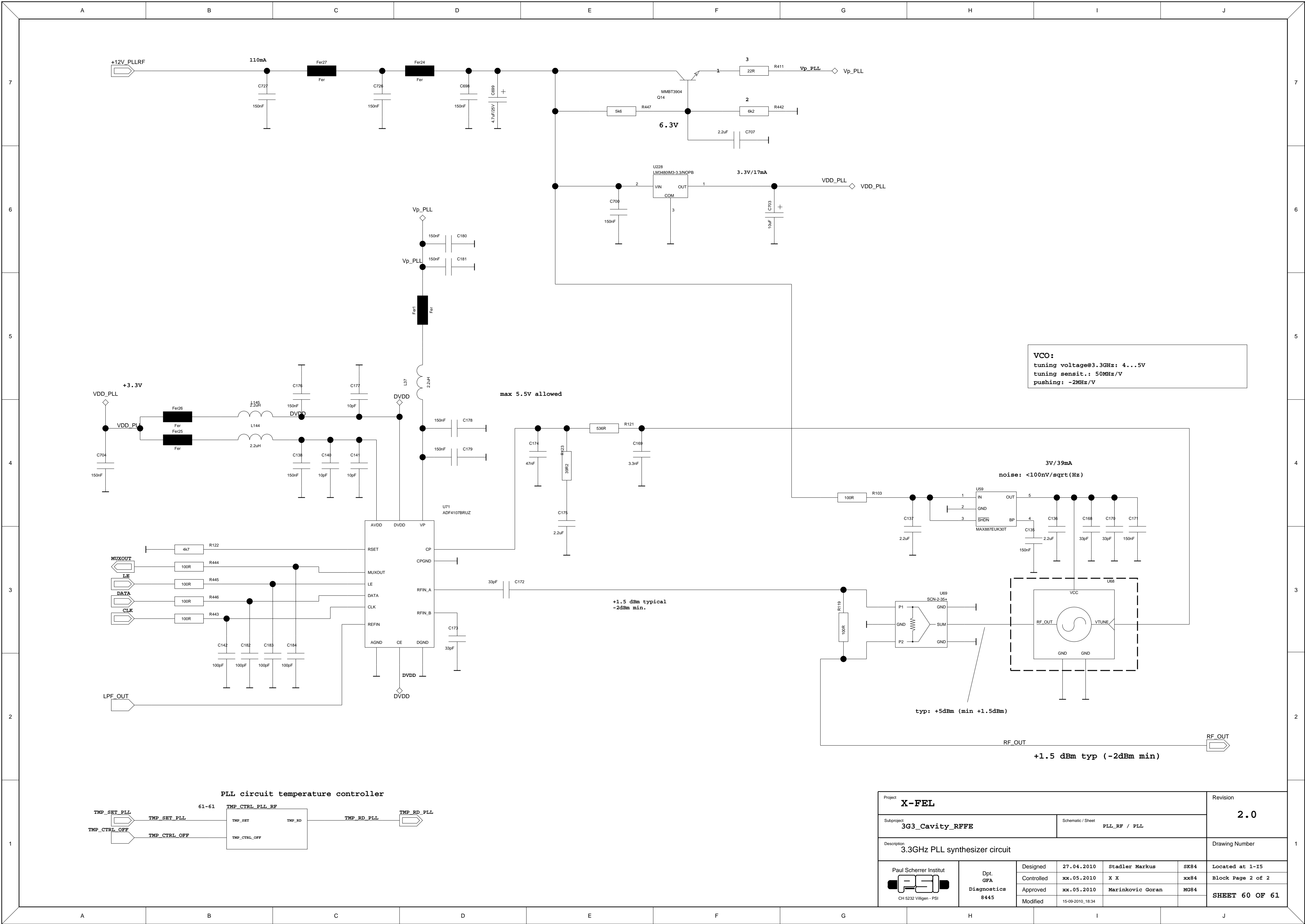
Project X-FEL						Revision	
Subproject 3G3_Cavity_RFFE				Schematic / Sheet Downconverter_YZ / Switch_Amplif		2.0	
Description Downconverter Front-End amplifier and range control						Drawing Number	
<div>Paul Scherrer Institut</div> <div></div> <div>CH 5232 Villigen - PSI</div>		<div>Dpt. GFA</div> <div>Diagnostics</div> <div>8445</div>	Designed	27.04.2010	Stadler Markus	SK84	Located at 1-K6
			Controlled	xx.05.2010	X X	xx84	Block Page 2 of 3
			Approved	xx.05.2010	Marinkovic Goran	MG84	SHEET 26 OF 61
			Modified	20-09-2010_12:07			

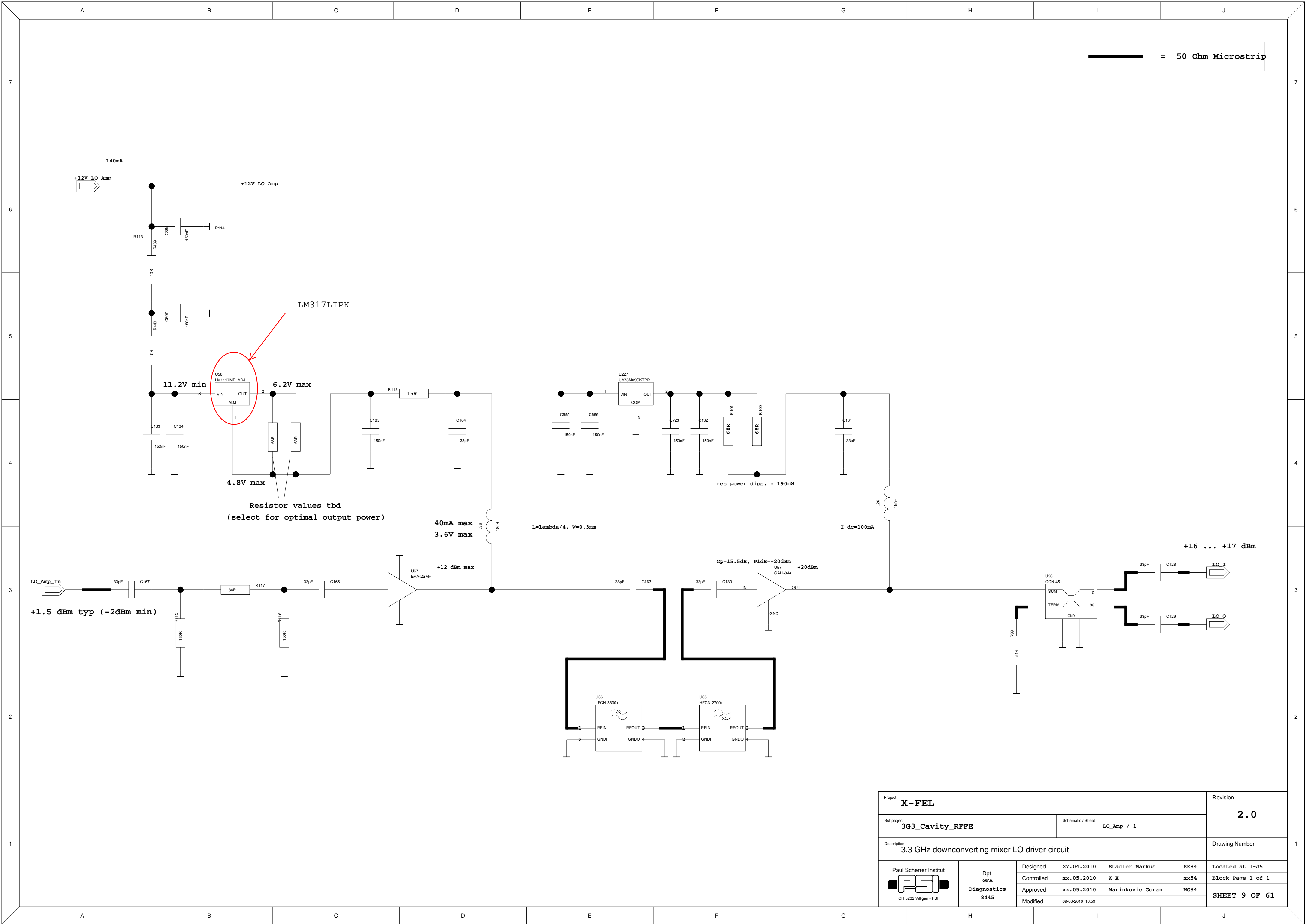
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6											6
5											5
4											4
3											3
2											2
1											1
	A	B	C	D	E	F	G	H	I	J	

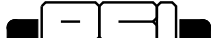
Project X-FEL						Revision 2.0	
Subproject 3G3_Cavity_RFFE				Schematic / Sheet			
Description						Drawing Number	
<div>Paul Scherrer Institut</div> <div></div> <div>CH 5232 Villigen · PSI</div>		<div>Dpt. GFA</div> <div>Diagnostics</div> <div>8445</div>		Designed	27.04.2010	Stadler Markus	SK84
				Controlled	xx.05.2010	X X	xx84
				Approved	xx.05.2010	Marinkovic Goran	MG84
				Modified	10-08-2010_14:09		

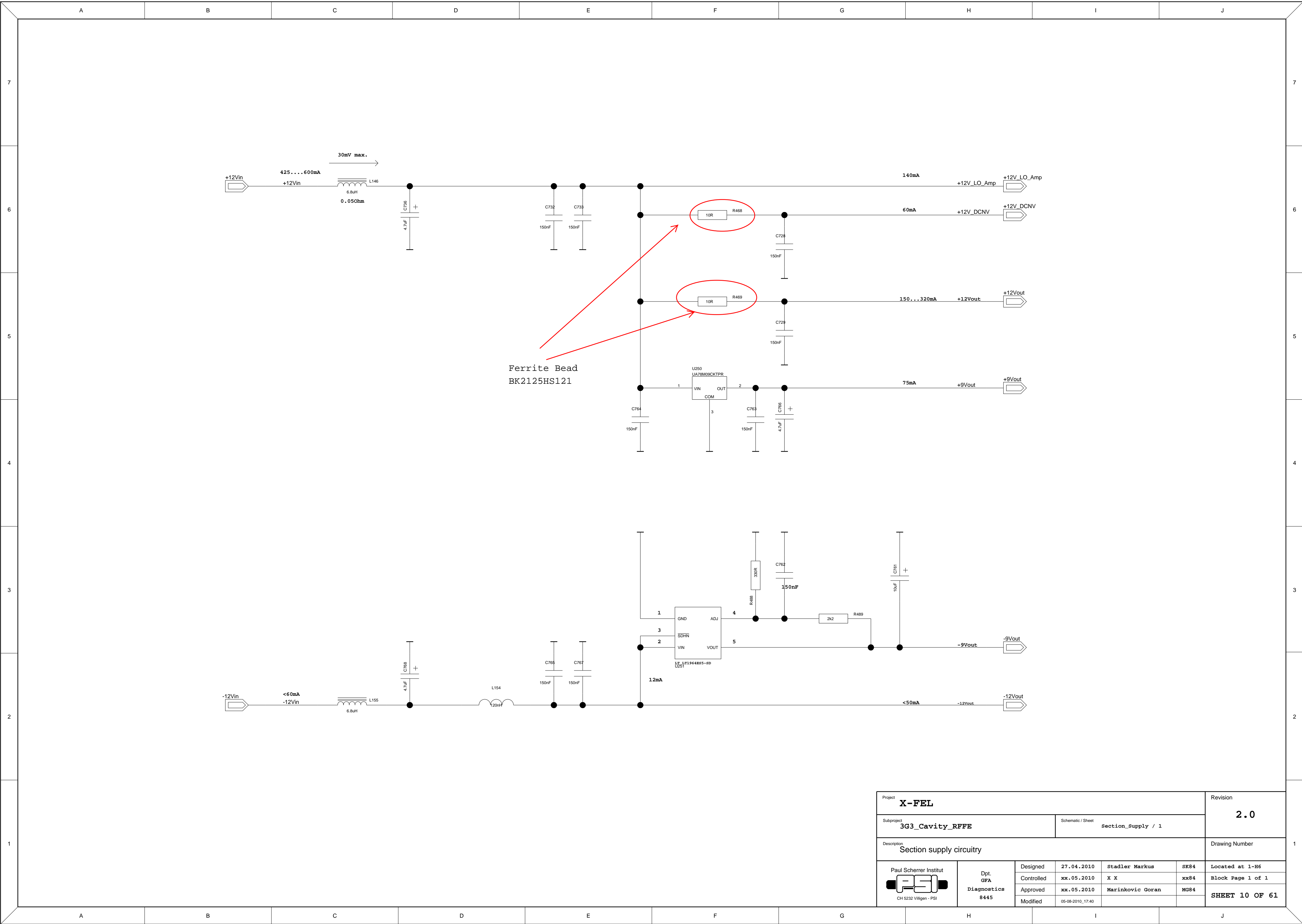


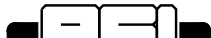
Project X-FEL						Revision 2.0		
Subproject 3G3_Cavity_RFFE			Schematic / Sheet PLL_RF / I_amp_Phase_shifter			Drawing Number		
Description Reference buffer & analog phase shifter for RF-PLL								
<div>Paul Scherrer Institut</div> <div></div> <div>CH 5232 Villigen - PSI</div>		<div>Dpt. GFA</div> <div>Diagnostics</div> <div>8445</div>		Designed	27.04.2010	Stadler Markus	SK84	Located at 1-I5
				Controlled	xx.05.2010	X X	xx84	Block Page 1 of 2
				Approved	xx.05.2010	Marinkovic Goran	MG84	SHEET 58 OF 61
				Modified	06-08-2010_16:29			

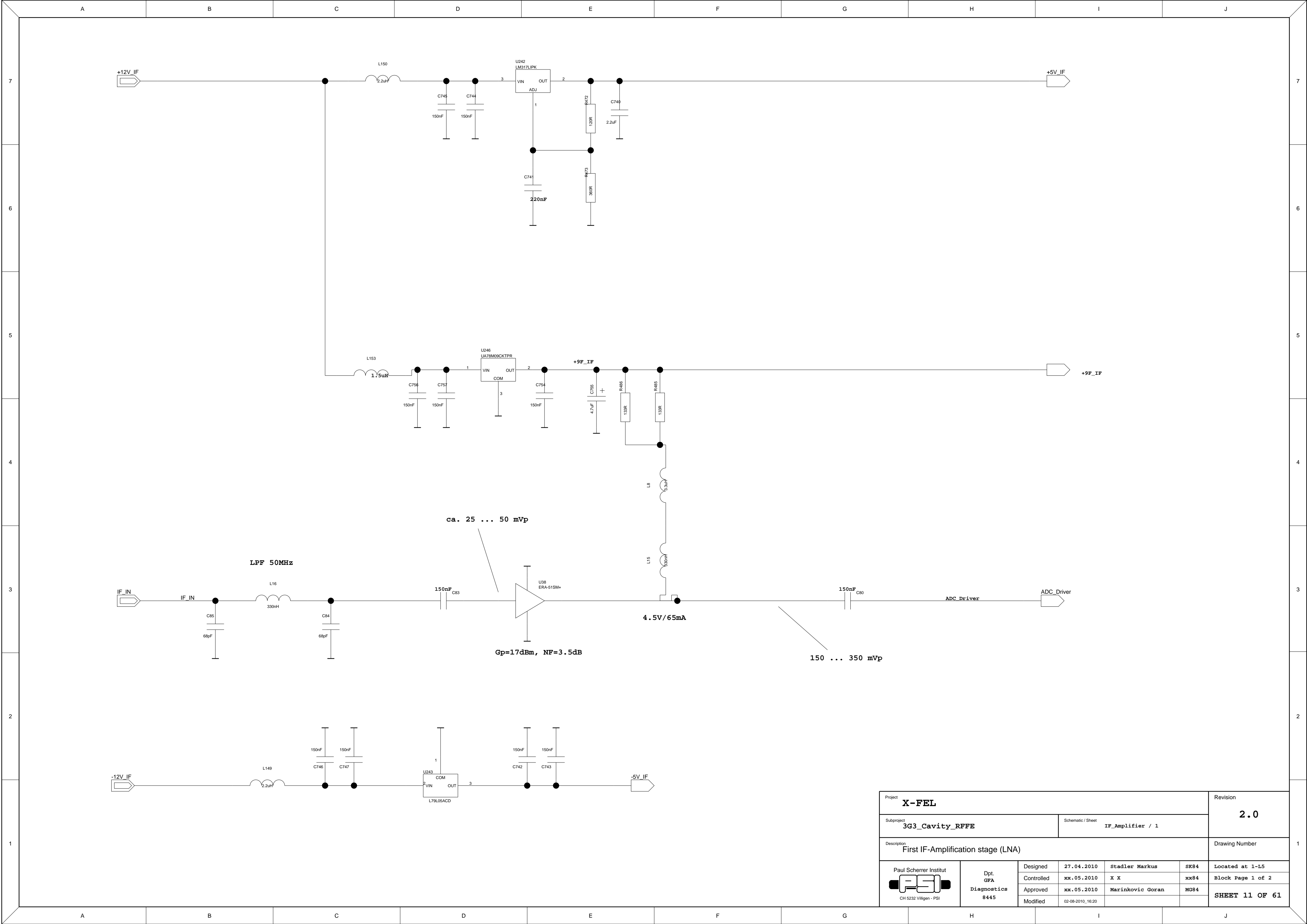


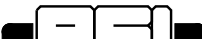


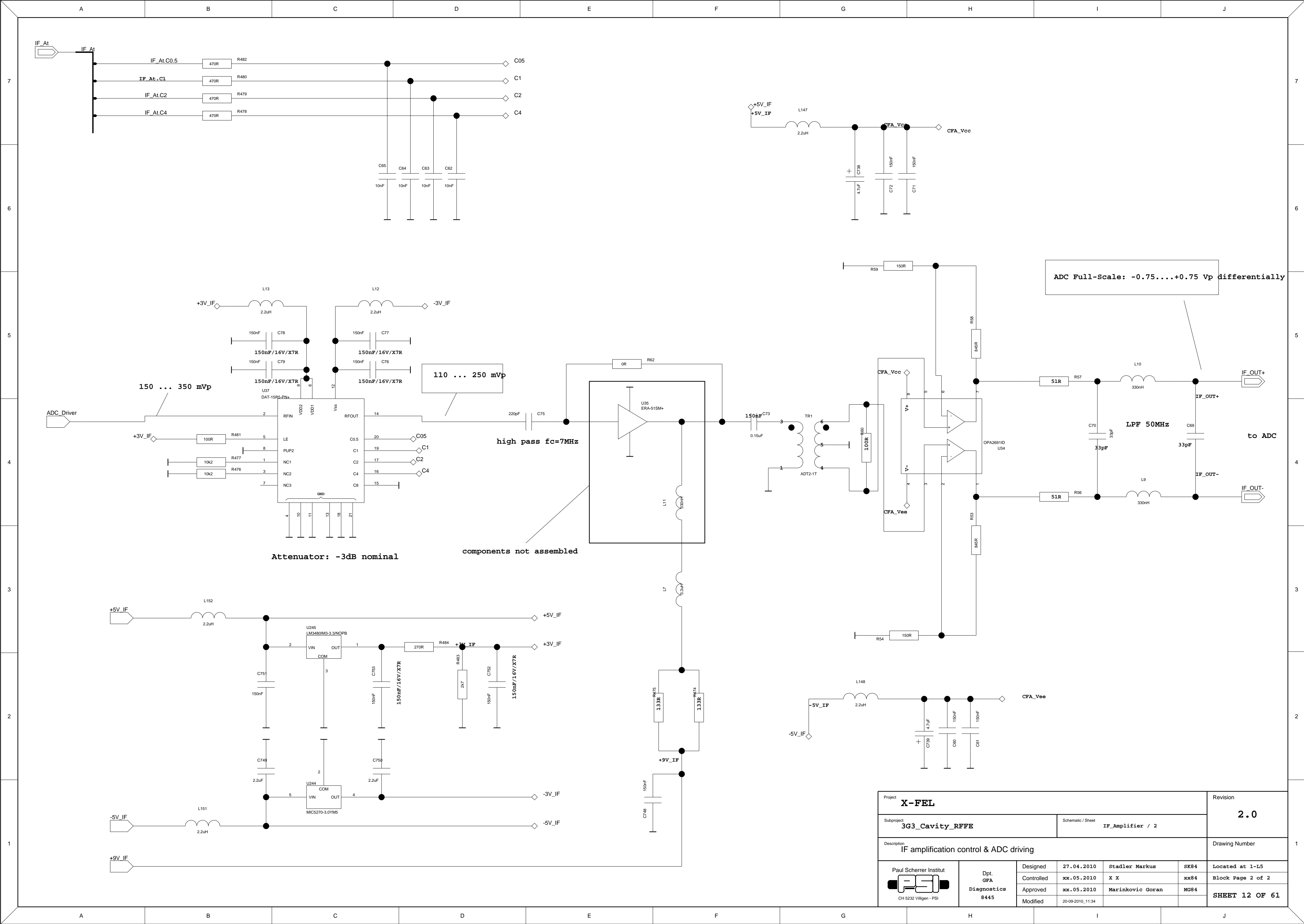
Project X-FEL						Revision 2.0		
Subproject 3G3_Cavity_RFFE			Schematic / Sheet LO_Amp / 1					
Description 3.3 GHz downconverting mixer LO driver circuit						Drawing Number		
<div>Paul Scherrer Institut</div> <div></div> <div>CH 5232 Villigen - PSI</div>		<div>Dpt. GFA</div> <div>Diagnostics</div> <div>8445</div>		Designed	27.04.2010	Stadler Markus	SK84	Located at 1-J5
				Controlled	xx.05.2010	X X	xx84	Block Page 1 of 1
				Approved	xx.05.2010	Marinkovic Goran	MG84	SHEET 9 OF 61
				Modified	09-08-2010_16:59			

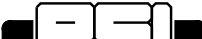


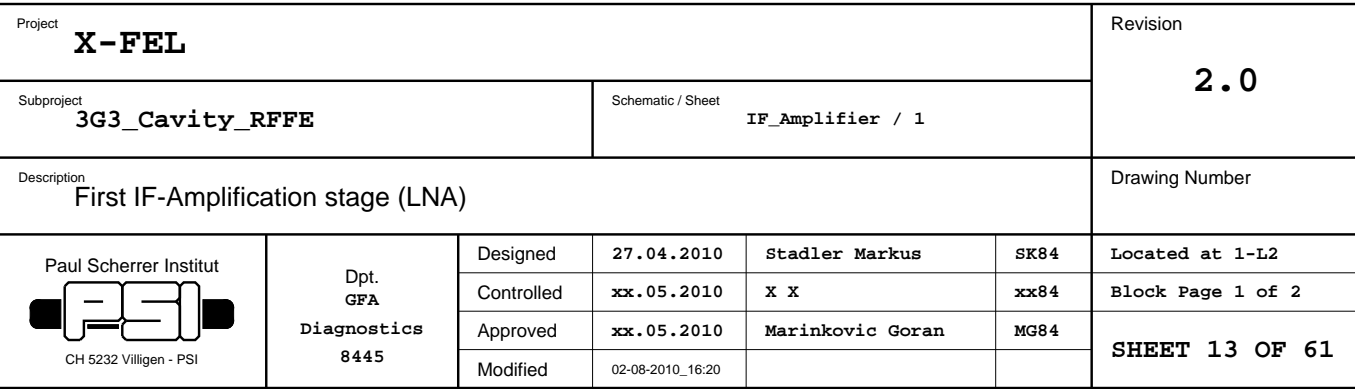
Project X-FEL						Revision 2.0
Subproject 3G3_Cavity_RFFE			Schematic / Sheet Section_Supply / 1			
Description Section supply circuitry						Drawing Number
<div>Paul Scherrer Institut</div> <div></div> <div>CH 5232 Villigen - PSI</div>	<div>Dpt. GFA</div> <div>Diagnostics</div> <div>8445</div>	Designed	27.04.2010	Stadler Markus	SK84	Located at 1-H6
		Controlled	xx.05.2010	X X	xx84	Block Page 1 of 1
		Approved	xx.05.2010	Marinkovic Goran	MG84	SHEET 10 OF 61
Modified		05-08-2010_17:40				

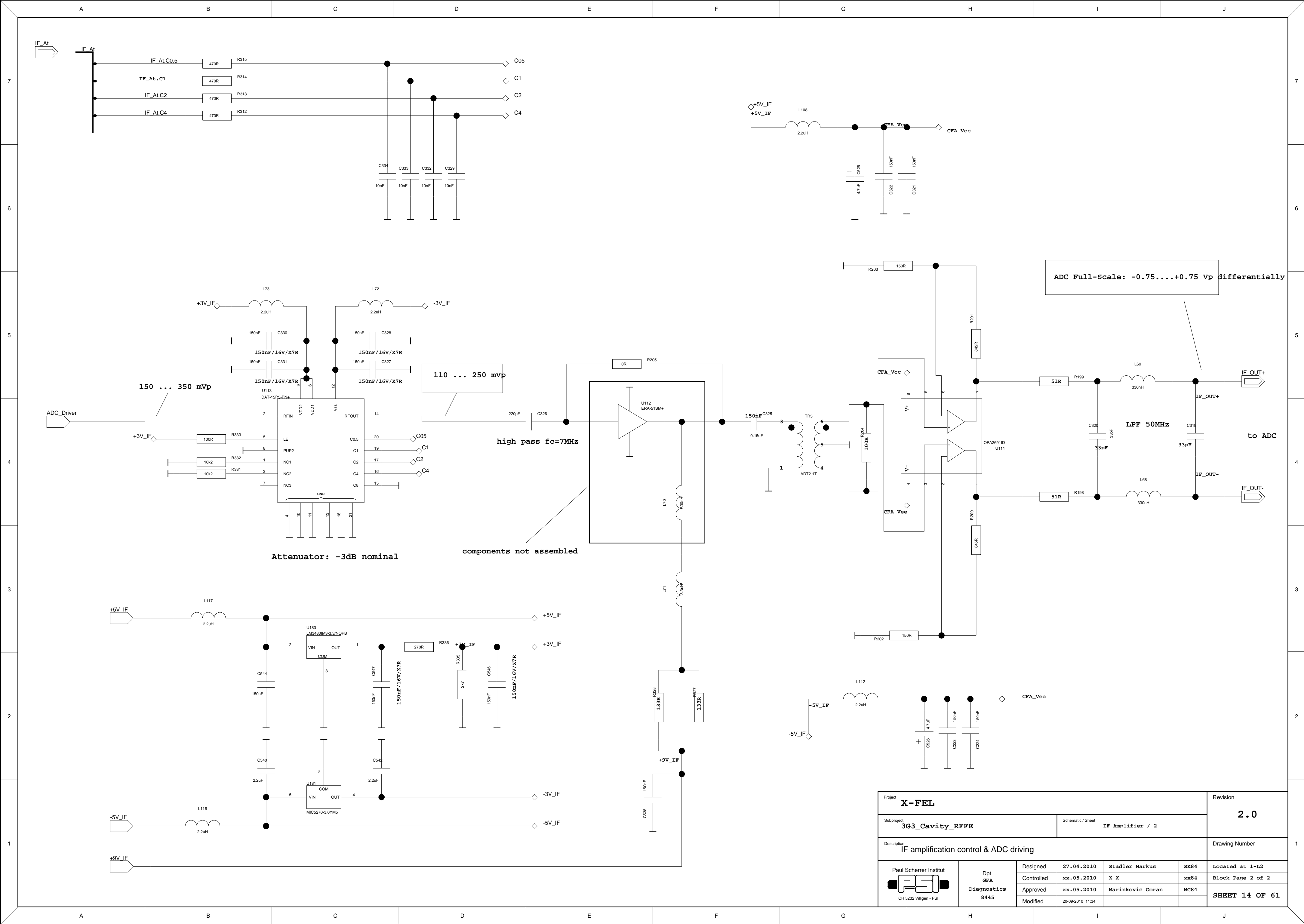


Project X-FEL						Revision 2.0	
Subproject 3G3_Cavity_RFFE			Schematic / Sheet IF_Amplifier / 1				
Description First IF-Amplification stage (LNA)						Drawing Number	
<div>Paul Scherrer Institut</div> <div></div> <div>CH 5232 Villigen - PSI</div>	<div>Dpt. GFA</div> <div>Diagnostics</div> <div>8445</div>	Designed	27.04.2010	Stadler Markus	SK84	Located at 1-L5	
		Controlled	xx.05.2010	X X	xx84	Block Page 1 of 2	
		Approved	xx.05.2010	Marinkovic Goran	MG84	SHEET 11 OF 61	
		Modified	02-08-2010_16:20				




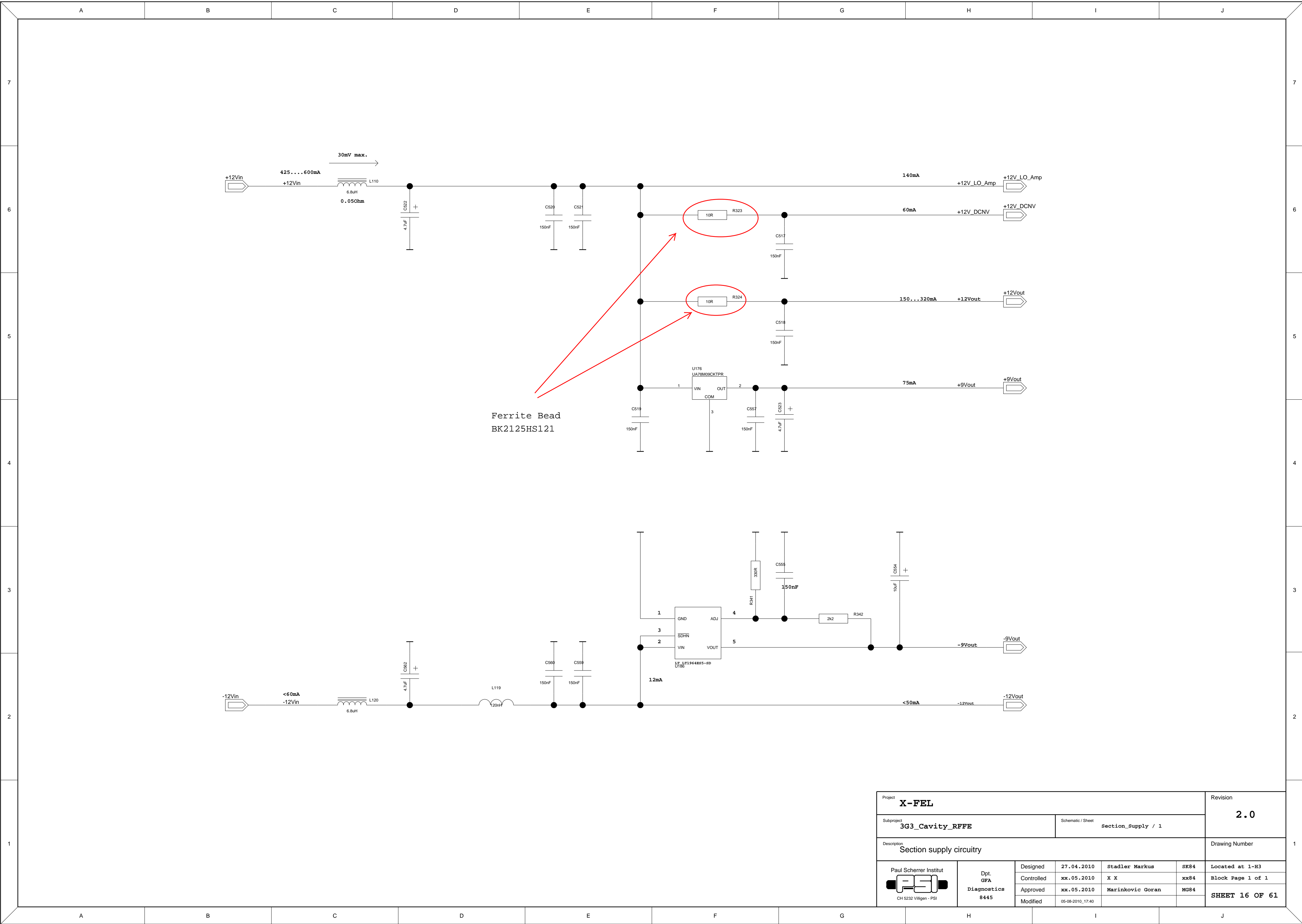
Project X-FEL						Revision 2.0		
Subproject 3G3_Cavity_RFFE			Schematic / Sheet IF_Amplifier / 2					
Description IF amplification control & ADC driving						Drawing Number		
<div>Paul Scherrer Institut</div> <div></div> <div>CH 5232 Villigen - PSI</div>		<div>Dpt. GFA</div> <div>Diagnostics</div> <div>8445</div>		Designed	27.04.2010	Stadler Markus	SK84	Located at 1-L5
				Controlled	xx.05.2010	X X	xx84	Block Page 2 of 2
				Approved	xx.05.2010	Marinkovic Goran	MG84	SHEET 12 OF 61
				Modified	20-09-2010_11:34			

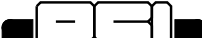


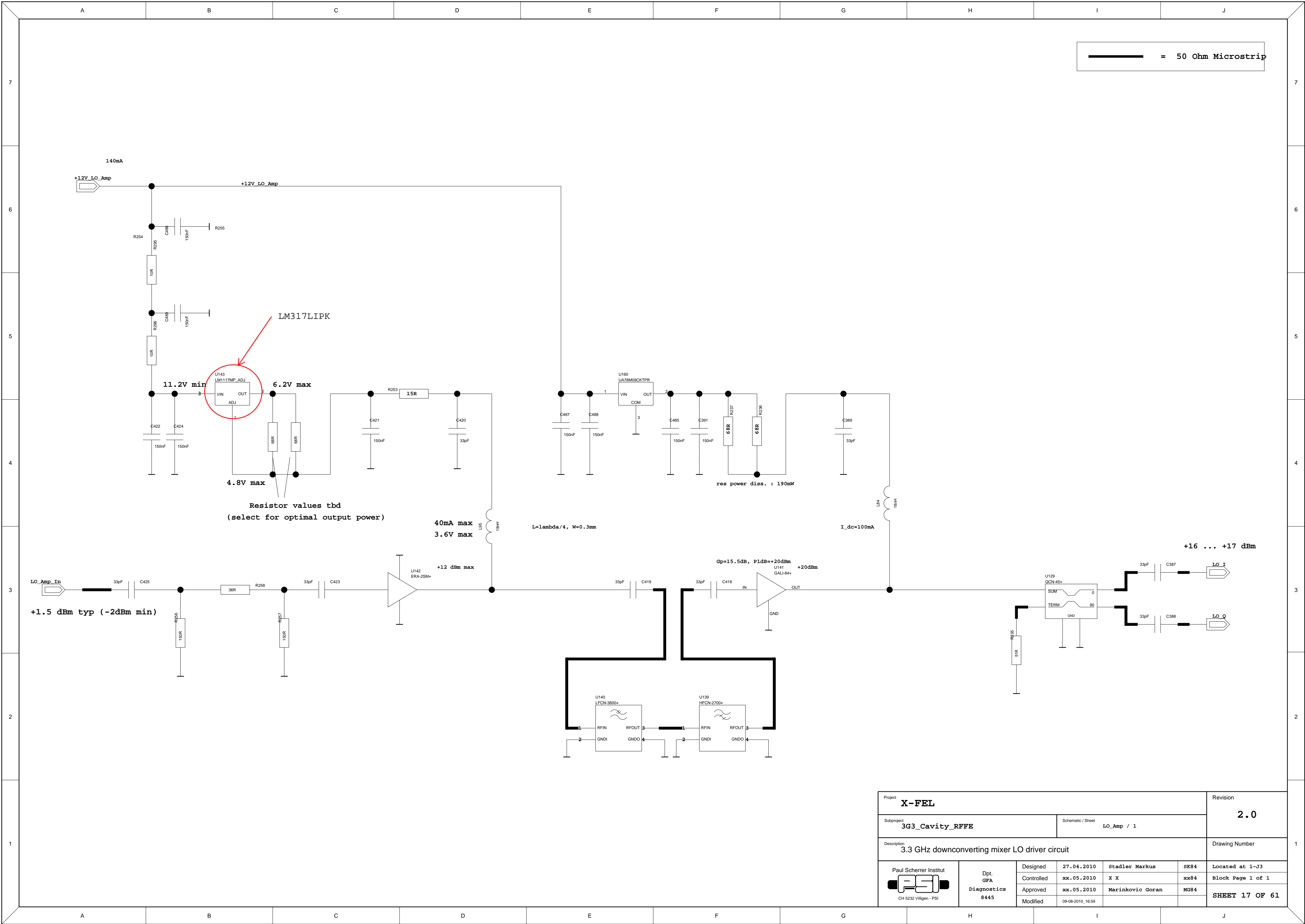



ADC Full-Scale: -0.75....+0.75 Vp differentially

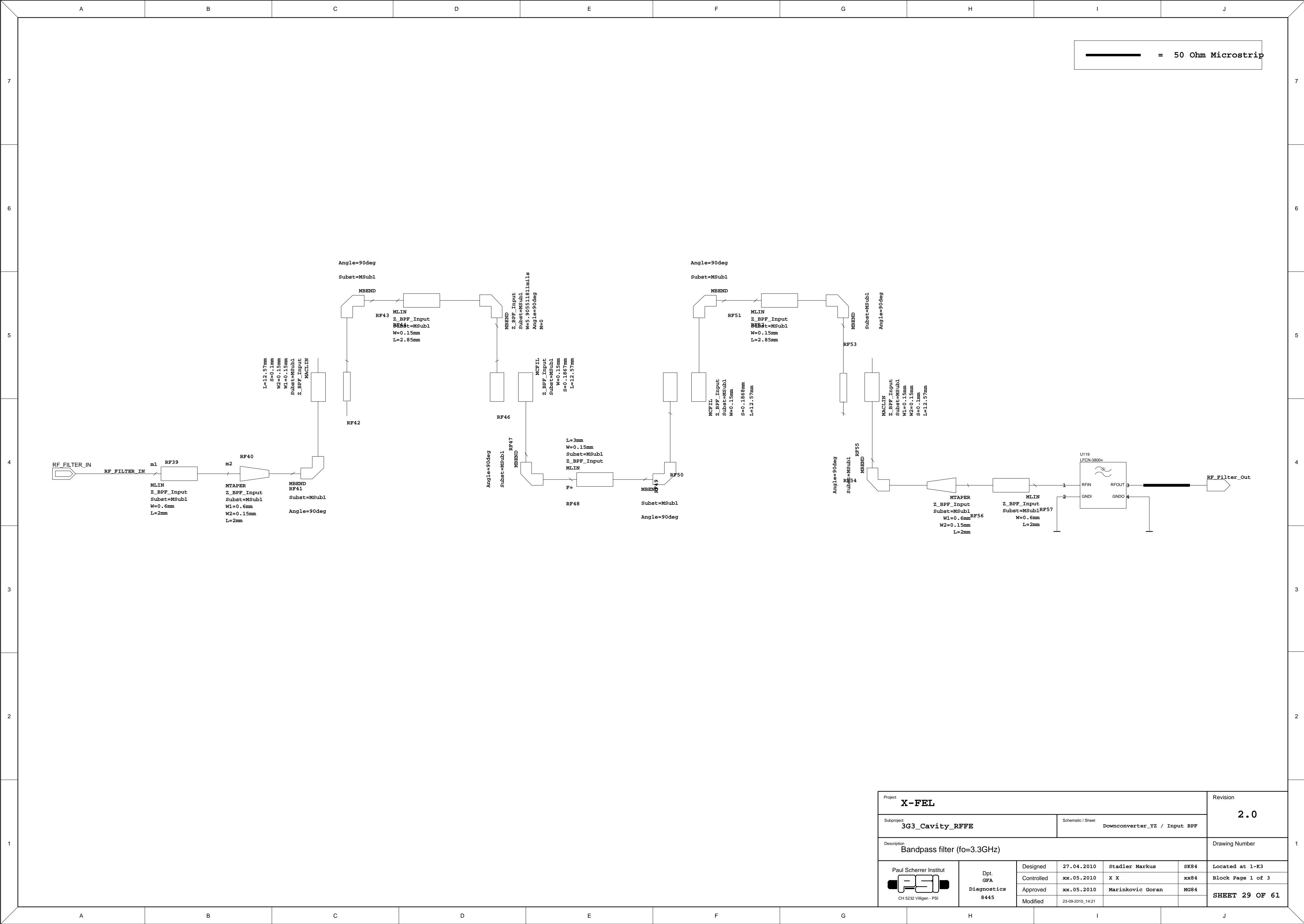
Project X-FEL						Revision 2.0	
Subproject 3G3_Cavity_RFFE			Schematic / Sheet IF_Amplifier / 2				
Description IF amplification control & ADC driving						Drawing Number	
<div>Paul Scherrer Institut</div> <div></div> <div>CH 5232 Villigen - PSI</div>		Dpt. GFA	Designed	27.04.2010	Stadler Markus	SK84	Located at 1-L2
		Diagnostics 8445	Controlled	xx.05.2010	X X	xx84	Block Page 2 of 2
			Approved	xx.05.2010	Marinkovic Goran	MG84	SHEET 14 OF 61
			Modified	20-09-2010_11:34			




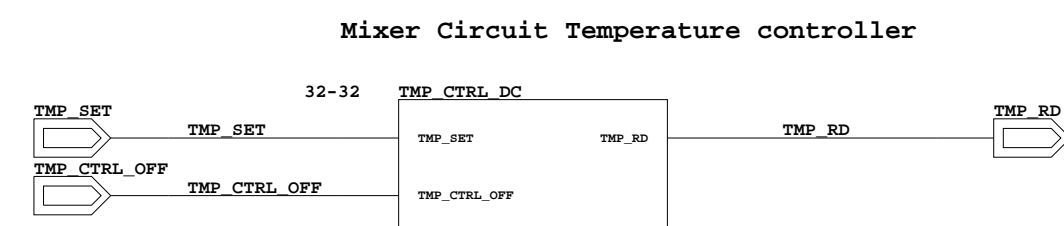
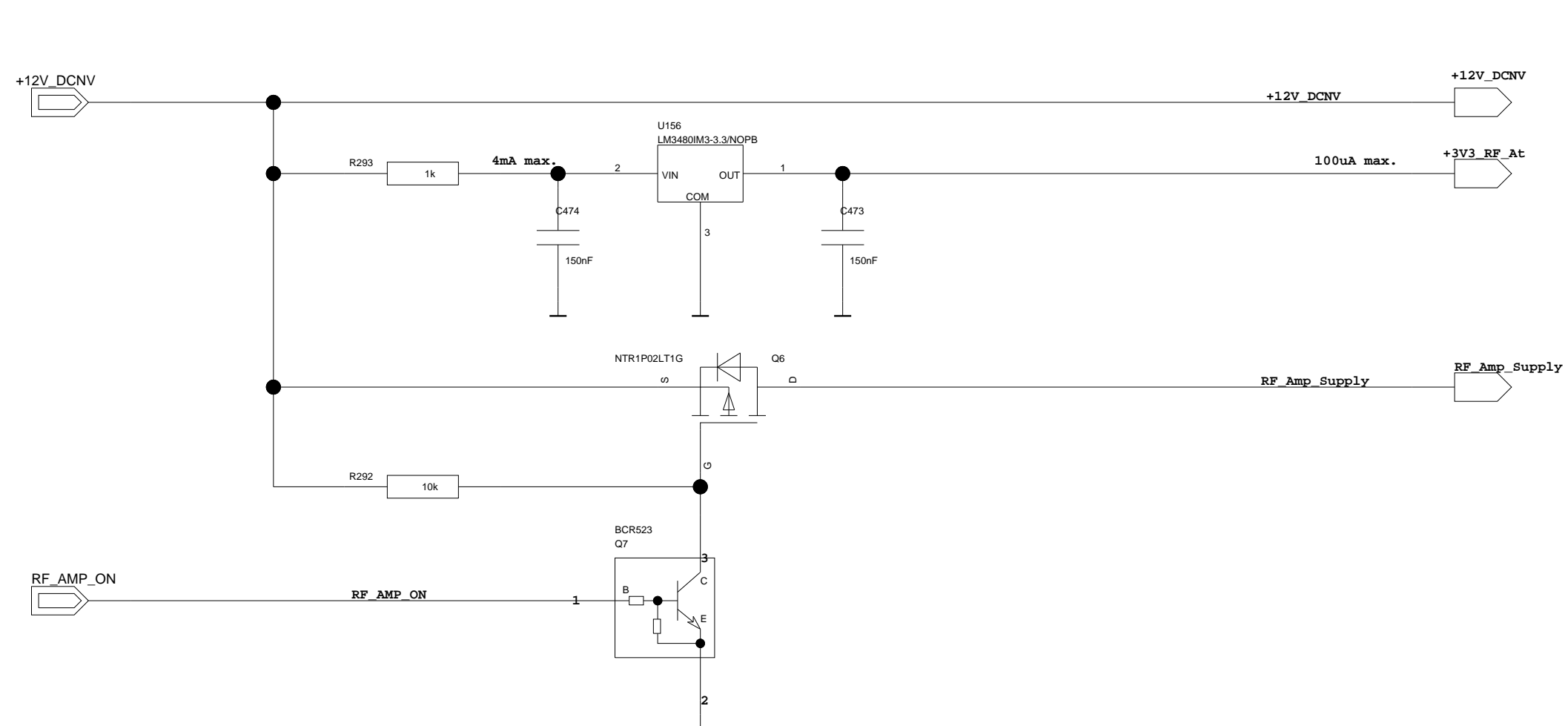
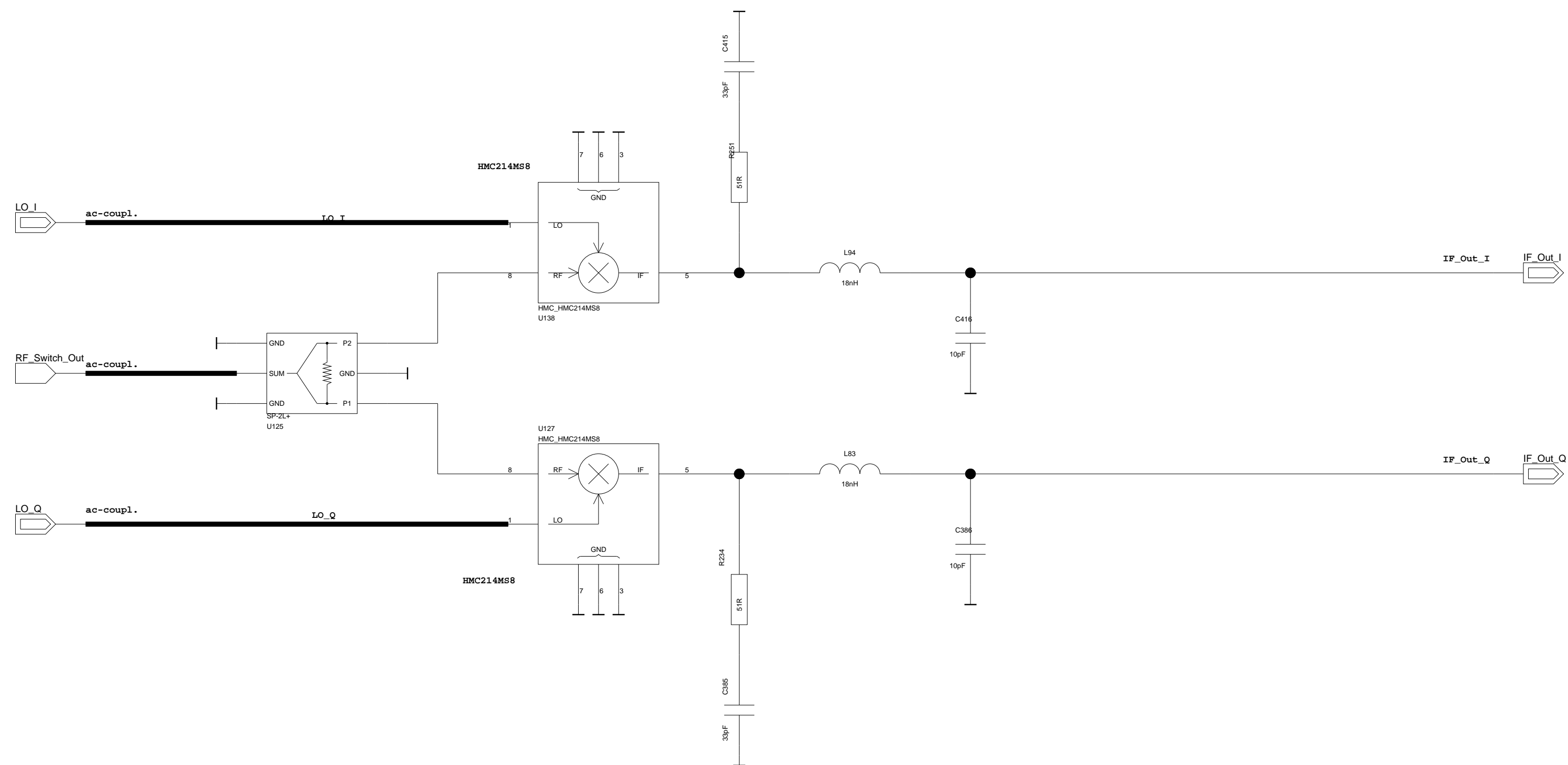
Project X-FEL						Revision 2.0
Subproject 3G3_Cavity_RFFE			Schematic / Sheet Section_Supply / 1			
Description Section supply circuitry						Drawing Number
<div>Paul Scherrer Institut</div> <div></div> <div>CH 5232 Villigen - PSI</div>	Dpt. GFA Diagnostics 8445	Designed	27.04.2010	Stadler Markus	SK84	Located at 1-H3
		Controlled	xx.05.2010	X X	xx84	Block Page 1 of 1
		Approved	xx.05.2010	Marinkovic Goran	MG84	SHEET 16 OF 61
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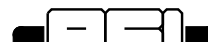


Project X-FEL						Revision 2.0		
Subproject 3G3_Cavity_RFFE			Schematic / Sheet LO_Amp / 1					
Description 3.3 GHz downconverting mixer LO driver circuit						Drawing Number		
<div>Paul Scherrer Institut</div> <div></div> <div>CH 5232 Villigen - PSI</div>		<div>Dpt. GFA</div> <div>Diagnostics</div> <div>8445</div>		Designed	27.04.2010	Stadler Markus	SK84	Located at 1-J3
				Controlled	xx.05.2010	X X	xx84	Block Page 1 of 1
				Approved	xx.05.2010	Marinkovic Goran	MG84	SHEET 17 OF 61
				Modified	09-08-2010_16:59			




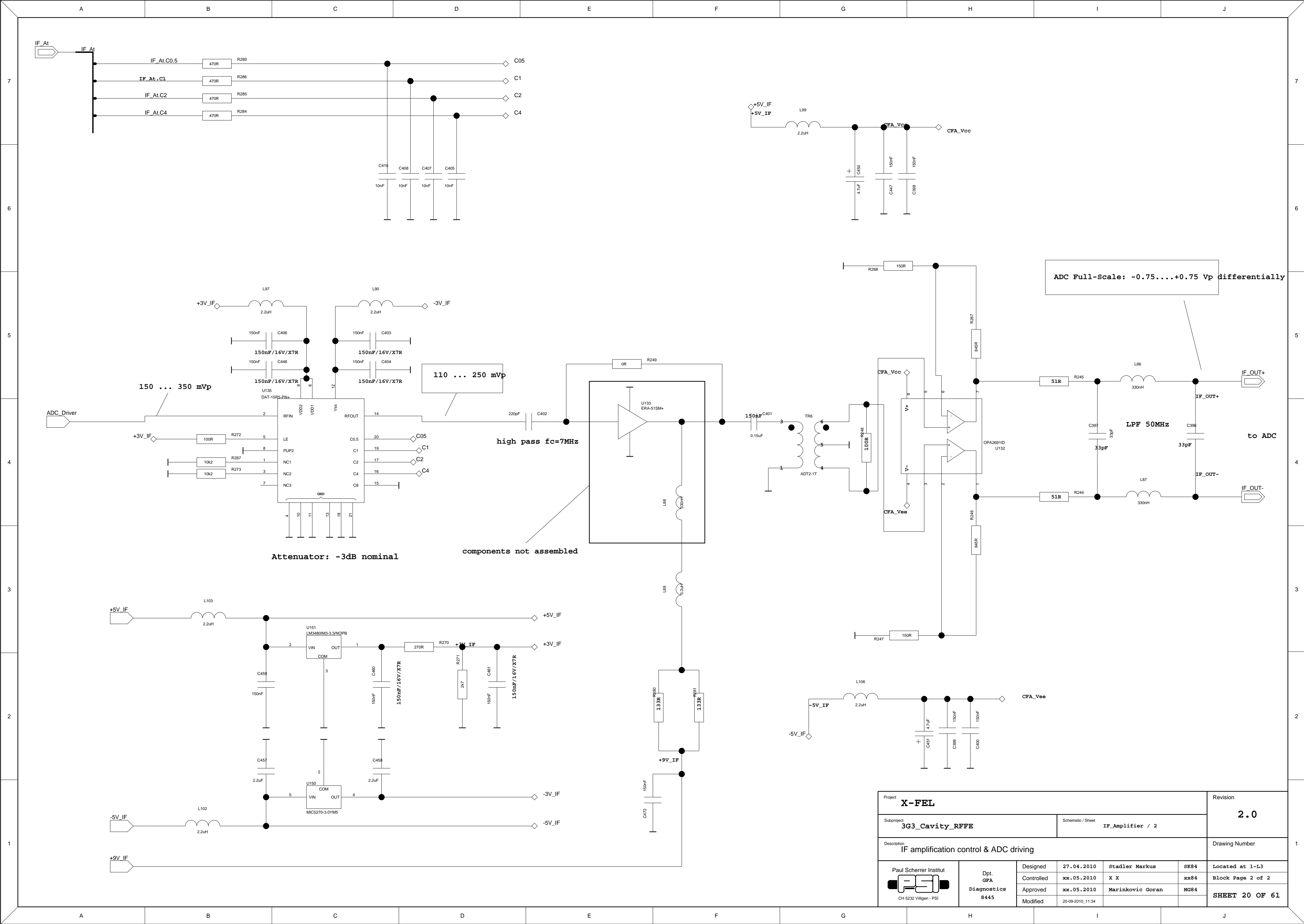
Project X-FEL						Revision 2.0
Subproject 3G3_Cavity_RFFE			Schematic / Sheet Downconverter_Y2 / Input BPF			
Description Bandpass filter (fo=3.3GHz)						Drawing Number
<div>Paul Scherrer Institut</div> <div></div> <div>CH 5232 Villigen - PSI</div>	Dpt. GFA Diagnostics 8445	Designed	27.04.2010	Stadler Markus	SK84	Located at 1-K3
		Controlled	xx.05.2010	X X	xx84	Block Page 1 of 3
		Approved	xx.05.2010	Marinkovic Goran	MG84	SHEET 29 OF 61
		Modified	23-09-2010_14:21			

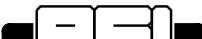


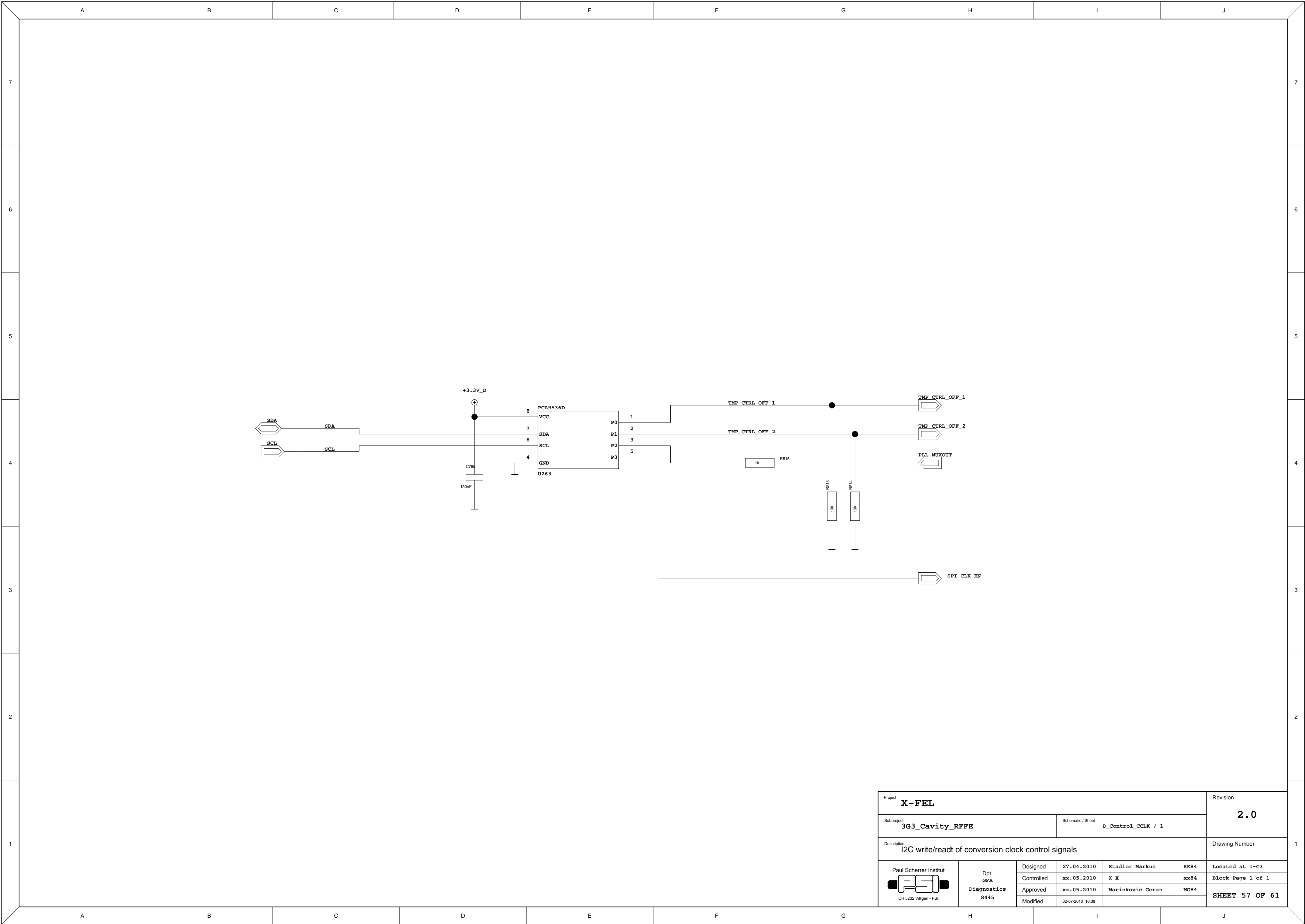
Project X-FEL					Revision 2.0			
Subproject 3G3_Cavity_RFFE			Schematic / Sheet Downconverter_YZ / Mixer					
Description Reference channel downconverting mixer stages					Drawing Number			
<div>Paul Scherrer Institut</div> <div></div> <div>CH 5232 Villigen - PSI</div>		Dpt. GFA		Designed	27.04.2010	Stadler Markus	SK84	Located at 1-K3
		Diagnostics 8445		Controlled	xx.05.2010	X X	xx84	Block Page 3 of 3
				Approved	xx.05.2010	Marinkovic Goran	MG84	SHEET 31 OF 61
				Modified	10-08-2010_09-16			


	A	B	C	D	E	F	G	H	I	J	
7											7
6											6
5											5
4											4
3											3
2											2
1											1
	A	B	C	D	E	F	G	H	I	J	

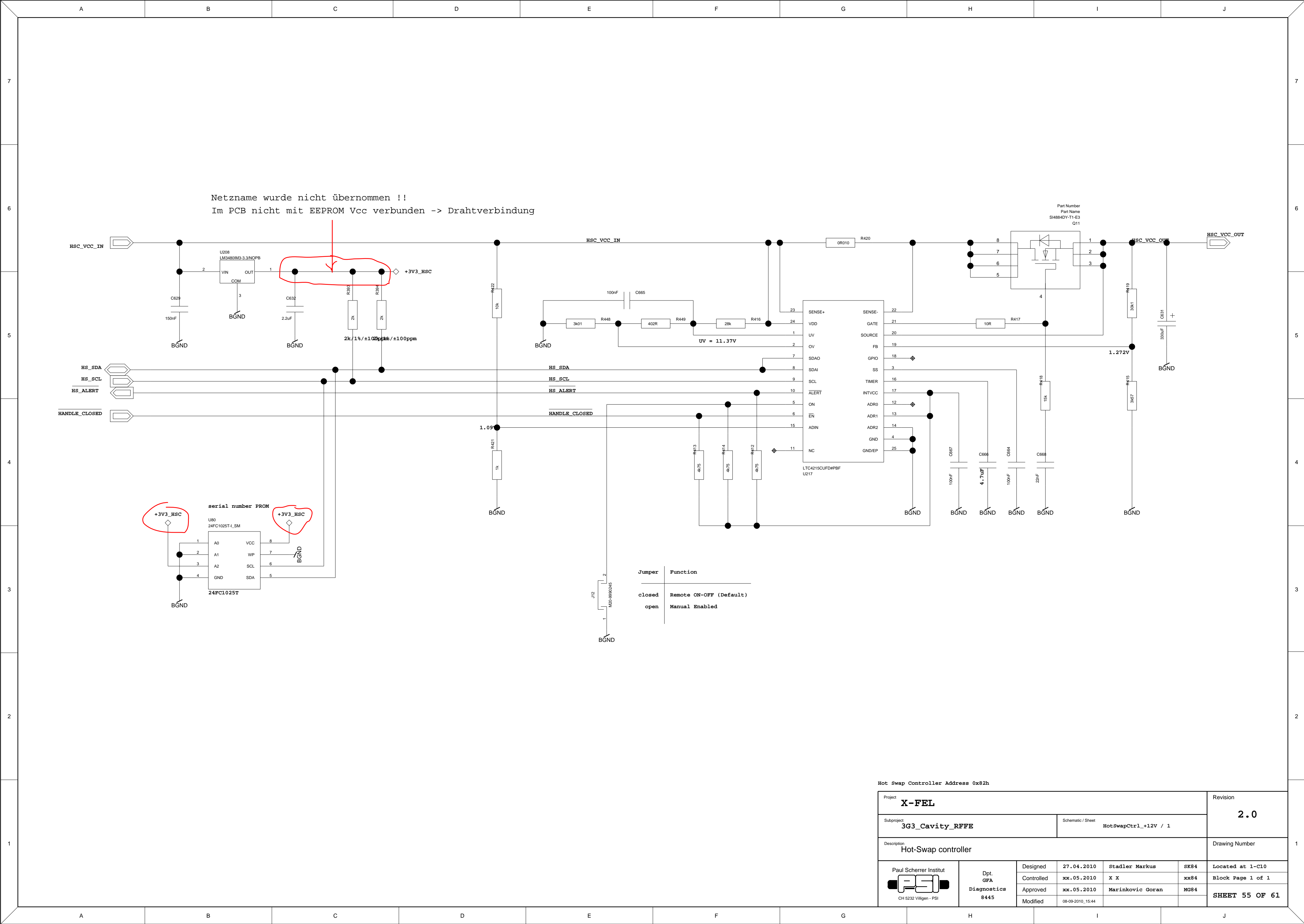
Project X-FEL						Revision 2.0	
Subproject 3G3_Cavity_RFFE				Schematic / Sheet			
Description						Drawing Number	
<div>Paul Scherrer Institut</div> <div></div> <div>CH 5232 Villigen · PSI</div>		<div>Dpt. GFA</div> <div>Diagnostics</div> <div>8445</div>		Designed	27.04.2010	Stadler Markus	SK84
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				Approved	xx.05.2010	Marinkovic Goran	MG84
				Modified	10-08-2010_14:09		




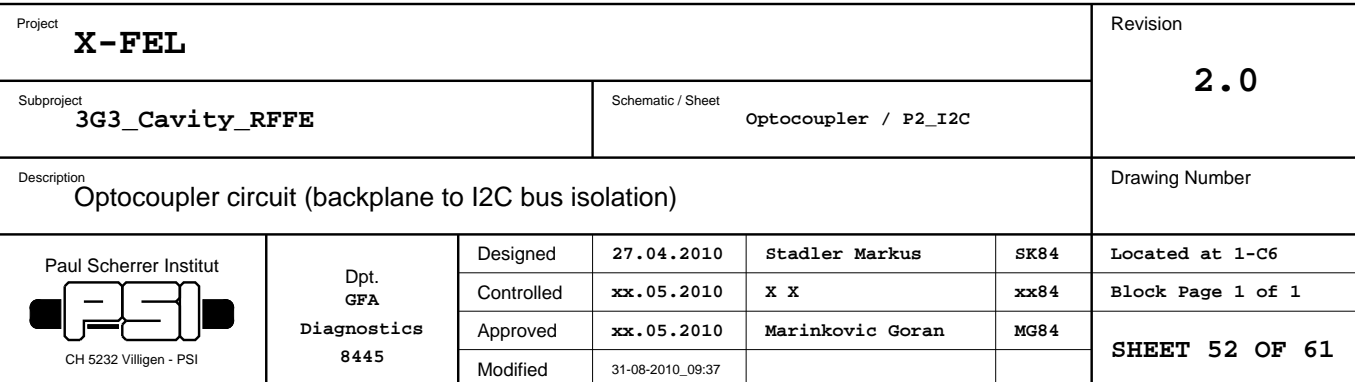
Project X-FEL						Revision 2.0	
Subproject 3G3_Cavity_RFPE			Schematic / Sheet IF_Amplifier / 2				
Description IF amplification control & ADC driving						Drawing Number	
<div>Paul Scherrer Institut</div> <div></div> <div>CH 5232 Villigen - PSI</div>		<div>Dpt. GFA</div> <div>Diagnostics</div> <div>8445</div>	Designed	27.04.2010	Stadler Markus	SK84	Located at 1-L3
			Controlled	xx.05.2010	X X	xx84	Block Page 2 of 2
			Approved	xx.05.2010	Marinkovic Goran	MG84	SHEET 20 OF 61
			Modified	20-09-2010_11:34			

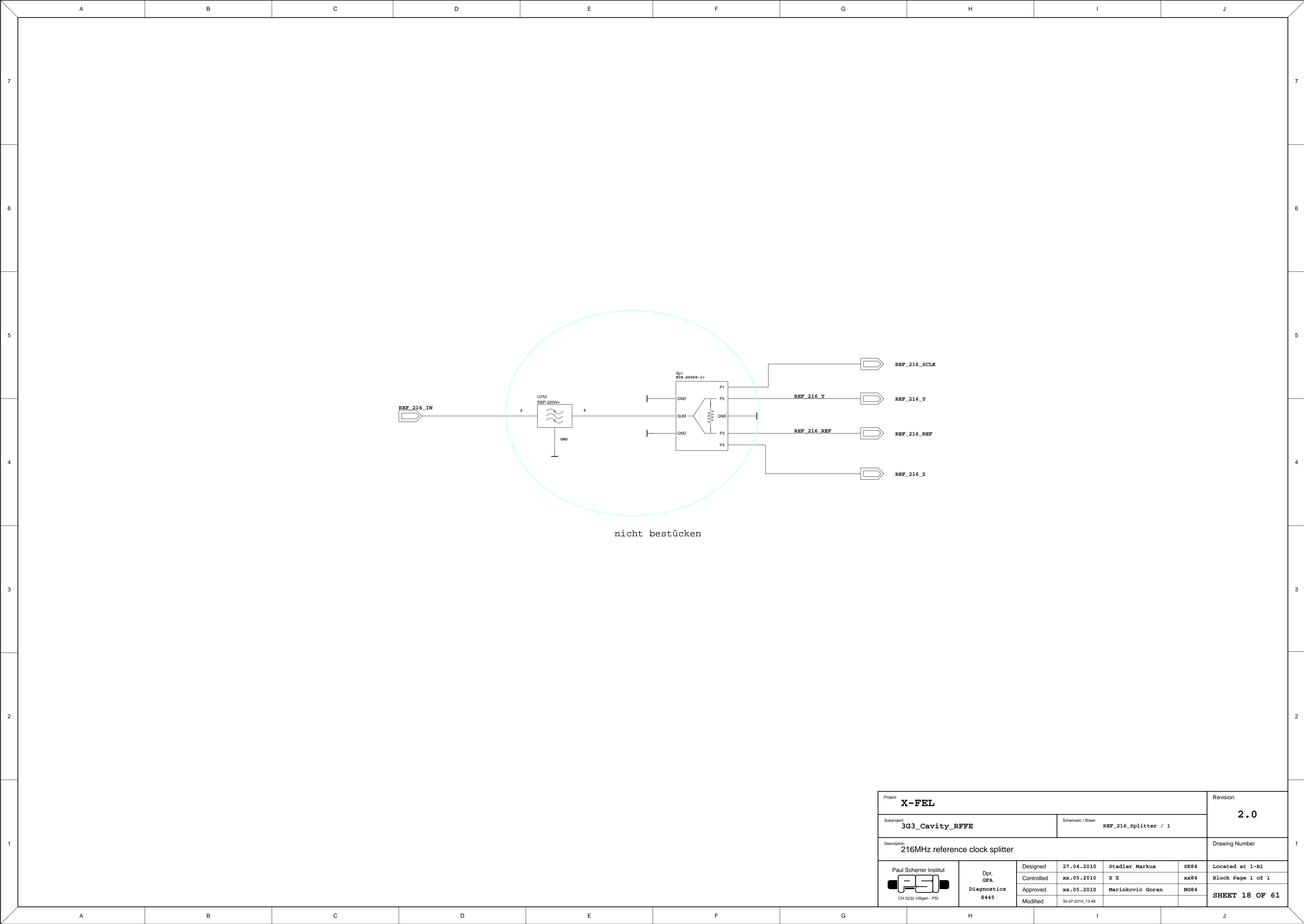


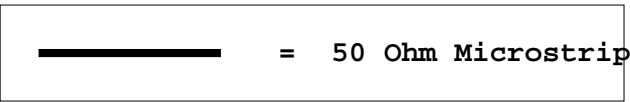
Project X-FEL						Revision 2.0	
Subproject 3G3_Cavity_RFFE			Schematic / Sheet D_Control_CCLK / 1				
Description I2C write/read of conversion clock control signals						Drawing Number	
<div>Paul Scherrer Institut</div> <div></div> <div>CH 5232 Villigen · PSI</div>		<div>Dpt. GFA</div> <div>Diagnostics 8445</div>	Designed	27.04.2010	Stadler Markus	SK84	Located at 1-C3
			Controlled	xx.05.2010	X X	xx84	Block Page 1 of 1
			Approved	xx.05.2010	Marinkovic Goran	MG84	SHEET 57 OF 61
			Modified	02-07-2010_16:38			




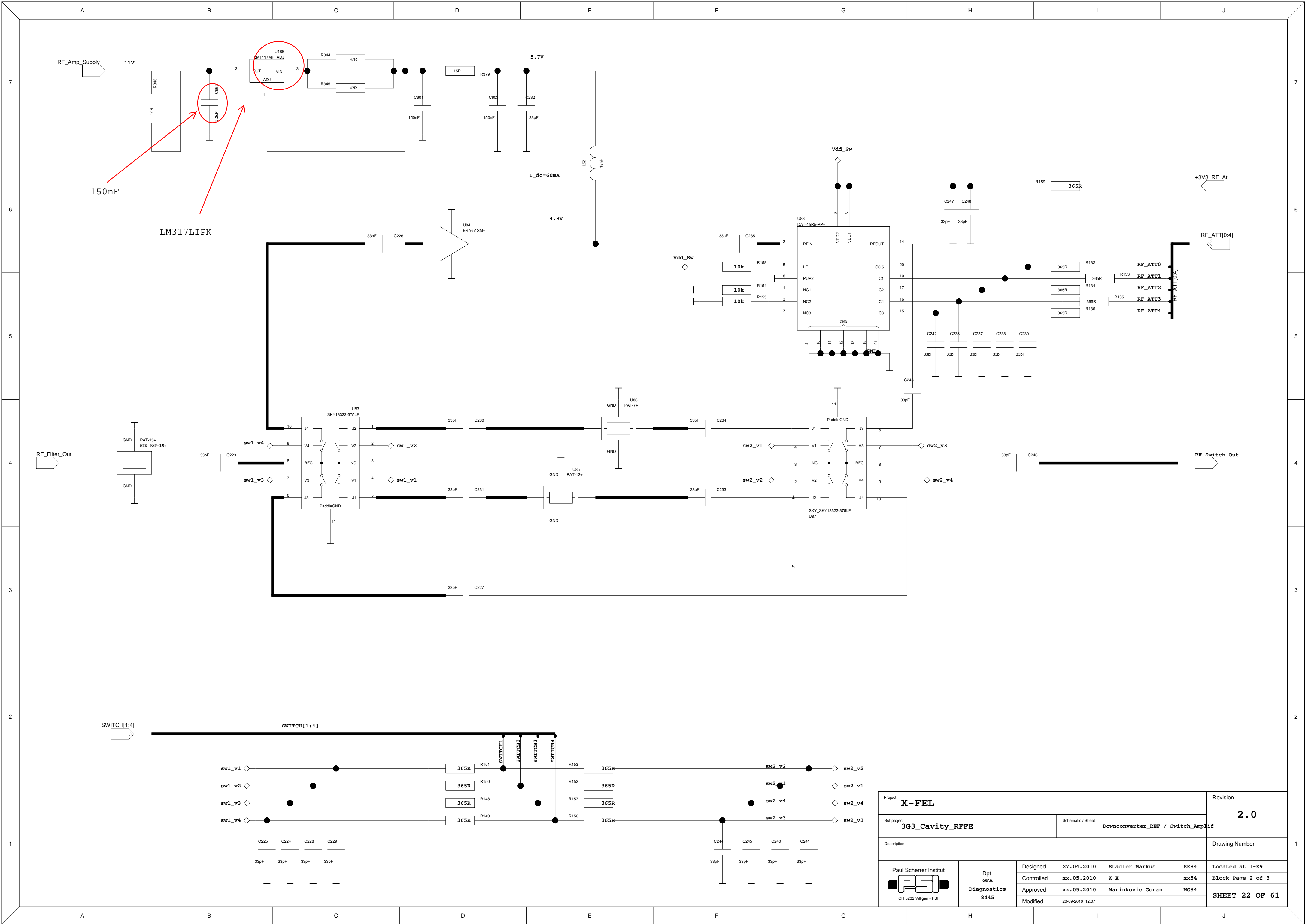
Hot Swap Controller Address 0x82h								
Project X-FEL					Revision 2.0			
Subproject 3G3_Cavity_RFFE		Schematic / Sheet HotSwapCtrl_+12V / 1						
Description Hot-Swap controller					Drawing Number			
<div>Paul Scherrer Institut</div> <div></div> <div>CH 5232 Villigen - PSI</div>		<div>Dpt. GFA</div> <div>Diagnostics</div> <div>8445</div>		Designed	27.04.2010	Stadler Markus	SK84	Located at 1-C10
				Controlled	xx.05.2010	X X	xx84	Block Page 1 of 1
				Approved	xx.05.2010	Marinkovic Goran	MG84	SHEET 55 OF 61
				Modified	08-09-2010_15:44			




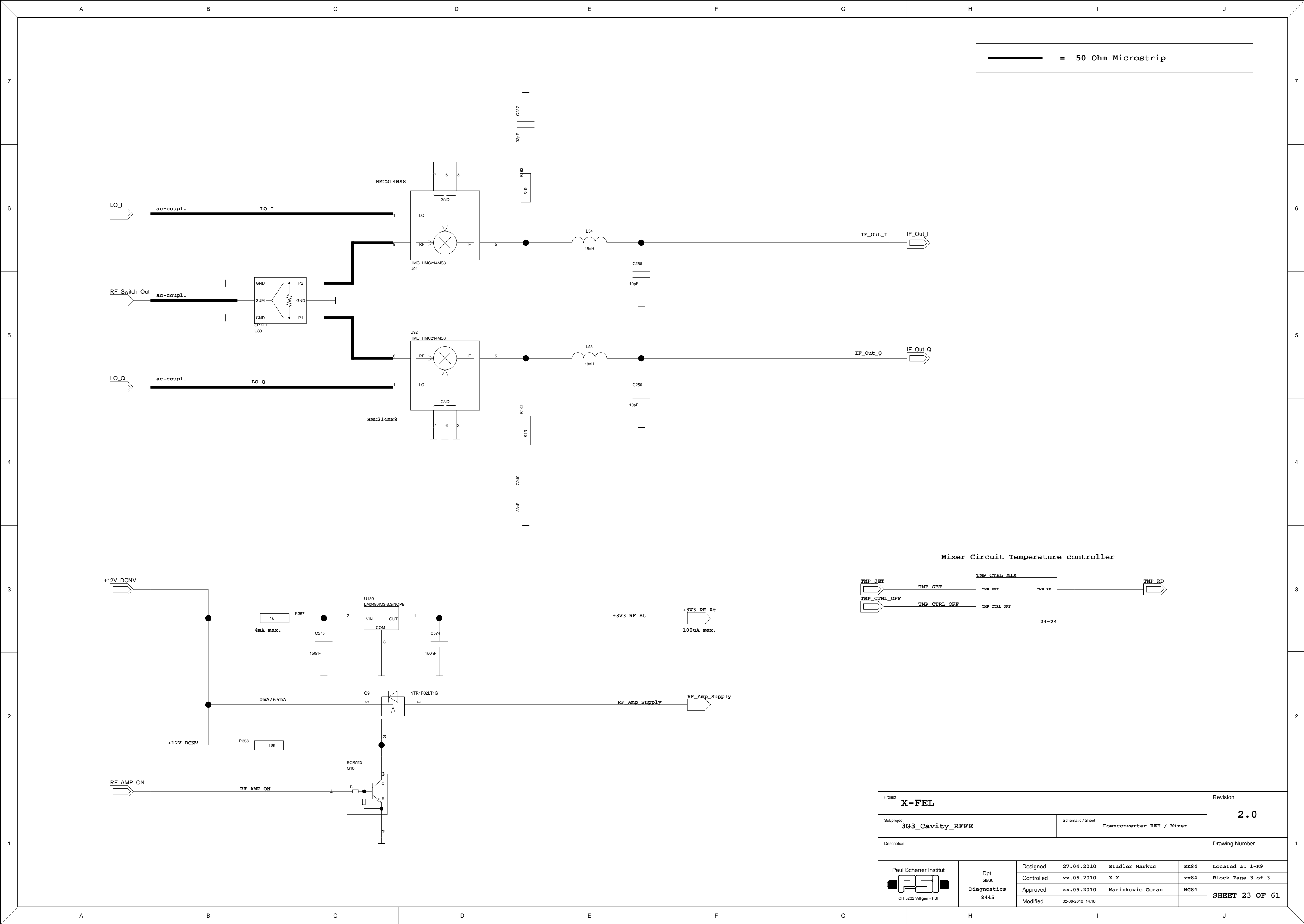


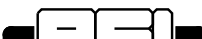


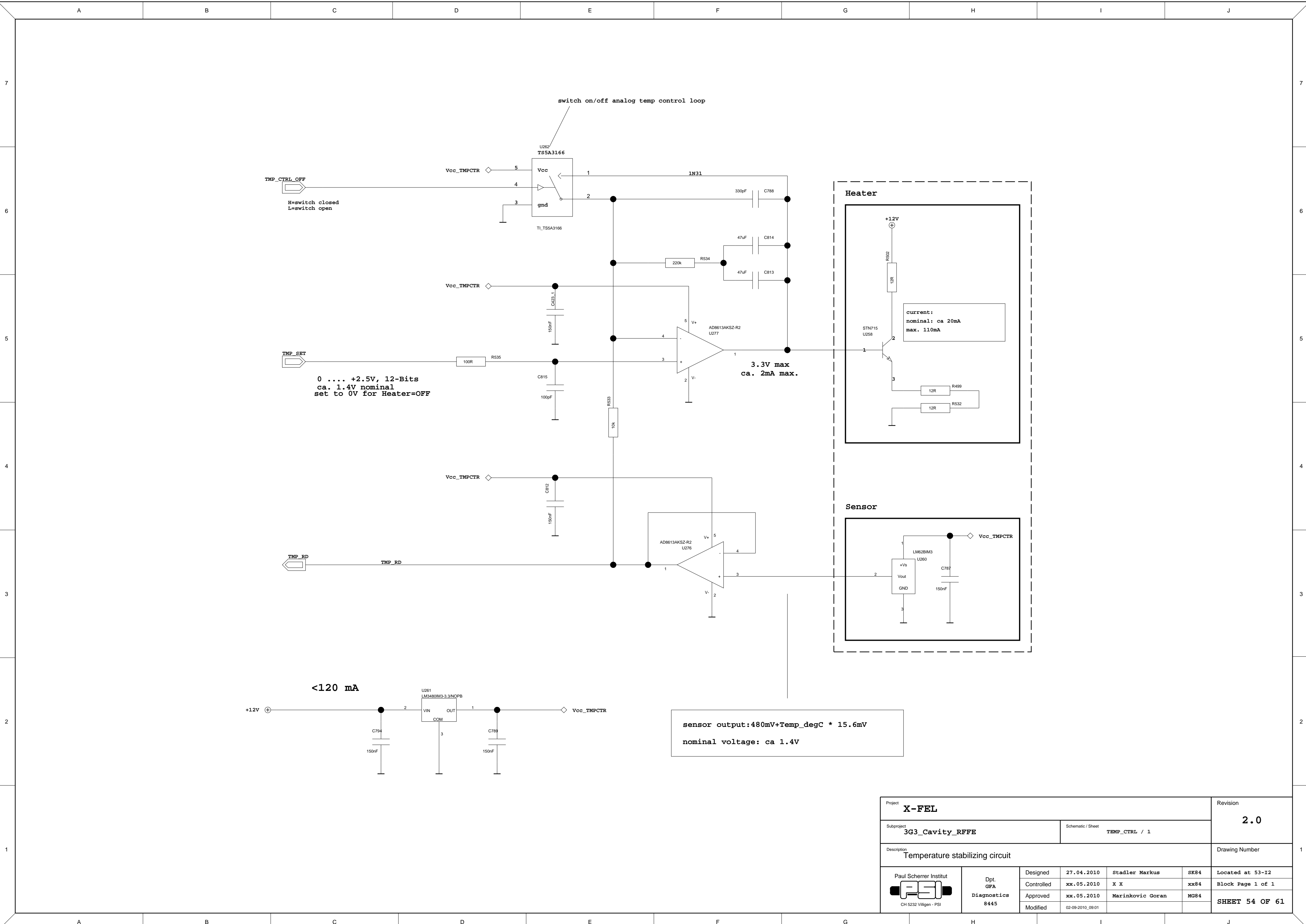
Project X-FEL					Revision 2.0			
Subproject 3G3_Cavity_RFFE			Schematic / Sheet Downconverter_REF / Input_BPF					
Description Bandpass filter (f_center=3.3GHz)					Drawing Number			
<div>Paul Scherrer Institut</div> <div></div> <div>CH 5232 Villigen - PSI</div>		<div>Dpt. GFA</div> <div>Diagnostics 8445</div>		Designed	27.04.2010	Stadler Markus	SK84	Located at 1-K9
				Controlled	xx.05.2010	X X	xx84	Block Page 1 of 3
				Approved	xx.05.2010	Marinkovic Goran	MG84	SHEET 21 OF 61
				Modified	23-09-2010, 11:34			

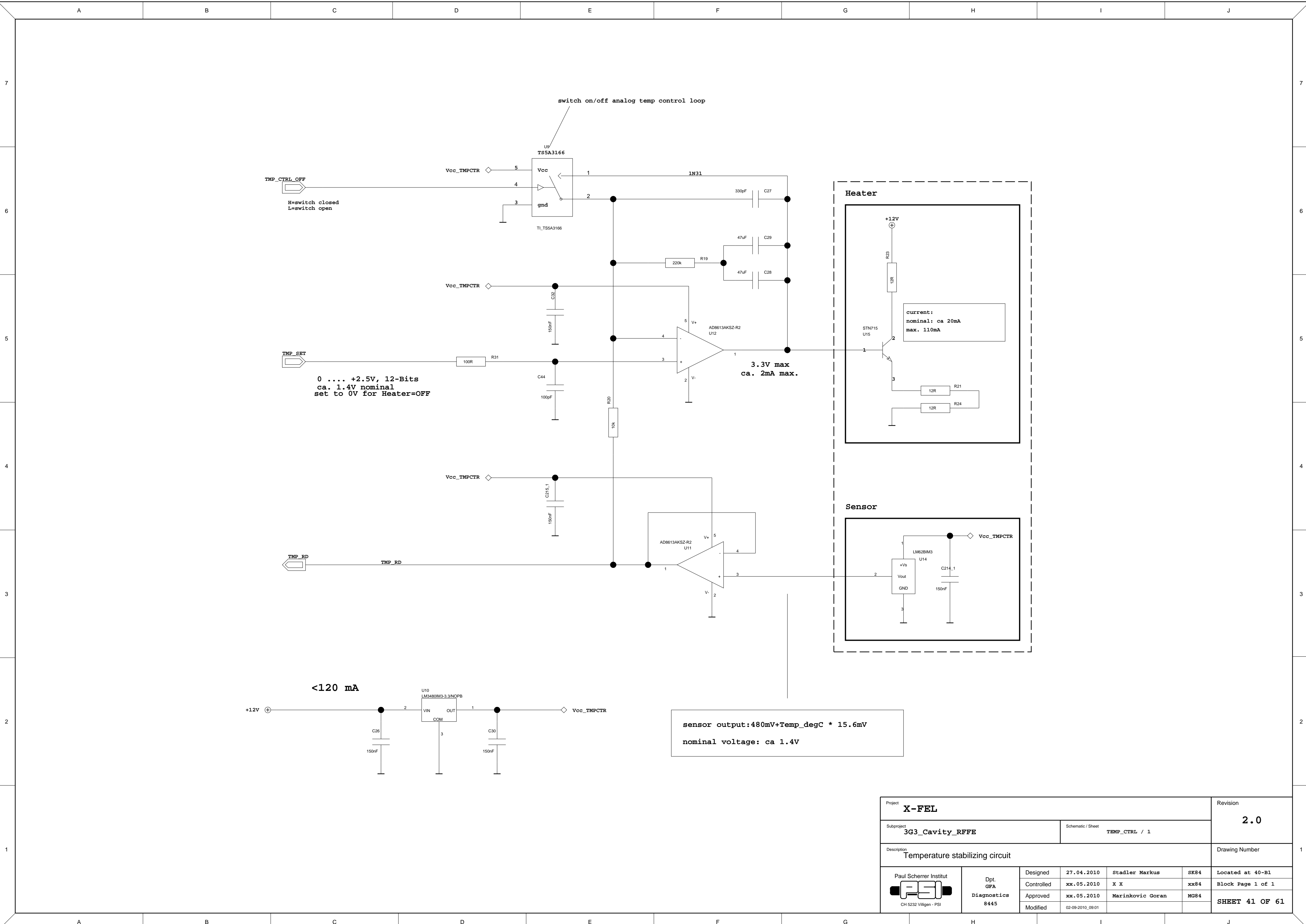


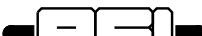
Project X-FEL						Revision 2.0	
Subproject 3G3_Cavity_RFFE			Schematic / Sheet Downconverter_REF / Switch_Amplif			Drawing Number	
Description							
<div>Paul Scherrer Institut</div> <div></div> <div>CH 5232 Villigen - PSI</div>		Dpt. GFA Diagnostics 8445	Designed	27.04.2010	Stadler Markus	SK84	Located at 1-K9
			Controlled	xx.05.2010	X X	xx84	Block Page 2 of 3
			Approved	xx.05.2010	Marinkovic Goran	MG84	SHEET 22 OF 61
			Modified	20-09-2010_12:07			

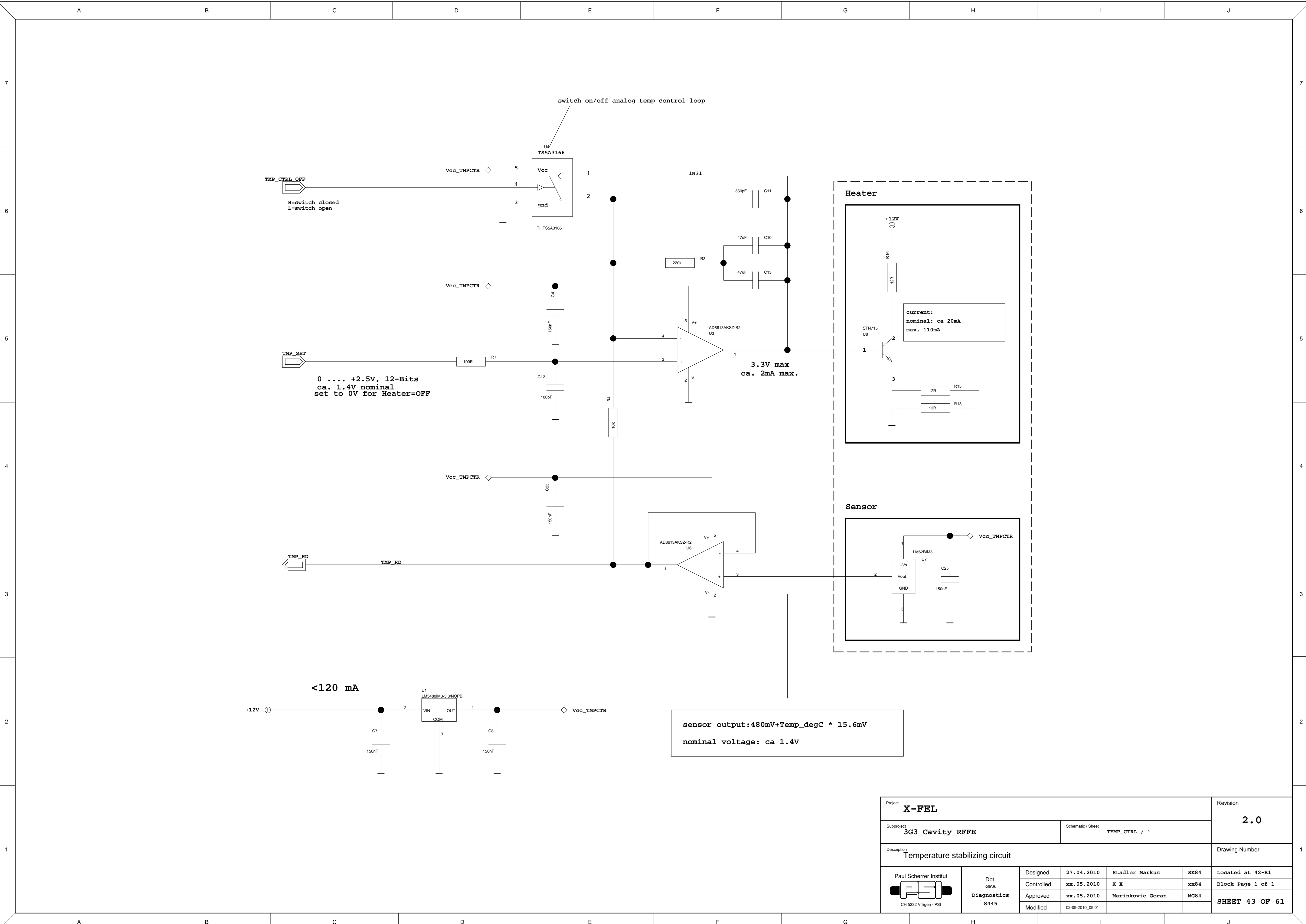


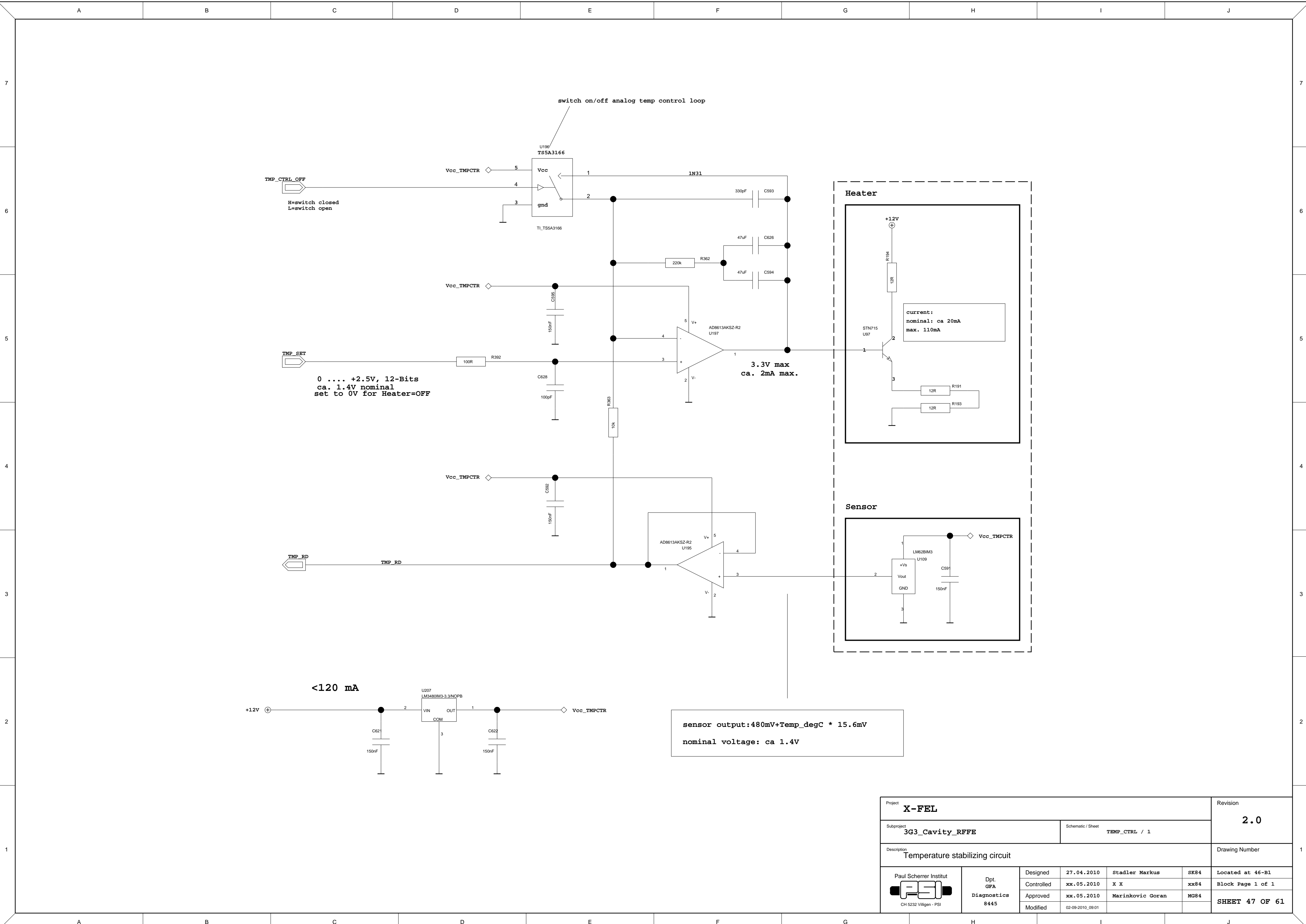
Project X-FEL						Revision 2.0
Subproject 3G3_Cavity_RFFE			Schematic / Sheet Downconverter_REF / Mixer			
Description						Drawing Number
<div>Paul Scherrer Institut</div> <div></div> <div>CH 5232 Villigen - PSI</div>	Dpt. GFA Diagnostics 8445	Designed	27.04.2010	Stadler Markus	SK84	Located at 1-K9
		Controlled	xx.05.2010	X X	xx84	Block Page 3 of 3
		Approved	xx.05.2010	Marinkovic Goran	MG84	SHEET 23 OF 61
		Modified	02-08-2010_14:16			

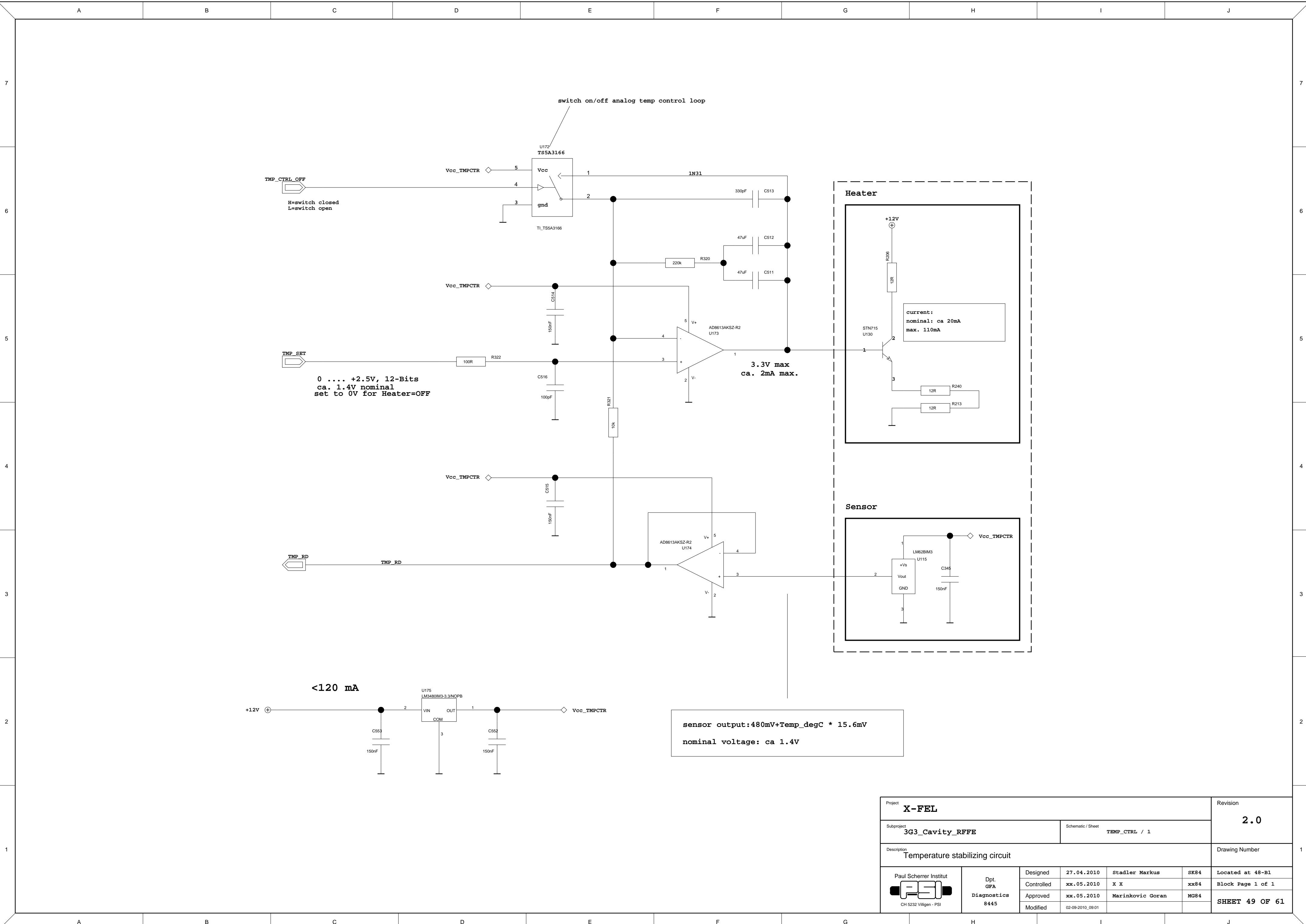





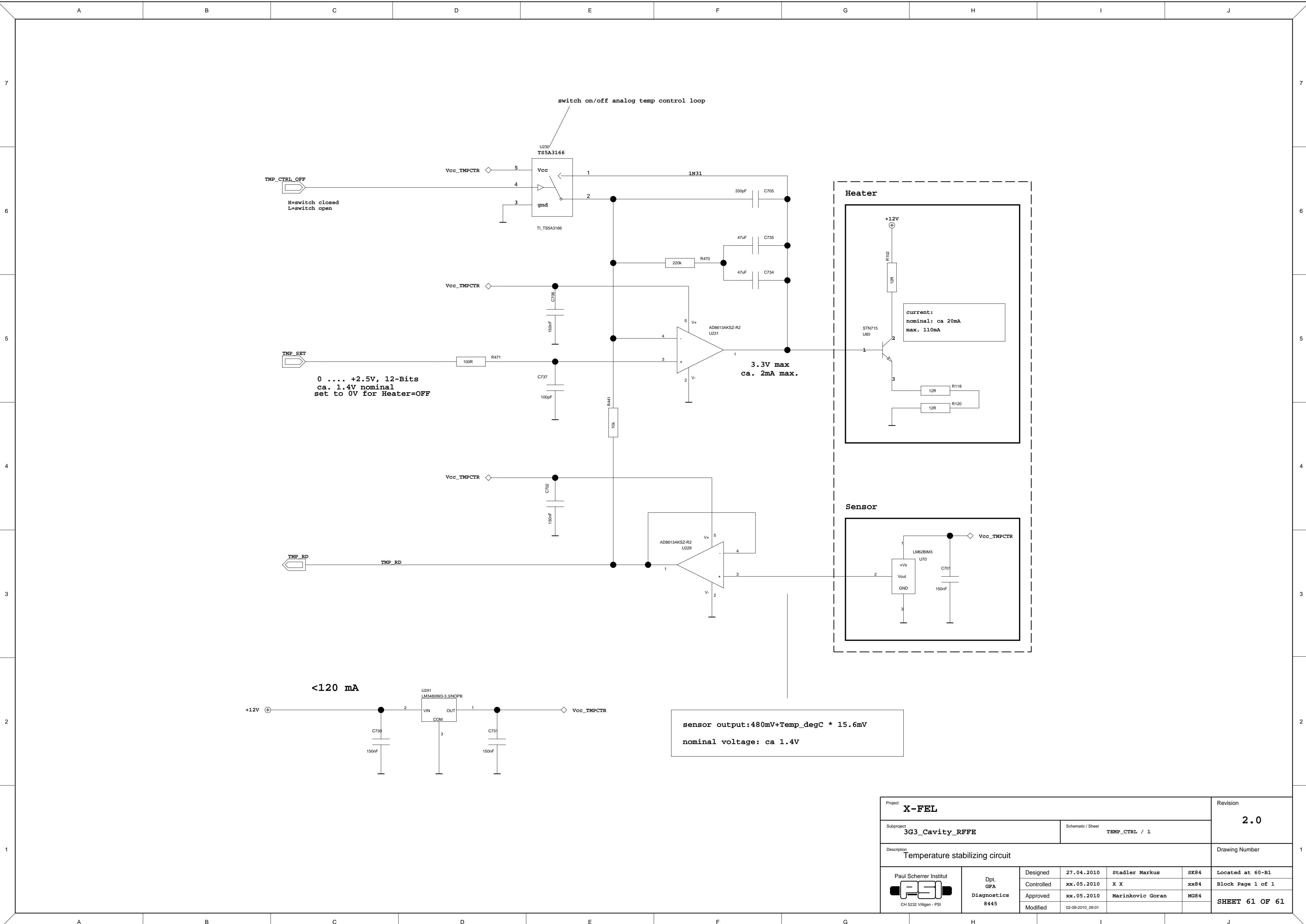
Project X-FEL						Revision 2.0
Subproject 3G3_Cavity_RFFE			Schematic / Sheet TEMP_CTRL / 1			
Description Temperature stabilizing circuit						Drawing Number
<div>Paul Scherrer Institut</div> <div></div> <div>CH 5232 Villigen - PSI</div>	Dpt. GFA Diagnostics 8445	Designed	27.04.2010	Stadler Markus	SK84	Located at 40-B1
		Controlled	xx.05.2010	X X	xx84	Block Page 1 of 1
		Approved	xx.05.2010	Marinkovic Goran	MG84	SHEET 41 OF 61
		Modified		02-09-2010_09:01		

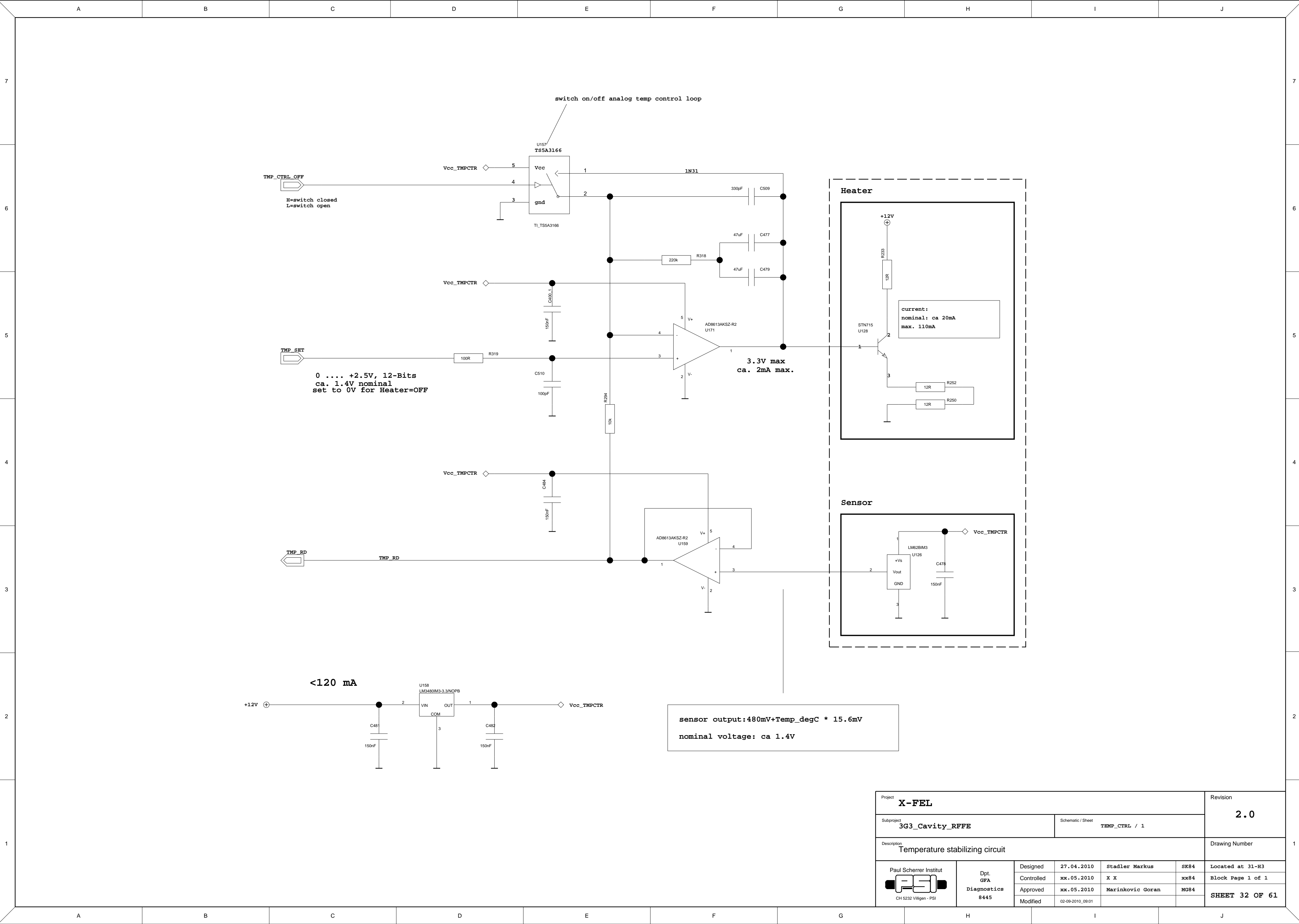







Project X-FEL						Revision 2.0
Subproject 3G3_Cavity_RFFE			Schematic / Sheet TEMP_CTRL / 1			
Description Temperature stabilizing circuit						Drawing Number
<div><div></div><div>Paul Scherrer Institut CH 5232 Villigen - PSI</div></div>	Dpt. GFA Diagnostics 8445	Designed	27.04.2010	Stadler Markus	SK84	Located at 48-B1
		Controlled	xx.05.2010	X X	xx84	Block Page 1 of 1
		Approved	xx.05.2010	Marinkovic Goran	MG84	SHEET 49 OF 61
		Modified		02-09-2010_09:01		





Project X-FEL						Revision 2.0
Subproject 3G3_Cavity_RFFE			Schematic / Sheet TEMP_CTRL / 1			
Description Temperature stabilizing circuit						Drawing Number
<div><div><div>Paul Scherrer Institut</div><div></div><div>CH 5232 Villigen - PSI</div></div></div>	Dpt. GFA Diagnostics 8445	Designed	27.04.2010	Stadler Markus	SK84	Located at 31-H3
		Controlled	xx.05.2010	X X	xx84	Block Page 1 of 1
		Approved	xx.05.2010	Marinkovic Goran	MG84	SHEET 32 OF 61
		Modified		02-09-2010_09:01		

