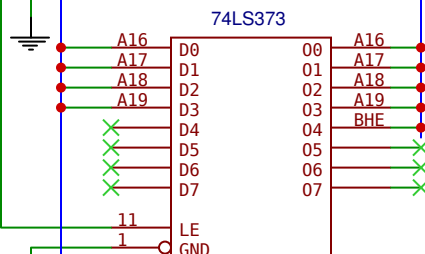
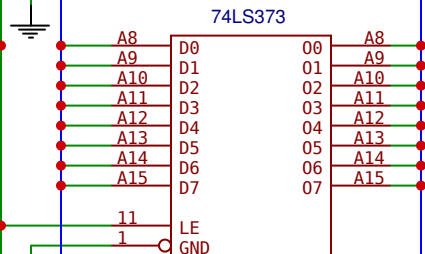
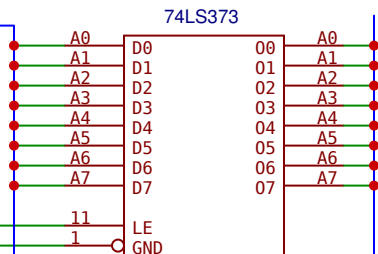
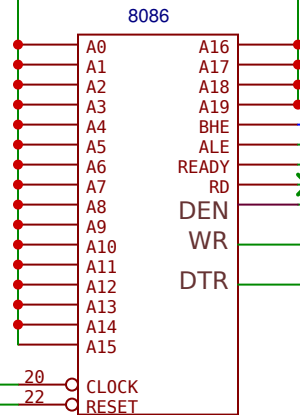
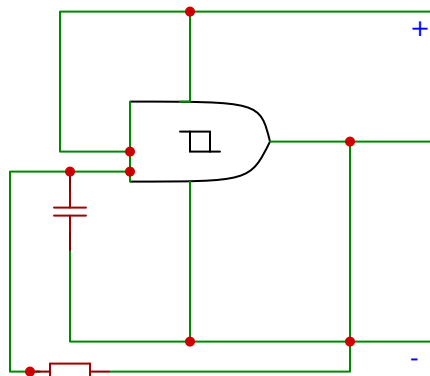
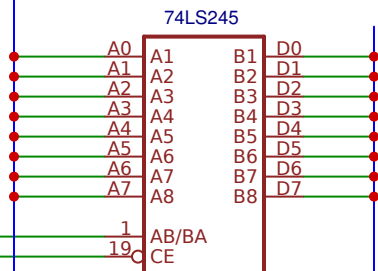


Generator de clock

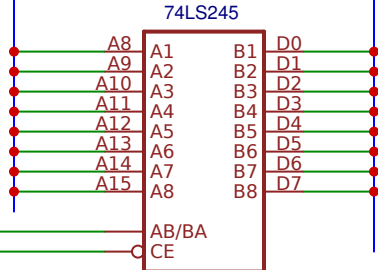
8284A.1



Adress Bus



Data Bus



TITLE:		REV: 1.0	
Unitate centrala			
Date:		Sheet:	
EasyEDA V4.11.5		Drawn By:	