



NOTES

- The different grounds should only be connected at one junction using the net tie
- The grounds should have their own planes and the power, digital, and analog sections should tried to be kept separate – different regions of the board
- Bypass capacitors need to be placed close to the IC and must be physically between the power and IC
- The TVS (zener) diodes need to be placed physically before the capacitors and before any connections to power sensor
- Temp IC should be close to the power sensor

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