CENTRAL UNIVERSITY OF HARYANA

Department of Computer Science & Engineering under SOET



Micro Processors and

Interfacing Lab

Practical 1-4

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Practical 1.(i)- Write an ALP to move block of data without overlap.

Aim:

To Write an ALP to Move a Block of Data without Overlap

Software Required:

Masm 16 Bit

Algorithm:

- 1. Define block of data
- 2. Save memory for block transfer as block2
- 3. Load block1 into SI
- 4. Load block2 into DI
- 5. Initialize counter
- 6. Move first data into DI
- 7. Repeat step 6 until counter is zero
- 8. End

Program:

.MODEL SMALL .DATA

BLK1 DB 01,02,03,04,05,06,07,08,09,0AH BLK2 DB 10 DUP (?) COUNT DW 0AH

.CODE

MOV AX, @DATA MOV DS, AX MOV ES, AX MOV SI, OFFSET BLK1; MOV DI, OFFSET BLK2 MOV CX, COUNT

AGAIN: CLD

REP MOVSB MOV

OUTPUT: BEFORE EXECUTION												
											32	200
									2	ZF A	F PF C	F O
0 0										0		
+												
CMD > 1			0	1	2	3	4	5	6	7		
+	DS	:0000		00	FC F	3 A4	B4 4	4C C	D 2	1		
000 8ED8	MO	DS,AX	DS:0008		0	0	0	0	0	0	0	
000 8EC0	MO	ES,AX	DS:0010		0	0	0	0	0	0	0	
000 BE0800	MO	SI,0008	DS:0018		0	0	0	0	0	0	0	
000 BF1200	MO	DI,0012	DS:0020		0	0	0	0	0	0	0	
000 8B0E1C00	MO	CX,[001C]	DS:0028		0	0	0	0	0	0	0	
001 FC	CL		DS:0030		0	0	0	0	0	0	0	
001 F3A4	REP	MOVSB	DS:0038		0	0	0	0	0	0	0	
001 B44C	MO	AH,4C	DS:0040		0	0	0	0	0	0	0	
001 CD21	INT	2.1	DS:0048		0	0	0	0	0	0	0	

AFTER EXECUTION 0 0 |CMD > 11 1 2 3 4 5 +----- \mid DS:0000 00 FC F3 A4 B4 4C CD 21 001 B44C MO AH,4C 00 0 DS:0008 0 0 DS:0010 001 CD21 INT21 000 0 0 001 0102 00AD [BP+SI],AX DS:0018 0 0 0 001 0304 AD AX,[SI] DS:0020 0 0 0 001 050607 AD AX,0706 000 0 DS:0028 001 0809 OR [BX+DI],CL 000 0 0 DS:0030 002 0A01 OR AL,[BX+DI] DS:0038 000 0 0 002 0203 AD AL,[BP+DI] DS:0040 000 0 002 0405 AD AL,05 DS:0048 00

3200

ZF AF PF CF 0

7

0

0 0

0 0

0 0

0 0

0 0

0 0

0 0

Result:

The Block Of Data Defined In The Program Is Moved From Source To Destination Without Overlap Successfully.

Verification And Validation:

Output Is Verified For Different Bytes Of Data And Is Successfully Moved From Default Source Address To Destination Address Without Overlap.

Conclusion:

The Block Of Data Defined In The Program Is Moved To Destination Without Overlap And Output Is Verified.

Practical 1.(ii) - Write an ALP to move a block with overlap

Aim - To Write An Alp To Move Block Of Data With Overlap

Software Required:

Masm 16 Bit

Algorithm:

- 1. Define block of data
- 2. Reserve memory for block transfer as block2
- 3. Move block1 address to SI
- 4. Move block2 address to DI
- 5. Initialize counter
- 6. Point DI to block+ n
- 7. Move block1 data to block2
- 8. Repeat step 7 until counter is zero
- 9. End

Program:

.MODEL SMALL

.DATA

BLK1 DB 01,02,03,04,05,06,07,08,09,0AH BLK2 DB 10 DUP (?)

; SET POINTER

.CODE

MOV AX, @DATA ; MOV THE STARTING ADDRESS MOV DS, AX

MOV ES, AX

MOV SI, OFFSET BLK1 ; SET POINTER REG TO BLK1 MOV DI, OFFSET BLK2

REG TO BLK2 MOV CX, 0AH ; SET COUNTER

ADD SI, 0009H ADD DI, 0004H

AGAIN:

MOV AL, [SI] MOV [DI], AL DEC SI

DEC DI

DEC CL ; DECREMENT COUNTER

JNZ AGAIN ; TO END PROGRAM MOV AH, 4CH

INT 21H END

OUTPUT:

BEFORE EXECUTION

	0	1	2	3 4	5 6	7	8	9 A	В	C	D	E	F
DS:0000		B 4	C	20	0 0	04	0	0 0	0	0	0	0	0
DS:0010		0	0	0 0	0 0	00	0	0 0	0	0	0	0	0
DS:0020		0	0	0.0	0.0	00	0	0.0	0	0	0	0	0

	DS:	003	0		0 0	0 0	0	0 00	0		0 0	0	0	0	0	0
	DS:	004	0		0 0	0 0	0	0 00	0		0 0	0	0	0	0	0
=======																
ΓER EXECUTION	ON ====	===														
			•	- 4		_	_		_	~	_	_	_			
	0	1	2	3 4	5 6	7	8	9 A	В	C	D	Ε	F			
DS:0000	0 B	1 4	2 C	3 4 2 0	56	7 04	8 0	9 A 0 0	B 0	0	D 0	E 0	F 0			
DS:0000 DS:0010	•	1 4 0	_		• •	,	_		_	_	_	_	-			
	B 0	-	C	20	0 0	04	0	0 0	0	0	0	0	0			
DS:0010	B 0	0	C 0	2 0 0 0	00	04 00	0	00	0	0	0	0	0			

Result:

The Block Of Data Defined In The Program Is Moved From Source To Destination With Overlap Successfully.

Verification And Validation:

Output Is Verified For Different Bytes Of Data And Is Successfully Moved From Default Source Address To Destination Address With Overlap.

Conclusion:

The Block Of Data Defined In The Program Is Moved To Destination With Overlap And Output Is Verified.

Practical 1.(iii) - Write an ALP to interchange a block of data.

Aim:

To Program To Interchange A Block Of Data

Software Required:

Masm 16 Bit

Algorithm:

- 1. Define two sets of data.
- 2. Load address of src to SI
- 3. Load address of dst to DI
- 4. Initialize counter
- **5**. Interchange data in src and dst **6**. Repeat step 5 until counter = 0.
- 7. End

Program:

```
.MODEL SMALL
```

.DATA

SRC DB 10H,20H,30H,40H,50h DST DB 06,07,08,09,0AH COUNT EQU 05

.CODE

MOV AX, @DATA MOV DS, AX

; INITIALIZE THE DATA REGISTER

LEA SI, SRC LEA DI,

DST

BACK:

MOV CL, COUNT

; INITIALIZE THE COUNTER

MOVAL,[I]

MOV BL, [DI]

MOV [SI], BL ; INTERCHANGE THE DATA

MOV [DI], AL INC

SI INC DI DEC CL

JNZ BACK MOV ; REPEAT UNTIL COUNTER BECOMES ZERO

AH, 4CH INT 21H END

OUTPUT:

BEFORE EXECUTION

	0	1	2	3	4	5	6	7	8	9	A	В	C	D	E	F
DS:0000	1	2	3	4	5	0	0	08	0	0	0	0	0	0	0	0
DS:0010	0	0	0	0	0	0	0	00	0	0	0	0	0	0	0	0
DS-0020	0	-0	0	0	0	0	0	-00	-0-	-0	0	0	0	0	0	<u> </u>

DS:0030 DS:0040	0 0	0	0	0	0	0	0	00	0	0	0	0	0	0	0	0
AFTER EXECUTION	N															
============	=															
	0	1	2	3	4	5	6	7	8	9	A	В	C	D	E	F
DS:0000	0	0	0	0	0	1	2	30	4	5	0	0	0	0	0	0
DS:0010	0	0	0	0	0	0	0	00	0	0	0	0	0	0	0	0
DS:0020	0	0	0	0	0	0	0	00	0	0	0	0	0	0	0	0
DS:0030	0	0	0	0	0	0	0	00	0	0	0	0	0	0	0	0
DS:0040	0	0	0	0	0	0	0	00	0	0	0	0	0	0	0	0

Result:

Program Is Executed Without Errors And The Output Is Verified

Verification And Validation:

Output Is Verified And Is Found Correct

Conclusion:

The Blocks Of Data Defined In The Program Is Interchanged And Output Is Verified.

Practical 2.(i) - Write an ALP to add 2 Multibyte numbers.

Aim: To Write An Alp To Add 2 Multibyte No.s

Software Required:

Masm 16 Bit

Algorithm:

- 1. Initialize the MSBs of sum to 0
- 2. Get the first number.
- 3. Add the second number to the first number.
- 4. If there is any carry, increment MSBs of sum by 1.
- 5. Store LSBs of sum. 6. Store MSBs of sum.

Program:

```
.MODEL SMALL
.DATA
           N1 DQ 122334455667788H; FIRST NUMBER N2 DQ
              122334455667788H; SECOND NUMBER
              SUM DT?
                                    ; INITIALIZE THE DATA REGISTER
.CODE
                                    ; POINTER TO FIRST NUMBER
           MOV AX, @DATA
                                    ; POINTER TO SECOND NUMBER
              MOV DS, AX
           LEA SI, N1
                                    ; COUNTER FOUR WORD
           LEA DI, N2 LEA
              BX, SUM
           MOV CL, 04H
                                    ;MOVE FIRST WORD
              CLC
BACK
           MOV AX, [SI]
              ADC AX, [DI]
            MOV [BX], AX
            INC SI INCSI
```

INC DI **INC** DI

OVER:

JNZ BACK ; REPEAT UNTIL COUNTER BECOMES JNC OVER **ZERO** MOV AX. 0001H MOV

MOV AH, 4CH INT 21H **END**

[BX], AX

OUTPUT:

BEFORE EXECUTION

	0	1	2	3	4	5	6	7	8	9	A	В	C	D	E	F
DS:0000	8	7	6	5	4	3	2	01	8	7	6	5	4	3	2	0
DS:0010	0	0	0	0	0	0	0	00	0	0	0	0	0	0	0	0
DS:0020	0	0	0	0	0	0	0	00	0	0	0	0	0	0	0	0
DS:0030	0	0	0	0	0	0	0	00	0	0	0	0	0	0	0	0
DS:0040	0	0	0	0	0	0	0	00	0	0	0	0	0	0	0	0

AFTER EXECUTION

	0	1	2	3	4	5	6	7	8	9	A	В	C	D	E	F
DS:0000	8	7	6	5	4	3	2	01	8	7	6	5	4	3	2	0
DS:0010	1	E	C	A	8	6	4	02	0	0	0	0	0	0	0	0
DS:0020	0	0	0	0	0	0	0	00	0	0	0	0	0	0	0	0
DS:0030	0	0	0	0	0	0	0	00	0	0	0	0	0	0	0	0
DS:0040	0	0	0	0	0	0	0	00	0	0	0	0	0	0	0	0

Result:

Program Is Executed Without Errors And The Output Is Verified

Verification And Validation:

Output Is Verified And Is Found Correct

Conclusion:

The Addition Of Two Multibye Data Is Done And The Output Is Verified

Practical 2.(ii) - Write an ALP to subtract two Multibyte numbers

Aim:

To Write An Alp To Subtract Two Multibyte Numbers

Software Required: MASM 16 BIT

Algorithm:

- Initialize the MSBs of difference to 0
- Get the first number 2.
- 3. Subtract the second number from the first number.
- If there is any borrow, increment MSBs of difference by 1. 4.
- 5. Store LSBs of difference
- 6. Store MSBs of difference

Program:

```
.MODEL SMALL
```

.DATA

; FIRST NUMBER N1 DQ 122334455667788H N2 DQ 11111111111111H ; SECOND NUMBER

RESULT DT?

.CODE

MOV AX, @DATA ; INITIALIZE THE DATA REGISTER

MOV DS, AX

LEA SI, N1 ; POINTER TO FIRST NUMBER LEA DI. N2 : POINTER TO SECOND NUMBER

LEA BX,MOV CX, RESULT 04H ; COUNTER FOUR WORD

CLC

BACK

; MOVE FIRST WORD MOV AX, [SI]

SBB AX, [DI] MOV [BX], AX INC SI

INC SI ; MOVE SI, DI CONTENTS

INC DI INC DI

INC BX

; INCREMENT BX TO STORE RESULTS

INC BX LOOP BACK MOV AH, 4CH INT 21H **END**

=======OUTPUT:

BEFORE EXECUTION ========

	0	12	3	4	5	6	7	8	9 A	В	C	DΕ	F	
DS:0000	4	EF	В	4	l C	2	00	8	765	4	3	2	0	
DS:0010 1 1 1	1 1	1 1 01	000	0.0	0.0	0 L	S:0020	00000	0 00 00 0	0 0 0	00	0 0		
DS:0030	0	0.0	0	0	0	0	00	0	$0 \ 0 \ 0$	0	0	0	0	
DS:0040	0	0.0	0	0	0	0	00	0	$0 \ 0 \ 0$	0	0	0	0	

AFTER EXECUTION

	0	1	2	3 4	56	7	8	9 A	В	C	D	E	F
DS:0000 4 E F	FB 4	C 2	0087	65432	2 0 DS:0	010 1	1111	1 1 01 7	654	432	10	DS:002	20
00000000	0 0	000	0000	0									
DS:0030	0	0 (0	0 0	0.0	00	0	0 0	0	0	0	0	0
DS:0040	0	0 (0	0 0	0.0	00	0	0 0	0	0	0	0	0

Result:

Program Is Executed Without Errors And The Output Is Verified

Verification And Validation:

Output Is Verified And Is Found Correct

Conclusion:

The Subtraction Of Two Multibye Data Is Done And The Output Is Verified

Practical 2.(iii)- Write An Alp To Multiply Two 16-Bit Numbers.

Aim:

To Write An Alp To Multiply Two 16-Bit Numbers

Software Required:

Masm 16 Bit

Algorithm:

- 1. Get The Multiplier.
- 2. Get The Multiplicand 3. Initialize The Product To 0.
- 4. Product = Product + Multiplicand
- 5. Decrement The Multiplier By 1
- 6. If Multiplicand Is Not Equal To 0, Repeat From Step (D) Otherwise Store The Product.

Program:

.MODEL SMALL .STACK .DATA

MULTIPLICAND DW 00FFH; FIRST WORD HERE MULTIPLIER DW 00FFH; SECOND WORD HERE

.CODE START: PRODUCT DW 2 DUP(0); RESULT OF MULIPLICATION HERE

MOV AX, @DATA MOV
DS, AX
MOV AX, MULTIPLICAND MUL
MULTIPLIER
MOV PRODUCT, AX MOV
PRODUCT+2,
DX MOV AH, 4CH
INT 21H END
START

==========											===	===== OUTPUT	' :
BEFORE EXECUTION	ON												
=======================================													
	0 1	2	3 4	5 6	7	8	9 A	В	C	D	E	F	
DS:0000	10	0	B 4	C 2	00	F	0 F	0	0	0	0	0	
DS:0010	0 0	0	0 0	0 0	00	0	0 0	0	0	0	0	0	
DS:0020	0 0	0	0 0	0 0	00	0	0 0	0	0	0	0	0	
DS:0030	0 0	0	0 0	0 0	00	0	0 0	0	0	0	0	0	
DS:0040	0 0	0	0 0	0 0	00	0	0 0	0	0	0	0	0	
AFTER EXECUTION	N												
=======================================													
	0 1	2	3 4	5 6	7	8	9 A	В	C	D	Е	F	
DS:0000	10	0	B 4	C 2	00	F	0 F	0	0	F	0	0	
DS:0010	0 0	0	0 0	0 0	00	0	0 0	0	0	0	0	0	
DS:0020	0 0	0	0 0	0 0	00	0	0 0	0	0	0	0	0	
DS:0030	0 0	0	0 0	0 (00 0	0	0 0	0	0	0	0	0	
DS:0040	0 0	0	0 0	0 (00 0	0	0 0	0	0	0	0	0	

=======================================	=======
Result: rogram Is Executed Without Errors And The Output Is Verified	
erification And Validation: utput Is Verified And Is Found Correct	
onclusion: the Multiplication Of Two 16 Bit Data Is Done And The Output Is Verified	

Practical 2.(iv) - Write an ALP to perform the conversion from BCD to binary.

AIM: TO DEVELOP AND EXECUTE AND ASSEMBLY LANGUAGE PROGRAM TO PERFORM THE CONVERSION FROM BCD TO BINARY

SOFTWARE REQUIRED: MASM 16 BIT

PROGRAM:

.MODEL SMALL

.DATA

. CODE

BCD_INPUT DB 61H ; BCD NUMBER IN_VALUE DB (?)

MOV AX, @DATA

MOV DS, AX
SINITIALIZE DATA SEGMENT MOV AL, BCD INPUT
MOV BL, AL
SINITIALIZE DATA SEGMENT MOV AL, BCD INPUT
SIN

AND AL, 0F0H MOV CL, 04H ROR AL, CL MOV BH, 0AH MUL BH

ADD AL, BL

MOV IN VALUE, AL ; STORE THE BINARY EQUIVALENT NUMBER MOV AH, 4CH

<u>INT 21H</u>

END ; END PROGRAM

OUTPUT:

BEFORE EXECUTION

	U	1	2	3	4	5	6	1	8	9	Α	В	C	D	E	F
DS:0000	6	0	0	0	0	0	0	00	0	0	0	0	0	0	0	0
DS:0010	0	0	0	0	0	0	0	00	0	0	0	0	0	0	0	0
DS:0020	0	0	0	0	0	0	0	00	0	0	0	0	0	0	0	0
DS:0030	0	0	0	0	0	0	0	00	0	0	0	0	0	0	0	0
DS:0040	0	0	0	0	0	0	0	00	0	0	0	0	0	0	0	0

AFTER EXECUTION

===========

	0	1	2	3	4	5	6	7	8	9	Α	В	C	D	Ε	F
DS:0000	6	3	0	0	0	0	0	00	0	0	0	0	0	0	0	0
DS:0010	0	0	0	0	0	0	0	00	0	0	0	0	0	0	0	0
DS:0020	0	0	0	0	0	0	0	00	0	0	0	0	0	0	0	0
DS:0030	0	0	0	0	0	0	0	00	0	0	0	0	0	0	0	0
DS:0040	0	0	0	0	0	0	0	00	0	0	0	0	0	0	0	0

RESULT: PROGRAM IS EXECUTED WITHOUT ERRORS AND THE OUTPUT IS VERIFIED

VERIFICATION AND VALIDATION: OUTPUT IS VERIFIED AND IS FOUND CORRECT

CONCLUSION: THE CONVERSION OF NUMBER FROM BCD TO BINARY IS DONE AND THE OUTPUT IS VERIFIED

Program 3.(i) - Write an ALP to separate odd and even numbers.

AIM: TO WRITE AN ALP TO SEPARATE ODD AND EVEN NUMBERS

SOFTWARE REQUIRED: MASM 16 BIT

PROGRAM:

.MODEL SMALL

.DATA

ARRAY DB 12H, 98H, 45H, 83H, 28H, 67H, 92H, 54H, 63H, 76H ARR_EVEN DB

10 DUP (?)

ARR_ODD DB 10 DUP (?)

. CODE

MOV AX, @DATA ; INITIALIZE THE DATA SEGMENT

MOV DS. AX

MOV CL, 0AH ; INITIALIZE THE COUNTER

 ${\bf XOR\ DI,\ DI} \qquad \qquad ; {\bf INITIALIZE\ THE\ ODD\ POINTER}$

XOR SI, SI ; INITIALIZE THE EVEN POINTER

BACK LEA BP, ARRAY

:

MOV AL, DS:[BP] ; GET THE NUMBER

TEST AL, 01H ; MASK ALL BITS EXCEPT LSB

 $\label{eq:JZNEXT} {\sf JZ\,NEXT} \qquad \qquad ; {\sf IF\,LSB} = 0 {\sf \,GOT\,\,TO\,\,NEXT}$

LEA BX, ARR ODD

MOV [BX+DI], AL

INC DI ; INCREMENT THE ODD

NEXT POINTER JMP SKIP

:

LEA BX, ARR_EVEN

MOV [BX+SI], AL

INC SI ; INCREMENT THE EVEN POINTER

SKIP:

INC BP ; INCREMENT ARRAY BASE POINTER

LOOP BACK ; DECREMENT THE

COUNTER MOV AH, 4CH

INT 21H

END; END PROGRAM

	===	===	===	===	===:	====	===		====	====	===	====	===	===	===:	====	===	=======================================
OUTPUT:																		
BEFORE I	EXI	ECU	TIO	N														
======	===	===	===															
		0	1	2	3	4	5	6	7	8	9	A	В	\mathbf{C}	D	\mathbf{E}	F	
DS:0000		1		9		4		8		2		6		9		54		6
7 0		0)	0		0		0		0 DS	5:00	10		0		0		0
0 0		0)	0		0		0		0		0		0		0		0
0 0				Ū		Ů	•	Ü		v		•		Ü		•		·
DS:0020		0	0	0	0	0	0	0	00	0	0	0	0	0	0	0	0	
DS:0030		0	0	0	0	0	0	0	00	0	0	0	0	0	0	0	0	
DS:0040		0	0	0	0	0	0	0	00	0	0	0	0	0	0	0	0	
DS.0040		U	U	U	U	U	U	U	vv	U	U	U	U	U	U	U	U	
A EWED EX		NT 17	TON	,														
AFTER EX	XEC	JUI	ION	_														
	===		≡ __		_		_	_	_				_	~	_	_	_	
		0	1	2	3	4	5	6	7	8	9	A	В	C	D		F	
DS:0000		1	9	4	8	2	6	9	54	6	7	1	9	2	9	5	7	
DS:0010		0	0	0	0	4	8	6	63	0	0	0	0	0	0	0	0	
DS:0020		0	0	0	0	0	0	0	00	0	0	0	0	0	0	0	0	
DS:0030		0	0	0	0	0	0	0	00	0	0	0	0	0	0	0	0	
DS:0040		0	0	0	0	0	0	0	00	0	0	0	0	0	0	0	0	

RESULT: PROGRAM IS EXECUTED WITHOUT ERRORS AND THE OUTPUT IS VERIFIED.

VERIFICATION AND VALIDATION: OUTPUT IS VERIFIED AND IS FOUND CORRECT

CONCLUSION: THE ODD AND EVEN NUMBERS ARE SEPERATED AND OUTPUT IS VERIFIED

Program 3.(ii) - Write an ALP to separate positive and negative numbers.

AIM: TO WRITE AN ALP TO SEPARATE POSITIVE AND NEGATIVE NUMBERS

SOFTWARE REQUIRED: MASM 16 BIT

PROGRAM:

.MODEL SMALL

.DATA

ARRAY DB 12H, -98H, -45H, 83H, -28H, 67H, 92H, -54H, -63H, 76H NEGI DB 10

DUP (?)

POSI DB 10 DUP (?)

. CODE

MOV AX, @DATA ; INITIALIZE THE DATA SEGMENT

MOV DS, AX

MOV CL, 0AH ; INITIALIZE THE COUNTER

XOR DI, DI ; INITIALIZE THE POINTER FOR NEGATIVE NUMBER XOR SI, SI ; INITIALIZE THE POINTER FOR POSITIVE NUMBER

LEA BP, ARRAY

BACK:

MOV AL, DS:[BP] ; GET THE NUMBER

LEA BX, NEGI MOV [BX+DI], AL

INC DI; INCREMENT THE NEGATIVE POINTER JMP SKIP

NEXT:

LEA BX, POSI MOV

[BX+SI], AL

INC SI; INCREMENT THE POSITIVE POINTER

SKIP: INC BP

; INCREMENT ARRAY BASE POINTER

LOOP BACK ; DECREMENT THE

COUNTER MOV AH, 4CH INT

21H

END; END PROGRAM

OUTPUT:

BEFORE EXECUTION

		===														
	0	1	2	3	4	5	6	7	8	9	A	В	C	D	E	F
DS:0000	1	6	В	8	D	6	9	A	9	7	0	0	0	0	0	0
DS:0010	0	0	0	0	0	0	0	00	0	0	0	0	0	0	0	0
DS:0020	0	0	0	0	0	0	0	00	0	0	0	0	0	0	0	0
DS:0030	0	0	0	0	0	0	0	00	0	0	0	0	0	0	0	0
DS-0040	0	0	0	0	0	0	0	-00	0	0	0	0	0	0	0	Λ

AFTER EXECUTION

	0	1	2	3	4	5	6	7	8	9	A	В	C	D	E	F
DS:0000	1	6	В	8	D	6	9	A	9	7	В	8	D	9	A	9
DS:0010	0	0	0	0	1	6	6	76	0	0	0	0	0	0	0	0
DS:0020	0	0	0	0	0	0	0	00	0	0	0	0	0	0	0	0
DS:0030	0	0	0	0	0	0	0	00	0	0	0	0	0	0	0	0
DS:0040	0	0	0	0	0	0	0	00	0	0	0	0	0	0	0	0

RESULT: PROGRAM IS EXECUTED WITHOUT ERRORS AND THE OUTPUT IS VERIFIED

VERIFICATION AND VALIDATION: OUTPUT IS VERIFIED AND IS FOUND CORRECT

CONCLUSION: THE POSITIVE AND NEGATIVE NUMBERS ARE SEPERATED AND OUTPUT IS VERIFIED

Program 3.(iii) - Write an ALP to check bitwise palindrome or not.

AIM: TO WRITE AN ALP TO CHECK BITWISE PALINDROME OR NOT

SOFTWARE REQUIRED: MASM 16 BIT

PROGRAM:

.MODEL SMALL

.STACK 100

PRINTSTRING MACRO MSG

MOV AH, 09H ; MACRO TO DISPLAY THE MESSAGE

MOV DX, OFFSET MSG

INT 21H ENDM

.DATA NUM DB 0FFH

TABLE DB 81H, 42H, 24H, 18H

MSG1 DB 'THE NUMBER EXHIBITS BITWISE PALINDROME:\$'

MSG2 DB 'THE NUMBER DOESNOT EXHIBITS BITWISE PALINDROM:\$'

; INITIALIZE THE DATA SEGMENT

<u>MOV AX, @DATA</u>

MOV DS, AX

LEA SI, TABLE ; SET COUNTER

MOV CX, 0004H ; CLEAR AX REGISTER

XOR AX, CX

L1: MOV AL, NUM

AND AL, [SI] JPE

<u>NEXT</u>

PRINTSTRING MSG2

JMP SKIP

; INCREMENT POINTER

; DISPLAY MESSAGE 2

NEXT: ; DECREMENT

DEC CX

COUNTER JNZ L1 ; DISPLAY MESSAGE 1

PRINTSTRING MSG1

SKIP: MOV AH, 4CH

INT 21H

END ; END PROGRAM

OUTPUT:

;C:\8086> ENTER THE FILE NAME

; THE NUMBER EXHIBITS BITWISE PALINDROME

RESULT: PROGRAM IS EXECUTED WITHOUT ERRORS AND THE OUTPUT IS VERIFIED.	
VERIFICATION AND VALIDATION: OUTPUT IS VERIFIED AND IS FOUND CORRECT. CONCLUSION: THE GIVEN NUMBER EXHIBITS BITWISE PALINDROME	

Program 4.(i) - Write an ALP to find largest number from a given array.

AIM: TO WRITE AN ALP TO FIND LARGEST NO FROM THE GIVEN ARRAY.

SOFTWARE REQUIRED: MASM 16 BIT

PROGRAM:

.MODEL SMALL

<u>.STACK 100</u>

.DATA

NUM DB 12H, 37H, 01H, 36H, 76H ; INITIALISE DATA

SMALL DB (?) ; TO STORE LARGEST NUM

.CODE

MOV AX, @DATA : INITIALIZE THE DATA SEGMENT

MOV DS, AX

MOV CL, 05H ; SET COUNTER

MOV AL, 00H LEA SI, NUM ; POINTER TO NUMBER

LOOP1:

CMP AL, [SI] ; COMPARE 1ST AND 2ND NUMBER

JNC LOOP2 MOV AL, [SI]

OUTPUT:

BEFORE EXECUTION

	0	1	2	3	4	5	6	7	8	9	Α	В	C	D	E	F
DS:0000	1	3	0	3	7	0	0	00	0	0	0	0	0	0	0	0
DS:0010	0	0	0	0	0	0	0	00	0	0	0	0	0	0	0	0
DS:0020	0	0	0	0	0	0	0	00	0	0	0	0	0	0	0	0
DS:0030	0	0	0	0	0	0	0	00	0	0	0	0	0	0	0	0
DS:0040	0	0	0	0	0	0	0	00	0	0	0	0	0	0	0	0

AFTER EXECUTION

	0	1	2	3	4	5	6	7	8	9	Α	В	C	D	E	F
DS:0000	1	3	0	3	7	7	0	00	0	0	0	0	0	0	0	0
DS:0010	0	0	0	0	0	0	0	00	0	0	0	0	0	0	0	0
DS:0020	0	0	0	0	0	0	0	00	0	0	0	0	0	0	0	0
DS:0030	0	0	0	0	0	0	0	00	0	0	0	0	0	0	0	0
DS:0040	0	0	0	0	0	0	0	00	0	0	0	0	0	0	0	0

RESULT: PROGRAM IS EXECUTED WITHOUT ERRORS AND THE OUTPUT IS VERIFIED.

VERIFICATION AND VALIDATION: OUTPUT IS VERIFIED AND IS FOUND CORRECT

CONCLUSION: THE LARGEST NUMBER IN THE GIVEN ARRAY IS 76 AND OUTPUT IS VERIFIED.

Practical 4.(ii) - WRITE AN ALP TO FIND SMALLEST NO FROM THE GIVEN ARRAY

AIM: TO WRITE AN ALP TO FIND SMALLEST NO FROM THE GIVEN ARRAY.

SOFTWARE REQUIRED: MASM 16 BIT

PROGRAM:

.MODEL SMALL .STACK 100

.DATA

NUM DB 12H, 37H, 01H, 36H, 76H ; INITIALISE DATA

; TO STORE SMALLEST NUM SMALL DB (?)

.CODE

; INITIALIZE THE DATA SEGMENT MOV AX, @DATA

MOV DS, AX ; SET COUNTER

MOV CL, 05H

MOV AL, 0FFH ; POINTER TO NUMBER

LEA SI, NUM

; COMPARE 1ST AND 2ND LOOP1

NUMBER CMP AL, [SI]

JC LOOP2

MOV AL, [SI]

LOOP2 INC SI

DEC

CL JNZ LOOP1

MOV SMALL, AL

MOV AH, 4CH

INT 21H

END : END PROGRAM

OUTPUT:

BEFORE EXECUTION

	0	1	2	3	4	5	6	7	8	9	\mathbf{A}	В	\mathbf{C}	D	\mathbf{E}	F
DS:0000	1	3	0	3	7	0	0	00	0	0	0	0	0	0	0	0
DS:0010	0	0	0	0	0	0	0	00	0	0	0	0	0	0	0	0
DS:0020	0	0	0	0	0	0	0	00	0	0	0	0	0	0	0	0
DS:0030	0	0	0	0	0	0	0	00	0	0	0	0	0	0	0	0
DS:0040	0	0	0	0	0	0	0	00	0	0	0	0	0	0	0	0

AFTER EXECUTION

		<u>=</u>														
	0	1	2	3	4	5	6	7	8	9	A	В	\mathbf{C}	D	\mathbf{E}	F
DS:0000	1	3	0	3	7	0	0	00	0	0	0	0	0	0	0	0
DS:0010	0	0	0	0	0	0	0	00	0	0	0	0	0	0	0	0
DS:0020	0	0	0	0	0	0	0	00	0	0	0	0	0	0	0	0
DS:0030	0	0	0	0	0	0	0	00	0	0	0	0	0	0	0	0
DS:0040	0	0	0	0	0	0	0	00	0	0	0	0	0	0	0	0

RESULT: PROGRAM IS EXECUTED WITHOUT ERRORS AND THE OUTPUT IS VERIFIED.

<u>VERIFICATION AND VALIDATION:</u> OUTPUT IS VERIFIED AND IS FOUND CORRECT. **CONCLUSION:** THE SMALLEST IN THE GIVEN NUMBER IS 01 AND OUTPUT IS VERIFIED

Practical 4.(iii) - WRITE AN ALP TO SORT A GIVEN SET OF 16BIT UNSIGNED INTEGERS INTO ASCENDING ORDER USING BUBBLE SORT ALGORITHM

<u>AIM:</u> TO WRITE AN ALP TO SORT A GIVEN SET OF 16BIT UNSIGNED INTEGERS INTO ASCENDING ORDER USING BUBBLE SORT ALGORITHM **SOFTWARE REQUIRED**: MASM 16 BIT

PROGRAM:

.MODEL SMALL

.DATA

A DB 23H, 45H, 55H, 22H, 64H ; INITIALISE DATA SIZE1 DW (\$-A) $\,$

; CALCULATE SIZE OF NUMBERS

CODE MOV AX, @DATA MOV

DS, AX

; INITIALIZE THE DATA SEGMENT

MOV BX, SIZE1 ; THE NO. OF DATA BYTES IS INITIALIZE IN BX

DEC BX **OUTLOOP**:

MOV CX, BX ; SAVE COUNTER IN CX REGISTER

MOV SI, 00 ; INITIALISE POINTER

INLOOP:

MOV AL, A[SI] ; LOAD THE DATA INTO AL POINTED BY SI

INC SI ; INCREMENT THE POINTER CMP AL, A[SI] ; IS CONTENT OF AL<SI POINTED

JB NEXT ; YES, GO NEXT

XCHG AL, A[SI] ; NO, EXCHANGE TWO DATA

MOV A[SI-1], AL ; MOVE TILL END OF

MEMORY

NEXT LOOP INLOOP

: DEC BX

JNZ OUTLOOP MOV AH, 4CH

INT 21H

END ; END PROGRAM

OUTPUT:

BEFORE EXECUTION

	==															
	0	1	2	3	4	5	6	7	8	9	A	В	\mathbf{C}	D	\mathbf{E}	F
DS:0000	В	4	\mathbf{C}	2	2	4	5	22	6	0	0	0	0	0	0	0
DS:0010	0	0	0	0	0	0	0	00	0	0	0	0	0	0	0	0
DS:0020	0	0	0	0	0	0	0	00	0	0	0	0	0	0	0	0
DS:0030	0	0	0	0	0	0	0	00	0	0	0	0	0	0	0	0
DS:0040	0	0	0	0	0	0	0	00	0	0	0	0	0	0	0	0

AFTER EXECUTION

		=															
	0	1	2	3	4	5	6	7	8	9	\mathbf{A}	В	\mathbf{C}	D	\mathbf{E}	F	
DS:0000	В	4	\mathbf{C}	2	2	2	4	55	6	0	0	0	0	0	0	0	
DS:0010	0	0	0	0	0	0	0	00	0	0	0	0	0	0	0	0	
DS:0020	0	0	0	0	0	0	0	00	0	0	0	0	0	0	0	0	
DS:0030	0	0	0	0	0	0	0	00	0	0	0	0	0	0	0	0	

DS:0040
RESULT: PROGRAM IS EXECUTED WITHOUT ERRORS AND THE OUTPUT IS VERIFIED
VERIFICATION AND VALIDATION: OUTPUT IS VERIFIED AND IS FOUND CORRECT
CONCLUSION: THE GIVEN NUMBERS ARE ARRANGED IN ASCENDING ORDER AND THE OUTPUT IS VERIFIED

