

FileResetAssemblerDebugHelp

Registers

A00

BC0600

DE0000

HL0D0B

PSW0000

PC4222

SPFFFF

Int-Reg00

Flag

S0

Z1

AC0

P1

C0

Decimal - Hex Conversion

Decimal

Hex

00

00

To Hex

To Dec

I/O Ports

0-+00

Update Port Value

Memory

0-+00

Update Memory

Load me at

1

2

3

4

5

6

7

8

9

10

11

12

13

14

15

16

17

18

19

20

21

22

23

24

25

26

27

28

29

; <Program title>

jmp start

;data

;code

start: nop

LOOP: LXI H, 3500

MVI D, 00

MVI C, 05

LOOP1: MOV A, H

INX H

CMP M

JNC LOOP2

MOV B, M

MOV M, A

DCX H

MOV M, B

INX H

MVI D, 01

LOOP2: DCR C

JNZ LOOP1

MOV A, D

RRC

JC LOOP

HLT

DataStackKeyPadMemoryI/O Ports

Start3500OK

Address (Hex)AddressData

0DAC35009

0DAD35016

0DAE35020

0DAF35030

0DB035040

0DB135050

0DB235060

0DB335070

0DB435080

0DB535090

0DB635100

0DB735110

0DB835120

0DB935130

Line NoAssembler Message

0Program assembled successfully

Simulator: Idle

Search

23:4421-06-2024



GNUSim8085 - 8085 Microprocessor Simulator

FileResetAssemblerDebugHelp

Registers

A04

BC0000

DE0000

HL0804

PSW0000

PC4219

SPFFFF

Int-Reg00

Flag

S0

Z1

AC0

P1

C0

Decimal - Hex Conversion

Decimal

0

Hex

0

To Hex

To Dec

I/O Ports

0

-

+

00

Update Port Value

Memory

0

-

+

00

Update Memory

Load me at

1

2

3

4

5

6

7

8

9

10

11

12

13

14

15

16

17

18

19

20

21

22

23

<Program title>

jmp start

:data

:code

start: nop

LXI H,2050

MOV C,M

DCR C

INX H

MOV A,M

LOOP1: INX H

CMP M

JC LOOP

MOV A,M

LOOP: DCR C

JNZ LOOP1

STA 2050

HLT

DataStackKeyPadMemoryI/O Ports

Start

2050

OK

Address (Hex)

Address

Data

0802

2050

0

0803

2051

6

0804

2052

4

0805

2053

0

0806

2054

0

0807

2055

0

0808

2056

0

0809

2057

0

080A

2058

4

080B

2059

0

080C

2060

0

080D

2061

0

080E

2062

0

080F

2063

0

Line No

Assembler Message

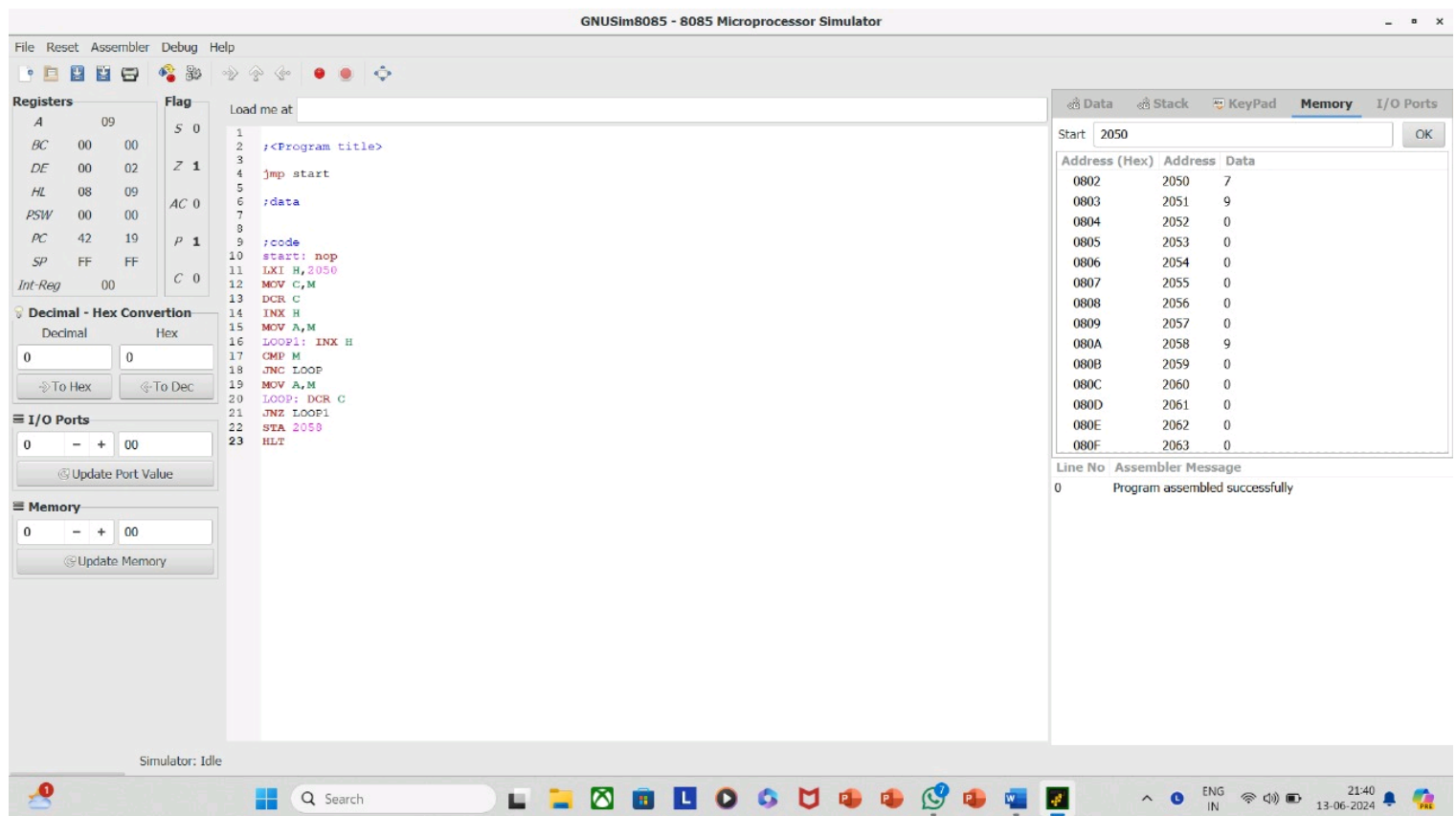
0

Program assembled successfully

Simulator: Idle

23:22

21-06-2024



FileResetAssemblerDebugHelp

Registers

A00

BC0800

DE0000

HL0D0B

PSW0000

PC4222

SPFFFF

Int-Reg00

Flag

S0

Z1

AC0

P1

C0

Decimal - Hex Conversion

Decimal

Hex

00

00

To Hex

To Dec

I/O Ports

0-+00

Update Port Value

Memory

0-+00

Update Memory

Load me at

1

2

3

4

5

6

7

8

9

10

11

12

13

14

15

16

17

18

19

20

21

22

23

24

25

26

27

28

29

; <Program title>

jmp start

;data

;code

start: nop

LOOP: LXI H, 3500

MVI D, 00

MVI C, 05

LOOP1: MOV A, H

INX H

CMP M

JNC LOOP2

MOV B, M

MOV M, A

DCX H

MOV M, B

INX H

MVI D, 01

LOOP2: DCR C

JNZ LOOP1

MOV A, D

RRC

JC LOOP

HLT

DataStackKeyPadMemoryI/O Ports

Start3500OK

Address (Hex)

Address

Data

0DAC

3500

8

0DAD

3501

7

0DAE

3502

0

0DAF

3503

0

0DB0

3504

0

0DB1

3505

0

0DB2

3506

0

0DB3

3507

0

0DB4

3508

0

0DB5

3509

0

0DB6

3510

0

0DB7

3511

0

0DB8

3512

0

0DB9

3513

0

Line No

Assembler Message

0

Program assembled successfully

Simulator: Idle

23:39

21-06-2024

ENG IN

Search

21-06-2024



GNUSim8085 - 8085 Microprocessor Simulator

FileResetAssemblerDebugHelp

Registers

A01

BC0000

DE0000

HL1F48

PSW0000

PC421B

SPFFFF

Int-Reg00

Flag

S0

Z1

AC0

P1

C0

Decimal - Hex Conversion

Decimal

0

Hex

0

To Hex

To Dec

I/O Ports

0

-

+

00

Update Port Value

Memory

0

-

+

00

Update Memory

Load me at

1

2

3

4

5

6

7

8

9

10

11

12

13

14

15

16

17

18

19

20

21

22

23

24

<Program title>

jmp start

:data

:code

start: nop

LXI H, 8000

MOV C, M

MVI A, 00

MOV B, A

LOOP: ADD C

JNC SKIP

INR B

SKIP: DCR C

JNZ LOOP

LXI H, 8007

MOV M, A

INX H

MOV M, H

HLT

DataStackKeyPadMemoryI/O Ports

Start

8000

OK

Address (Hex)

Address

Data

1F40

8000

1

1F41

8001

2

1F42

8002

3

1F43

8003

4

1F44

8004

5

1F45

8005

6

1F46

8006

7

1F47

8007

1

1F48

8008

0

1F49

8009

0

1F4A

8010

0

1F4B

8011

0

1F4C

8012

0

1F4D

8013

0

Line No

Assembler Message

0

Program assembled successfully

Simulator: Idle

23:35

21-06-2024