

# Iris: Higher-Order Concurrent Separation Logic

## Lecture 11: CAS and Spin Locks

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# Overview

Earlier:

- ▶ Operational Semantics of  $\lambda_{\text{ref},\text{conc}}$ 
  - ▶  $e, (h, e) \rightsquigarrow (h, e')$ , and  $(h, \mathcal{E}) \rightarrow (h', \mathcal{E}')$
- ▶ Basic Logic of Resources
  - ▶  $I \hookrightarrow v, P * Q, P \multimap Q, \Gamma \mid P \vdash Q$
- ▶ Basic Separation Logic
  - ▶  $\{P\} e \{v.Q\} : \text{Prop}, \text{isList } l \text{ xs, ADTs, foldr}$
- ▶ Later ( $\triangleright$ ) and Persistent ( $\Box$ ) Modalities.
- ▶ Concurrency Intro and Invariants.
- ▶ Ghost State

Today:

- ▶ Specification and proof of lock module and client thereof
- ▶ Key Points:
  - ▶ Programming with and reasoning about uses of CAS.
  - ▶ Coarse and fine-grained concurrency.

# Outline

- ▶ We consider a lock module.
- ▶ Note that our programming language  $\lambda_{\text{ref}, \text{conc}}$  does not include primitive locks – it is a feature of Iris that we can give expressive specifications to synchronization primitives programmed using [cas](#).
- ▶ We then consider a client of the lock module, a concurrent bag implementation.
- ▶ Finally, we show how to verify an implementation of the lock module.

# Lock Module Specification

$\exists \text{isLock} : \text{Val} \rightarrow \text{Prop} \rightarrow \text{GhostName} \rightarrow \text{Prop}.$

$\exists \text{locked} : \text{GhostName} \rightarrow \text{Prop}.$

$\forall P, v, \gamma. \text{isLock}(v, P, \gamma) \Rightarrow \Box \text{isLock}(v, P, \gamma)$

$\wedge \quad \forall \gamma. \text{locked}(\gamma) * \text{locked}(\gamma) \Rightarrow \text{False}$

$\wedge \quad \forall P. \{P\} \text{newLock}() \{v. \exists \gamma. \text{isLock}(v, P, \gamma)\}$

$\wedge \quad \forall P, v, \gamma. \{\text{isLock}(v, P, \gamma)\} \text{acquire } v \{v.P * \text{locked}(\gamma)\}$

$\wedge \quad \forall P, v, \gamma. \{\text{isLock}(v, P, \gamma) * P * \text{locked}(\gamma)\} \text{release } v \{ \_ . \text{True} \}$

## Remarks on lock spec

- ▶ `isLock` is persistent, hence duplicable, hence may can be shared among several threads, who will use the lock to coordinate access to shared memory.
- ▶ Quantification over  $P$ : the  $P$  predicates describes the resources the lock protects.
- ▶ Note the ownership transfer in acquire and release.
- ▶ The `locked( $\gamma$ )` predicate (think of it as a token) is used to ensure that only the thread who has acquired the lock can release it – it is not duplicable since that would defeat its purpose.
- ▶ A *higher-order (3rd-order)* specification.
  - ▶ the order of the  $\exists \text{isLock}$  and  $\forall P$  quantifiers is not accidental: see [iCap paper](#) for an example, where this is crucial.
- ▶ Note that the specification only talks about resources (no mention of mutual exclusion and interleavings).

## Client of Lock Module

- ▶ We now consider client of the lock module:
- ▶ A Concurrent (coarse-grained) bag.
- ▶ Note: **we prove the client relative to the lock module spec**, before considering an implementation of the lock module.
- ▶ **MODULARITY !**

## Bag Specification

$\exists \text{isBag} : (Val \rightarrow \text{Prop}) \times Val \rightarrow \text{Prop}.$

$\forall (\Phi : Val \rightarrow \text{Prop}).$

$\forall b. \text{isBag}(\Phi, b) \Rightarrow \Box \text{isBag}(\Phi, b)$

$\wedge \quad \{\text{True}\} \text{newBag}() \{b. \text{isBag}(\Phi, b)\}$

$\wedge \quad \forall bu. \{\text{isBag}(\Phi, b) * \Phi(u)\} \text{insert } b \ u \ \{ \_ . \text{True} \}$

$\wedge \quad \forall b. \{\text{isBag}(\Phi, b)\} \text{remove } b \ \{v. v = \text{None} \vee \exists x. v = \text{Some } x \wedge \Phi(x)\}$

- Note: for concurrent use (isBag is persistent).
- Hence we do not keep track of which elements the bag precisely contains.

## Client of Bag Module

Trivial client:

```
{True}
let b = newBag() in
{isBag(even, b)}
{isBag(even, b) * isBag(even, b)}
{isBag(even, b)}      {isBag(even, b)}
  insert(b, 4)      ||      remove(b)
{_.True}             {_.True}
```

See [Hocap paper](#) for a realistic client (a concurrent runner).



# Bag Implementation

- ▶ We represent the bag by a pair consisting of
  - ▶ a reference to a (functional) list of values
  - ▶ a lock, used to protect access to the list of values

# Bag Implementation

```
let newBag =  $\lambda_{..}$ . (ref(None), newLock())  
let insert =  $\lambda x$ .  $\lambda v$ . let  $\ell = \pi_1 x$  in  
    let lock =  $\pi_2 x$  in  
    acquire lock;  
     $\ell \leftarrow \text{Some}(v, !\ell)$ ;  
    release lock  
let remove =  $\lambda x$ . let  $\ell = \pi_1 x$  in  
    let lock =  $\pi_2 x$  in  
    acquire lock;  
    let  $r = \text{match } !\ell \text{ with}$   
        None  $\Rightarrow$  None  
        | Some  $p \Rightarrow \ell \leftarrow \pi_2 p$ ; Some( $\pi_1 p$ )  
    end  
in release lock;  $r$ 
```

## Remark: limitations

- ▶ An implementation in which `insert` simply returns `unit` and in which `remove` simply returns `None` would also satisfy the specification.
- ▶ Note that such implementations would also be *safe*!
- ▶ But not as intended.
- ▶ Similar problem: if we forget to call `release`, then we can still verify the `insert` method.
- ▶ The problem is that Iris is *affine*, we can forget about resources.
- ▶ See [Iron paper](#) for a proposal of variant of Iris, which can address these issues.

## Proof of Bag Spec

- ▶ The isBag predicate is defined as follows:

$$\text{isBag}(\Phi, b) = \exists \ell v \gamma. b = (\ell, v) \wedge \text{isLock}(v, \exists xs. \ell \hookrightarrow xs * \text{bagList}(\Phi, xs), \gamma)$$

where bagList is defined by guarded recursion as the unique predicate satisfying

$$\text{bagList}(\Phi, xs) = xs = \text{None} \vee \exists x. \exists r. xs = \text{Some}(x, r) \wedge \Phi(x) * \triangleright(\text{bagList}(\Phi, r)).$$

- ▶ Let  $\Phi : Val \rightarrow \text{Prop}$  be arbitrary.
- ▶ Note that  $\text{isBag}(\Phi, b)$  is persistent, for any  $b$  (why?)
- ▶ Showing the specs for newBag and insert is left as exercise.

## Proof of remove

► TS

$$\{\text{isBag}(\Phi, b)\} \text{ remove } b \{v.v = \text{None} \vee \exists x. v = \text{Some } x \wedge \Phi(x)\}$$

► By def'n of  $\text{isBag}(\Phi, b)$ , using HT-EXIST, and HT-ALWAYS together with HT-EQ, SFTS

$$\{\text{isLock}(\text{lock}, \exists xs. \ell \hookrightarrow xs * \text{bagList}(\Phi, xs), \gamma)\} \text{ remove}(\ell, \text{lock}) \{u.u = \text{None} \vee \exists x. u = \text{Some } x \wedge \Phi(x)\}$$

for some  $\ell$ , lock and  $\gamma$ .

► By HT-BETA and HT-LET-DET, SFTS

$$\{\text{isLock}(\text{lock}, \exists xs. \ell \hookrightarrow xs * \text{bagList}(\Phi, xs), \gamma)\} e \{u.u = \text{None} \vee \exists x. u = \text{Some } x \wedge \Phi(x)\}$$

where  $e$  is the program

acquire lock;

let  $r =$  match ! $\ell$  with

None  $\Rightarrow$  None

| Some  $p \Rightarrow \ell \leftarrow \pi_2 p$ ; Some( $\pi_1 p$ )

end

in release lock;  $r$

# Proof of remove

- ▶ Using HT-SEQ and spec for acquire – SFTS

$$\{\text{locked}(\gamma) * \exists xs. \ell \hookrightarrow xs * \text{bagList}(\Phi, xs)\} e' \{u.u = \text{None} \vee \exists x. u = \text{Some } x \wedge \Phi(x)\}$$

where  $e'$  is the part of program  $e$  after acquire.

- ▶ Use that  $\exists$  and  $\vee$  distribute over  $*$ , HT-EXIST, and def'n of  $\text{bagList}(\Phi, xs)$ , with HT-DISJ we consider two cases.
- ▶ The first case is

$$\{\text{locked}(\gamma) * \ell \hookrightarrow xs * xs = \text{None}\} e' \{u.u = \text{None} \vee \exists x. u = \text{Some } x \wedge \Phi(x)\}$$

Left as exercise!

- ▶ In the second case, after structural rules SFTS:

$$\{\text{locked}(\gamma) * \ell \hookrightarrow \text{Some}(x, r) * \Phi(x) * \triangleright \text{bagList}(\Phi, r)\} e' \{u.\exists x. u = \text{Some } x \wedge \Phi(x)\}.$$

# Proof of remove

- ▶ We use HT-LET-DET. For the first premise we show

$$\{\text{locked}(\gamma) * \ell \hookrightarrow \text{Some}(x, r) * \Phi(x) * \triangleright \text{bagList}(\Phi, r)\}$$

match ! $\ell$  with

None  $\Rightarrow$  None

| Some  $p \Rightarrow \ell \leftarrow \pi_2 p; \text{Some}(\pi_1 p)$

end

$$\{u.u = \text{Some } x \wedge \ell \hookrightarrow r * \Phi(x) * \text{locked}(\gamma) * \text{bagList}(\Phi, r)\}$$

(note the omission of  $\triangleright$  on bagList in the postcondition)

- ▶ See notes.

## Proof of remove

- ▶ For the second premise of the rule HT-LET-DET, SFTS

$$\begin{array}{c} \{\ell \hookrightarrow r * \Phi(x) * \text{locked}(\gamma) * \text{bagList}(\Phi, r)\} \\ \text{release lock; Some } x \\ \{u. \exists x. u = \text{Some } x \wedge \Phi(x)\} \end{array}$$

- ▶ We use sequencing rule together with the release spec to give away the resources  $\ell \hookrightarrow r$ ,  $\text{locked}(\gamma)$  and  $\text{bagList}(\Phi, r)$  back to the lock.
- ▶ We are left with proving

$$\begin{array}{c} \{\Phi(x)\} \\ \text{Some } x \\ \{u. \exists x. u = \text{Some } x \wedge \Phi(x)\} \end{array}$$

which is immediate.



## Spin lock implementation

- ▶ We now return to lock module and show that a spin lock implementation satisfies the lock module spec.
- ▶ The lock is implemented by a boolean flag:

```
let newLock() = ref(false)
let acquire l = if cas(l, false, true) then () else acquire l
let release l = l ← false
```

## Spin lock implementation

- ▶ We now return to lock module and show that a spin lock implementation satisfies the lock module spec.
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let acquire l = if cas(l, false, true) then () else acquire l
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```

- ▶ We now proceed to prove that this implementation meets the specification of the spin lock module. We do it in quite a lot of detail, since this example is very instructive! First, we need a proof rule for CAS.

# Proof rule for CAS

Basic proof rule for CAS:

HT-CAS

$$\frac{}{\{\triangleright \ell \hookrightarrow v\} \text{cas}(\ell, v_1, v_2) \{u. (u = \text{true} * v = v_1 * \ell \hookrightarrow v_2) \vee (u = \text{false} * v \neq v_1 * \ell \hookrightarrow v)\}}$$

Often the following derived rules are easier to use.

HT-CAS-SUCC

$$\frac{}{\{\triangleright \ell \hookrightarrow v_1\} \text{cas}(\ell, v_1, v_2) \{u. u = \text{true} * \ell \hookrightarrow v_2\}}$$

HT-CAS-FAIL

$$\frac{}{\{\triangleright \ell \hookrightarrow v * \triangleright (v \neq v_1)\} \text{cas}(\ell, v_1, v_2) \{u. u = \text{false} * \ell \hookrightarrow v\}}$$

## Proof of Spin Lock Spec

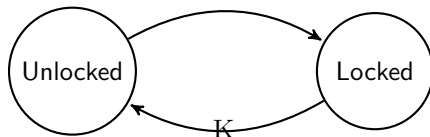
- ▶ Need to record whether lock is in a locked or unlocked state.
- ▶ Use resource algebra  $\{\varepsilon, \perp, K\}$ , with  $\varepsilon \cdot x = x \cdot \varepsilon = x$  and otherwise  $x \cdot y = \perp$ .
- ▶ isLock predicate:

$$\text{isLock}(v, P, \gamma) = \exists \ell \in \text{Loc}, \iota \in \text{InvName}. v = \ell \wedge \boxed{I(\ell, P, \gamma)}^\iota$$
$$\text{locked}(\gamma) = \boxed{K}^\gamma$$

where the invariant is

$$I(\ell, P, \gamma) = \ell \hookrightarrow \text{false} * \boxed{K}^\gamma * P \vee \ell \hookrightarrow \text{true}.$$

- ▶ Intuition:



## Remarks on the Invariant

- ▶ Notice that the invariant involves a predicate variable  $P$ .
- ▶ Hence the predicate in the invariant is *not* timeless.
- ▶ This is an example where it is important the invariant opening rule includes a  $\triangleright$  modality.
- ▶ Observe that the predicate variable  $P$  appears in the invariant, because we want to prove the very *general and modular* specification for the lock, which involves quantification over any predicate  $P$ .

## Proof of spin lock spec

- ▶ There are now five proof obligations, one for each of the conjuncts in the specification.
- ▶ The first says that  $\text{isLock}(v, P, \gamma)$  is persistent: clear because invariants and equality are persistent, and  $\wedge, \exists$  preserves persistency.
- ▶ The second says that  $\text{locked}(\gamma)$  *is not* duplicable. This follows as  $K \cdot K = \perp$  by definition of the resource algebra:  $[\underline{\underline{K}}]^\gamma * [\underline{\underline{K}}]^\gamma \vdash [\underline{\underline{K \cdot K}}]^\gamma$  by OWN-OP which yields False by OWN-VALID.
- ▶ Now consider each operation.

## Proof of newLock

- ▶ TS

$$\{P\} \text{ newLock}() \{v. \exists \gamma. \text{isLock}(v, P, \gamma)\}$$

- ▶ By HT-BETA, SFTS

$$\{P\} \text{ref}(\text{false}) \{v. \exists \gamma. \text{isLock}(v, P, \gamma)\}$$

- ▶ We allocate new ghost state by GHOST-ALLOC, use consequence HT-EXIST. We are left with proving

$$\{\text{locked}(\gamma) * P\} \text{ref}(\text{false}) \{v. \text{isLock}(v, P, \gamma)\}$$

for some  $\gamma$ .

- ▶ Exercise!

## Proof of acquire

- It is recursive, so we use derived rule for recursive functions, *i.e.*, we assume

$$\forall v, P, \gamma. \{\triangleright \text{isLock}(v, P, \gamma)\} \text{acquire } v \{v.P * \text{locked}(\gamma)\} \quad (1)$$

and then show

$$\{\text{isLock}(v, P, \gamma)\} \text{if } \text{cas}(v, \text{false}, \text{true}) \text{ then } () \text{ else } \text{acquire}(v) \{v.P * \text{locked}(\gamma)\}.$$

- By isLock def'n,  $v$  is a location  $\ell$  governed by an invariant, which we can move into the context as follows:

$$\boxed{I(\ell, P, \gamma)}^{\ell} \vdash \{\text{True}\} \text{if } \text{cas}(\ell, \text{false}, \text{true}) \text{ then } () \text{ else } \text{acquire}(\ell) \{v.P * \text{locked}(\gamma)\}$$

- We next use HT-BIND – and start by showing

$$\boxed{I(\ell, P, \gamma)}^{\ell} \vdash \{\text{True}\} \text{cas}(\ell, \text{false}, \text{true}) \{u.(u = \text{true} * P * \text{locked}(\gamma)) \vee (u = \text{false})\}.$$



## Proof of acquire

- ▶ As **cas** is atomic, we open the invariant to get at  $\ell$ , using HT-INV-OPEN. So SFTS

$$\boxed{I(\ell, P, \gamma)}^{\ell} \vdash \{\triangleright I(\ell, P, \gamma)\} \text{cas}(\ell, \text{false}, \text{true}) \{u.((u = \text{true} * P * \text{locked}(\gamma)) \vee (u = \text{false})) * \triangleright I(\ell, P, \gamma)\}.$$

- ▶ We proceed by cases on the invariant (using HT-DISJ). In the first case, TS

$$\boxed{I(\ell, P, \gamma)}^{\ell} \vdash$$

$$\{\triangleright(\ell \hookrightarrow \text{false} * \text{locked } \gamma * P)\} \text{cas}(\ell, \text{false}, \text{true}) \{u.(u = \text{true} * P * \text{locked}(\gamma) \vee (u = \text{false})) * \triangleright I(\ell, P, \gamma)\}.$$

- ▶ We use HT-CAS-SUCC and HT-FRAME to get  $u = \text{true} * P * \text{locked}(\gamma) * \ell \hookrightarrow \text{true}$ , which satisfies the disjunctions in the postcondition (also the one hidden in  $I(\ell, P, \gamma)$ ).
- ▶ In the second case, TS

$$\boxed{I(\ell, P, \gamma)}^{\ell} \vdash$$

$$\{\triangleright(\ell \hookrightarrow \text{true})\} \text{cas}(\ell, \text{false}, \text{true}) \{u.((u = \text{true} * P * \text{locked}(\gamma) \vee (u = \text{false})) * \triangleright I(\ell, P, \gamma)\}.$$

- ▶ We use consequence and HT-CAS-FAIL, which yields postcondition  $u = \text{false} * \ell \hookrightarrow \text{true}$ .

## Proof of acquire

- ▶ We now proceed with our use of HT-BIND, the evaluation of the **if**, and thus SFTS

$$\boxed{I(\ell, P, \gamma)}^{\iota} \vdash \{u = \text{true} * P * \text{locked}(\gamma) \vee u = \text{false}\} \text{ if } u \text{ then } () \text{ else acquire } \ell \{ \dots P * \text{locked}(\gamma) \}$$

- ▶ We consider the two cases in the precondition, using HT-DISJ.
- ▶ We use HT-IF-TRUE and HT-IF-FALSE in the first and second case respectively, so SFTS

$$\boxed{I(\ell, P, \gamma)}^{\iota} \vdash \{P * \text{locked}(\gamma)\} () \{ \dots P * \text{locked}(\gamma) \}$$

$$\boxed{I(\ell, P, \gamma)}^{\iota} \vdash \{\text{True}\} \text{ acquire } \ell \{ \dots P * \text{locked}(\gamma) \}$$

- ▶ The first follows by the rule for the unit expressions, the second by our induction hypothesis (1). Done!

## Proof of release

► TS

$$\{\text{isLock}(v, P, \gamma) * P * \text{locked}(\gamma)\} \text{ release } v \{ \_.\text{True} \}$$

► By  $\text{isLock}(v, P, \gamma)$  def'n  $v = \ell$  for some  $\ell$ , and by HT-BETA SFTS

$$\{\boxed{I(\ell, P, \gamma)}^\ell * P * \text{locked}(\gamma)\} \ell \leftarrow \text{false} \{ \_.\text{True} \}$$

► Invariants are persistent, hence we move it into context, and then use HT-INV-OPEN. SFTS

$$\boxed{I(\ell, P, \gamma)}^\ell \vdash \{ \triangleright I(\ell, P, \gamma) * P * \text{locked}(\gamma) \} \ell \leftarrow \text{false} \{ \_.\triangleright I(\ell, P, \gamma) \}$$

# Proof of release

- ▶ We consider two cases, based on the disjunction in  $I(\ell, P, \gamma)$  in the precondition.
- ▶ The first case is

$$\boxed{I(\ell, P, \gamma)}^{\ell} \vdash \{\triangleright (\ell \hookrightarrow \text{false} * \text{locked}(\gamma) * P) * P * \text{locked}(\gamma)\} \ell \leftarrow \text{false} \{ \dots \triangleright I(\ell, P, \gamma) \}$$

which is inconsistent as  $\text{locked}(\gamma) * \text{locked}(\gamma) \vdash \text{False}$ . Hence done by HT-LATER-FALSE.

- ▶ In the second case we need to prove

$$\boxed{I(\ell, P, \gamma)}^{\ell} \vdash \{\triangleright (\ell \hookrightarrow \text{true}) * P * \text{locked}(\gamma)\} \ell \leftarrow \text{false} \{ \dots \triangleright I(\ell, P, \gamma) \}$$

- ▶ In the postcondition we show the first disjunct; by consequence SFTS

$$\boxed{I(\ell, P, \gamma)}^{\ell} \vdash \{\triangleright (\ell \hookrightarrow \text{true}) * \triangleright (P * \text{locked}(\gamma))\} \ell \leftarrow \text{false} \{ \dots \triangleright (\ell \hookrightarrow \text{false}) * \triangleright (\text{locked}(\gamma) * P) \}$$

which holds by the frame rule and HT-STORE.