SREE VIDYANIKETHAN ENGINEERING COLLEGE
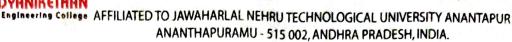
(AUTONOMOUS)

SREE SAINATH NAGAR, A. RANGAMPET - 517 102, ANDHRA PRADESH, INDIA

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GRADE SHEET

EXAMINATION:	II B.Tech I Sem.Regular
BRANCH:	INFORMATION TECHNOLOGY
NAME OF THE STUDENT:	JAINI VENKATA PAVITHRA LAKSHMI
NAME OF PARENT:	JAINI VENKATA SREERAMULU

HALL TICKET NO.:	15121A1238
MONTH & YEAR OF EXAM.	November 2016
Sl.No. G.S.	009899

S. No.	SUBJECT CODE	SUBJECT TITLE	INTERNAL MARKS	EXTERNAL MARKS	TOTAL MARKS	GRADE POINT	LETTER GRADE	CREDITS
1	14BT3BS03	PROBABILITY AND STATISTICS	30	70	100	10	S	3
2	14BT31201	DISCRETE MATHEMATICAL STRUCTURES	25	53	78	8	A	3
3	14BT30235	BASIC ELECTRICAL ENGINEERING	24	53	77	8	A	3
4	14BT30431	ELECTRONIC DEVICES AND CIRCUITS	24	33	57	6	C	3
5	14BT30501	DATA STRUCTURES	30	43	73	7	B	3
6	14BT30502	DIGITAL LOGIC DESIGN	24	51	75	8	A	3
7	14BT30422	ANALOG AND DIGITAL ELECTRONICS LAB	21	39	60	8	A	2
8	14BT30521	DATA STRUCTURES LAB	23	44	67	9	O	2
		TOTAL	201	386	587			22

GPA: 7.95

Date: 27th January 2017

CONTROLLER OF EXAMINATIONS

PRINCIPAL