8 total states

Name	Value
clk	0
rst	1
fsm_input[3:0]	0
reg1_in[7:0]	30
reg2_in[7:0]	5c
alu_result[7:0]	00
sseg_out_hi[6:0]	1111110
sseg_out_lo[6:0]	1111110

fsm_input	reg1_in	reg2_in	alu_result	sseg_out_hi	sseg_out_lo
0	30	5c	00	1111110	1111110
1			8c	1111111	1001110
2			d4	0111101	0110011
3			cf	1001110	1000111
4			ef	1001111	1111001
			83	1111111	

Name	Value
clk	0
rst	1
fsm_input[3:0]	0
reg1_in[7:0]	30
reg2_in[7:0]	5c
alu_result[7:0]	00
sseg_out_hi[6:0]	1111110
sseg_out_lo[6:0]	1111110

fsm_input	reg1_in	reg2_in	alu_result	sseg_out_hi	sseg_out_lo
4	30	5c	83	1111111	1111001
5			10	0110000	1111110
6			6c	1011111	1001110
7			7c	1110000	
8			93	1111011	1111001