Shri Ramdeobaba College of Engineering and Management, Nagpur-13.
Department of Electronics Engineering
Analog and Digital Communication Engineering Lab [ENP357]

Even Semester – 2023-24

<div style="border:1px solid black">

Lab 06

Amplitude Shift Keying

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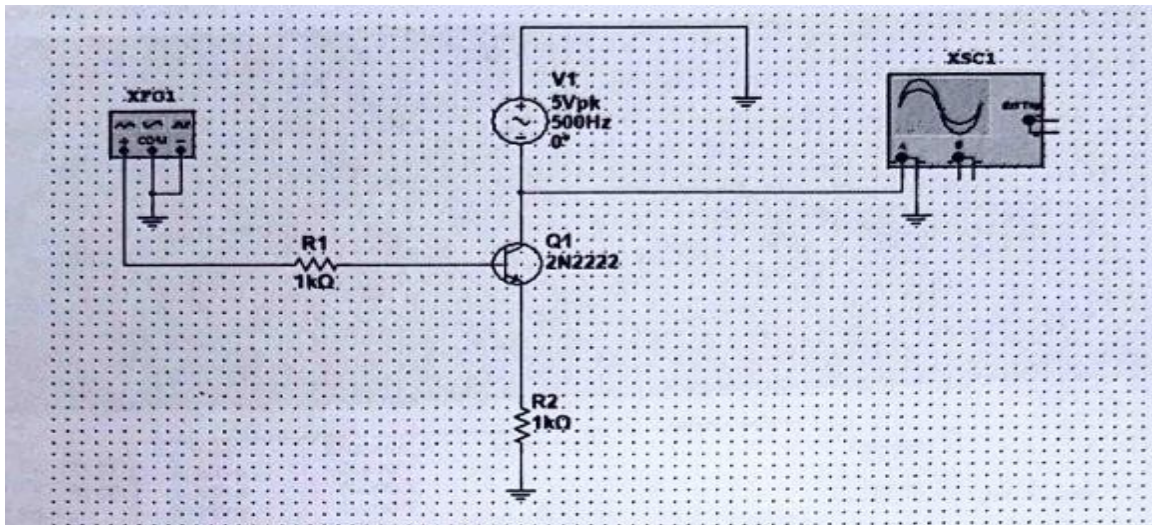|	
Name:	Pawan Dilip Sorte
Batch / Roll No. :	A1/12
Semester/Section:	6th/A
Date of Performance:	20/3/2023
Date of Submission:	17/4/2024
Name & Signature of Faculty :	Mrs. Rohini Ochawar

Lab-06

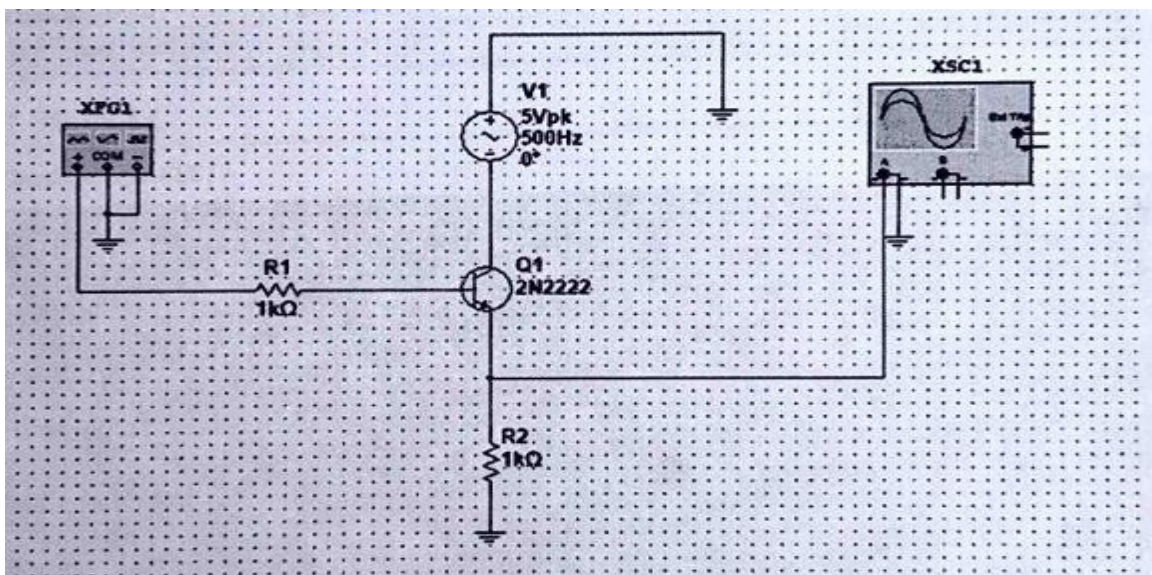**Aim:** Design and simulation of transistor based ASK modulator.

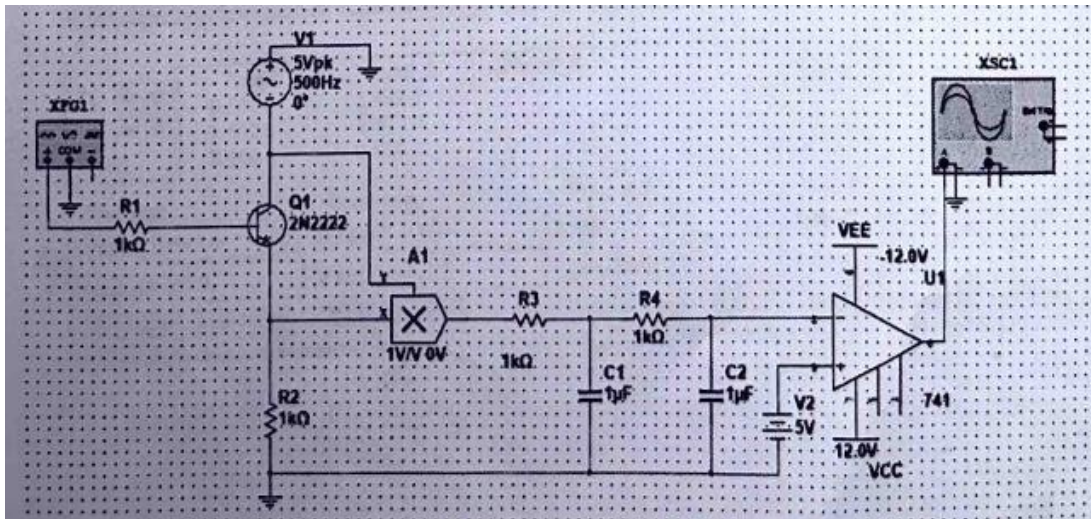✓ **Software Used:** MULTISIM
✓ **Circuit Diagram:**

CARRIER SIGNAL:

MODULATOR ASK:

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DEMODULATOR ASK:

✓ Observations

Modulating signal voltage: 5V , frequency : 100 HZ.

Carrier signal voltage : 5V , frequency: 500 HZ.
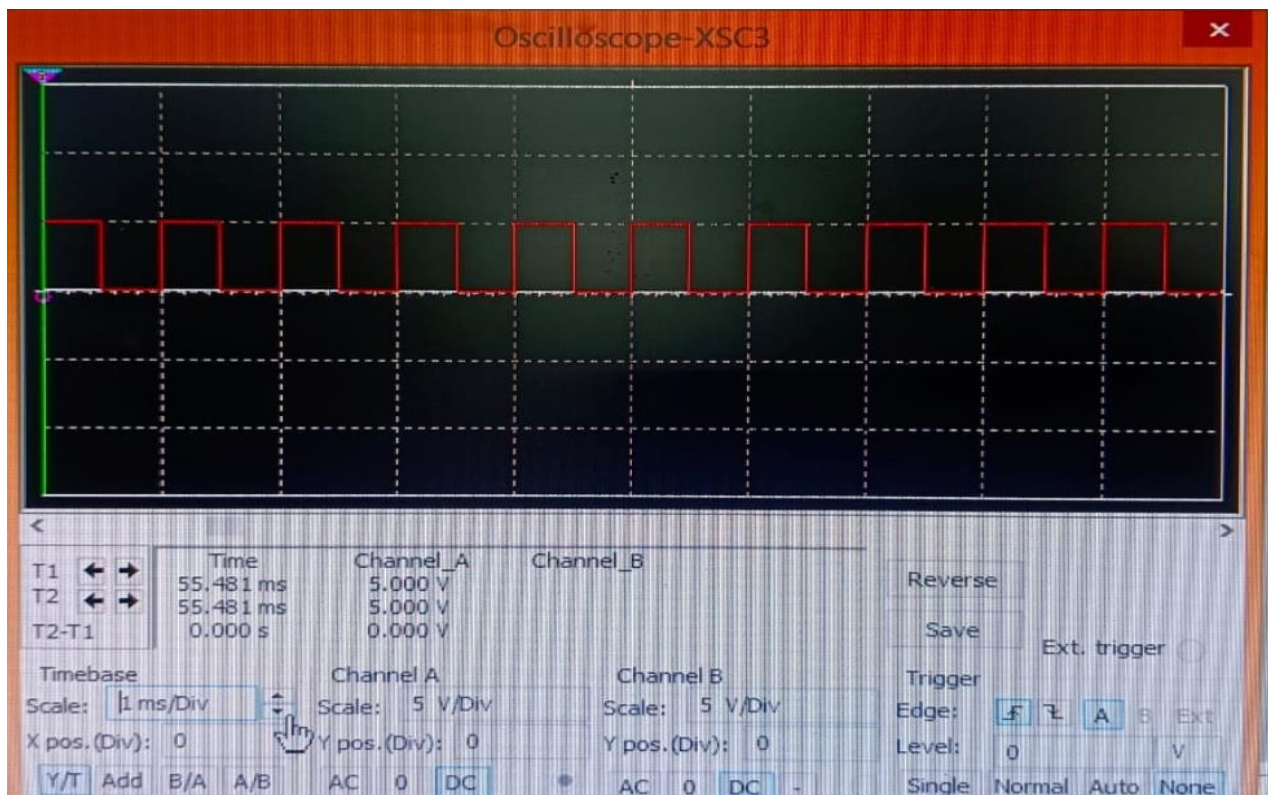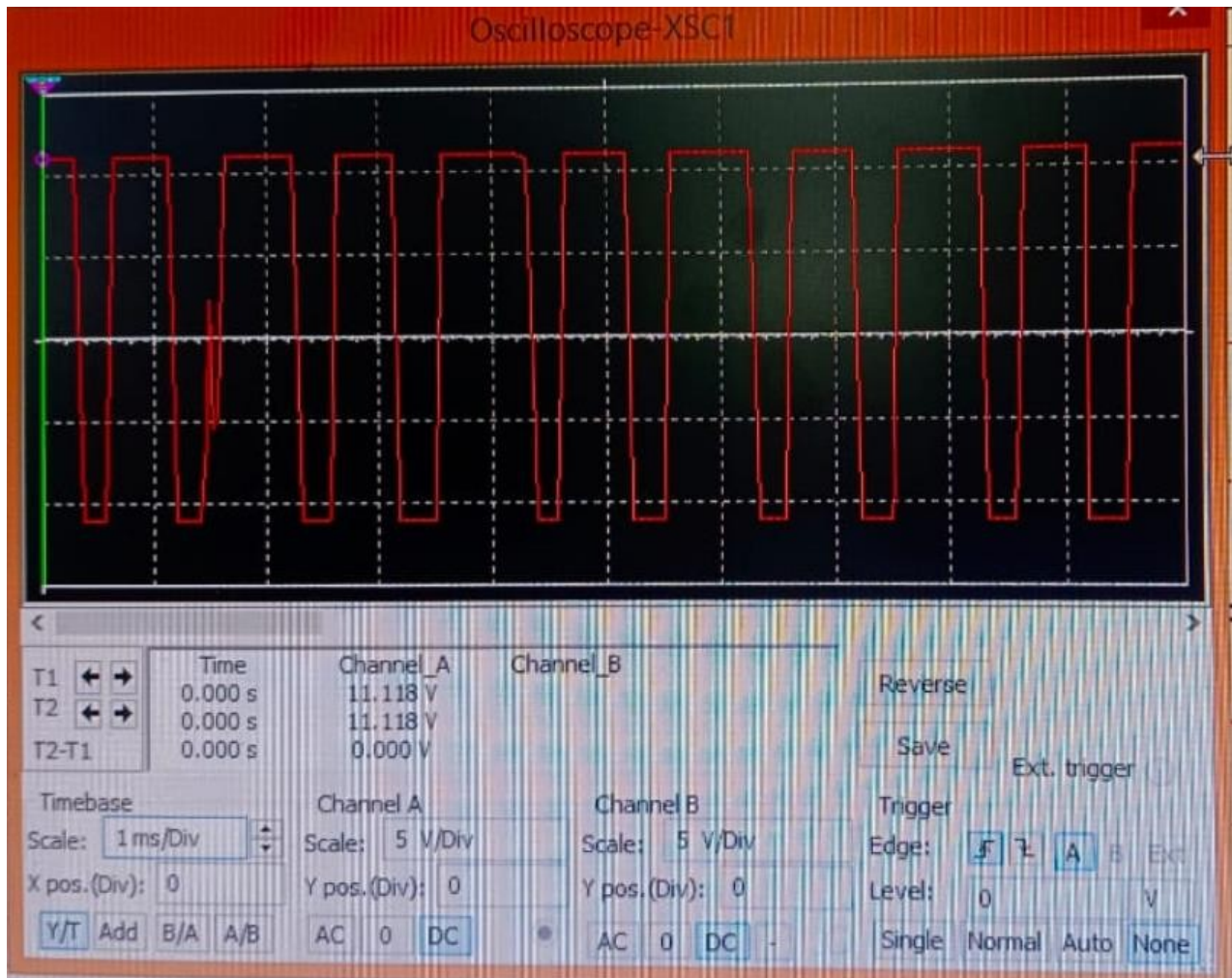
Observed waveform ASK:

ASK OUTPUT:

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DATA:

CARRIER:
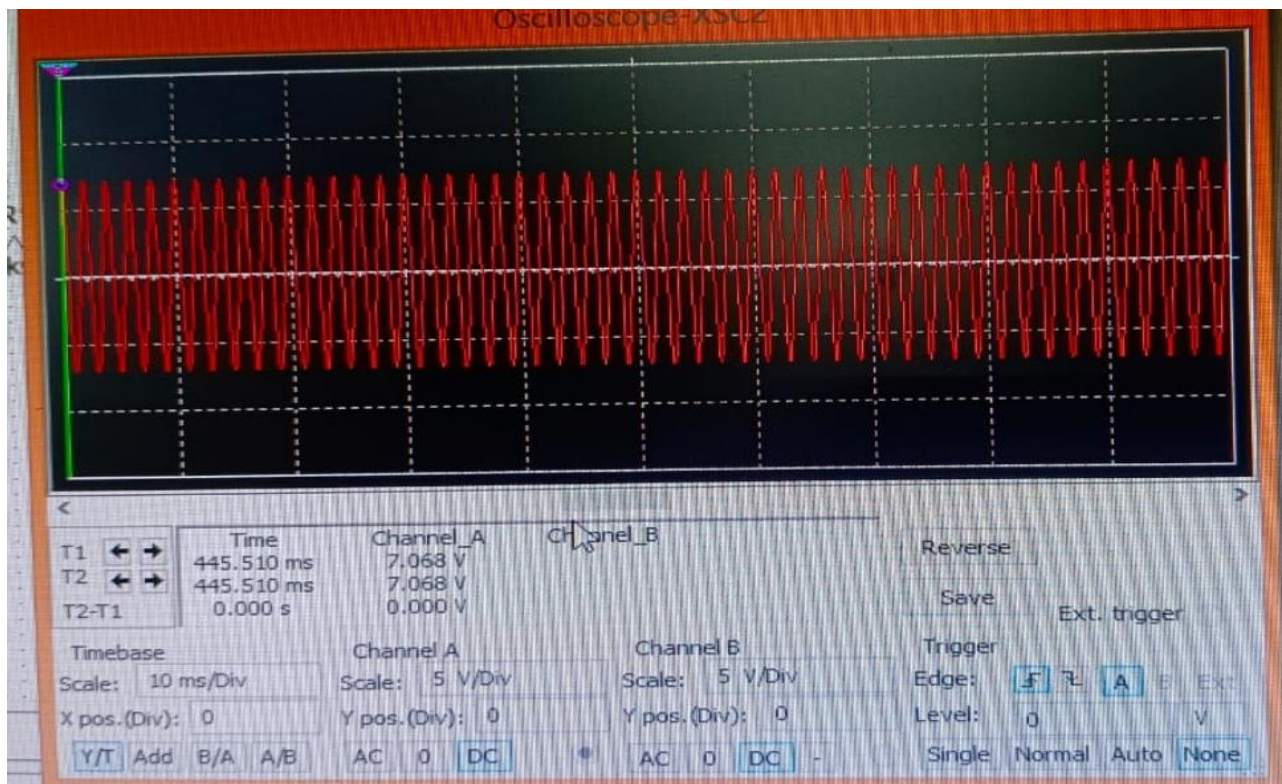
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DEMODULATOR:

Conclusion:

The transistor-based ASK modulator can be an effective way to transmit data over a communication channel using amplitude modulation.