

Shri Ramdeobaba College of Engineering and Management, Nagpur-13.
Department of Electronics Engineering
Analog and Digital Communication Engineering Lab [ENP357]
Even Semester – 2023-24

Lab 05

Mixer

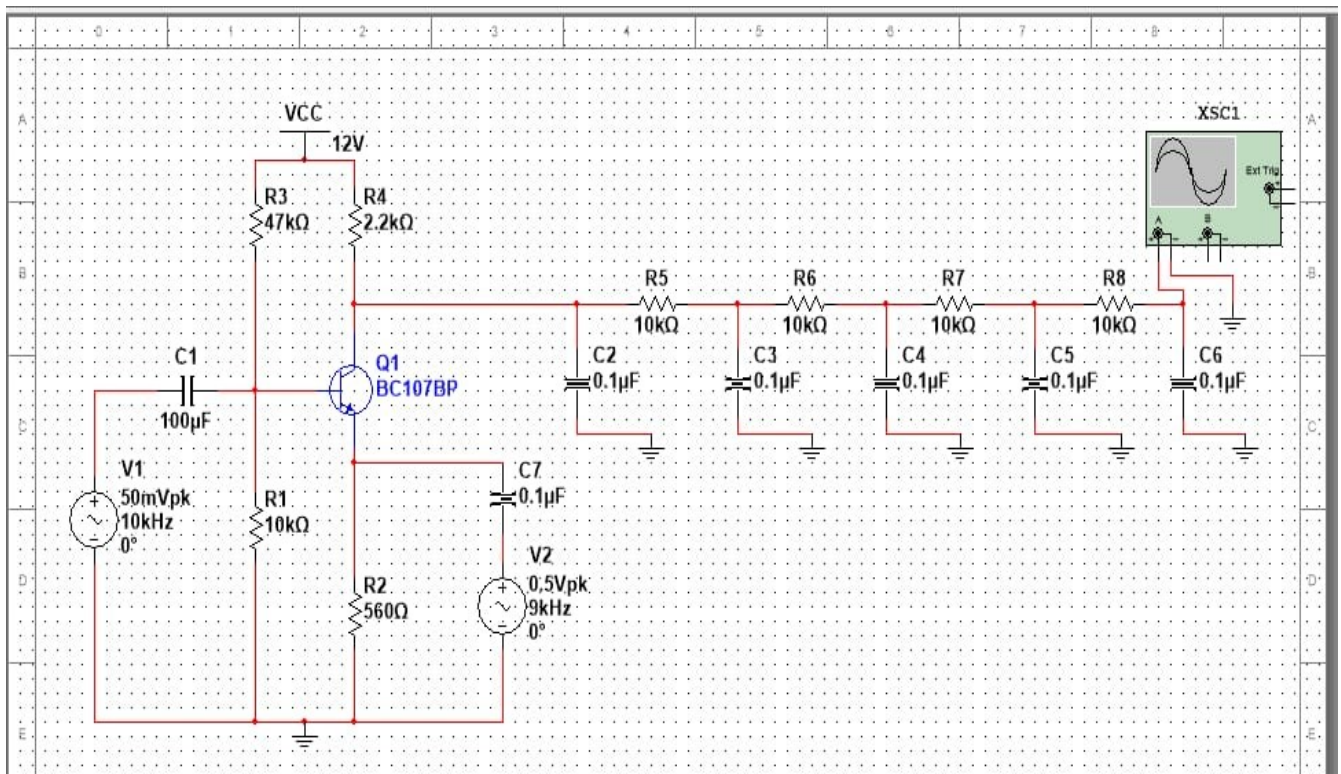
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Date of Performance:	13/3/2024
Date of Submission:	17/4/2024
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Lab-05

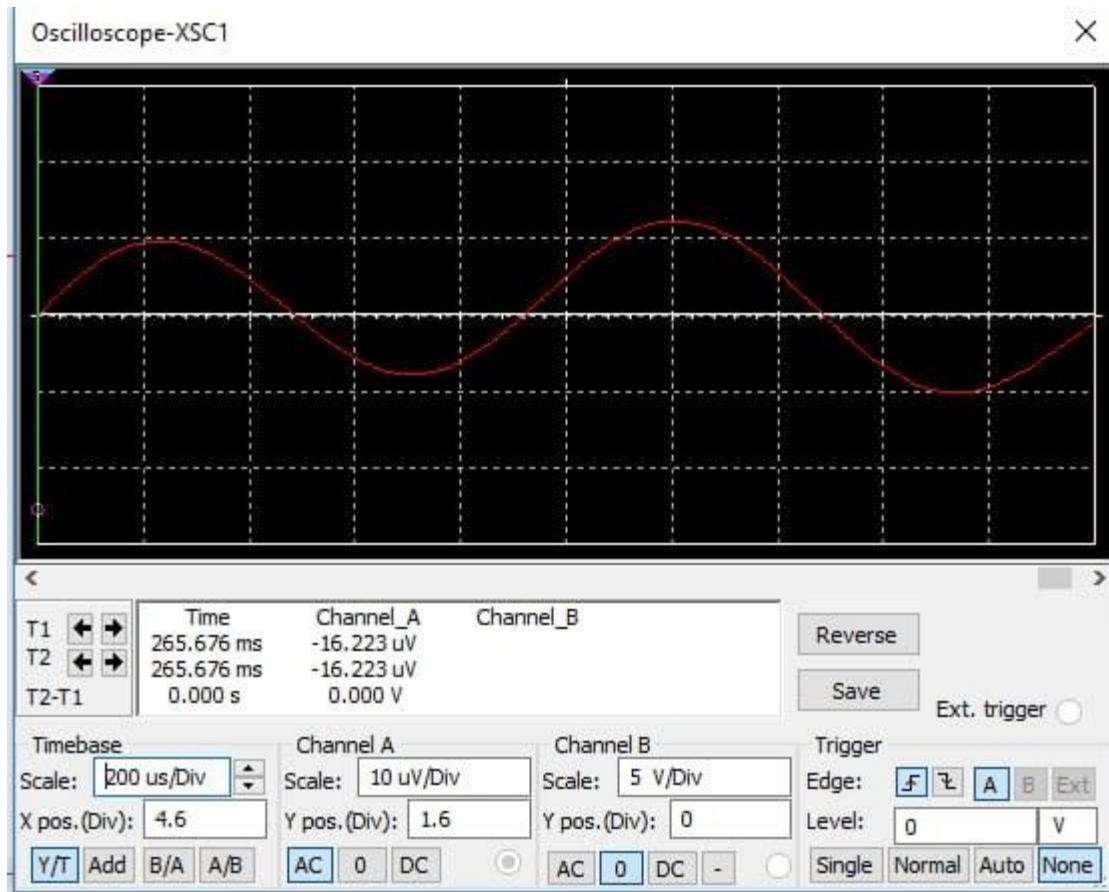
Aim: Design and simulation of Mixer circuit.

✓ **Software Used:** Multisim

✓ **Circuit Diagram:**



✓ Observations



Input signal 1: amplitude = 50mV, Frequency = 20kHz

Input signal 2: amplitude = 0.5V, Frequency = 19kHz

Frequency of output signal = $f_c - f_o$

Conclusion: The mixer circuit was designed and simulated using Multisim, showing effective signal mixing with minimal distortion.