Instr	Op$_{31:26}$	F$_{5:0}$	AluCtrl	RegWrite	RegDst	AluSrcA	AluSrcB	Branch	MemWrite	MemtoReg	Jump	AluOp
R-type	000000	Voir tab	Voir Tab	1	01	0	00	0	0	00	00	111
lw	100011	100000	100000	1	00	0	01	0	0	01	00	000
sw	101011	100000	100000	0	XX	0	01	0	1	XX	00	000
beq	000100	100010	100010	0	XX	0	00	1	0	XX	00	001
j	000010	XXXXXX	XXXXXX	0	XX	X	XX	X	0	XX	01	000
addi	001000	100000	100000	1	00	0	01	0	0	00	00	000
jr	000000	001000	XXXXXX	0	XX	X	XX	X	0	XX	10	111
andi	001100	XXXXXX	100100	1	00	0	10	0	0	00	00	010
ori	001101	XXXXXX	100101	1	00	0	10	0	0	00	00	011
jal	000011	XXXXXX	XXXXXX	1	10	X	XX	X	0	11	01	XXX
lui	001111	XXXXXX	XXXXXX	1	00	X	XX	0	0	10	00	XXX
sll	000000	000000	000000	1	01	X	00	0	0	00	00	111
sltu	000000	101011	101011	1	01	0	00	0	0	00	00	111
slt	000000	101010	101010	1	01	0	00	0	0	00	00	111
indexIntAdr	010001	XXXXXX	100000	1	01	1	00	0	0	00	00	000