Instr	Op$_{5:0}$	RegWrite	RegDst	AluSrc	Branch	MemWrite	MemtoReg	F$_{5:0}$	Jump			
R-type	000000	1	1	0	0	0	00	Voir tab	0			
lw	100011	1	0	1	0	0	00	100000	0			
sw	101011	0	X	1	0	1	XX	100000	0			
beq	000100	0	X	0	1	0	XX	100010	0			
j	000010	0	X	X	X	0	XX	XX	1			
addi	001000	1	0	1	0	0	00	100000	0			
jr	001000	0	x	xx	x	0	xx	xx	10			
andi	001100	1	0	10	0	0	00	100100	0			
jal *	000011	1	add(31) ?	XX		0	add(PC+4) ?	xx	1			
lui	001111	1	0	xx	0	0	01	xxxxx	0			
ori	001101	1	0	10	0	0	00	100101	0			
sll *	000000	1	1	0	0	0	0		0			
sltu *	101001	1	1	unsigned?	0	0	0	101010 ?	0			
slt *	101010	1	1	0	0	0	0	101010 ?	0			
indexIntAdr									0			