Alternate Address:
1000 Escalon Ave.
Apt. # D2031
Sunnyvale, CA 94085

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Current Address:
Nvidia Corporation
2701 San Tomas Expy.
Santa Clara, CA 95050

EDUCATION

Princeton University, Princeton, NJ — June 2014
Bachelor of Science in Engineering: Electrical Engineering, Department Salutatorian — GPA: 4.0/4.0 (dept.)
Minor in Computer Science
Coursework includes: High Tech Entrepreneurship*, Human-Computer Interface Technology, Advanced Programming Techniques, Operating Systems*, Algorithms and Data Structures, Advanced Systems and Signals, Very Large-Scale Integrated Circuit Design (VLSI)*, System Design and Analysis, Advanced Computer Architecture*, Parallel Computer Architecture*, Computer Graphics, Photonics and Lightwave Communication*
** Indicates Graduate Level Course*

Udacity and Coursera — June 2014 - Present
Coursework includes: Nanodegree in Artificial Intelligence, Stanford University: Machine Learning

EXPERIENCE

NVIDIA Corporation, Santa Clara, CA — Sept. 2014 - Present
Senior Software Systems Engineer - Linux Kernel Team — June - Aug. 2013

- Managed core system software bring-up for next-generation Tegra SOCs
- Developed low-power-state driver, security, and service-level processes for the Nintendo Switch
- Enhanced display, memory, GPU, and CPU system software for next-generation devices
- Coordinated processes between teams and projects to maintain code stability

Princeton University Labs, Princeton, NJ — Sept. 2010 - June 2014
Student, Research Assistant and Teaching Assistant

- Designed and tested the memory system for PITON: a scalable, open-source, massively-parallel system
- Designed and built "Histograph": a website to help users interact with their internet history (project lead)
- Designed and ran remote user testing for VIBE: a wearable that enhances "musical sensation" (project lead)
- Designed and developed a 3-D game engine, level editor, and fully functional game (project lead)
- Designed and programmed system-level integration for an embedded large-area sensing network
- Designed, built and tested a self-driving RC car; assisted in teaching robotic design course for two years

United Technologies - Goodrich ISR Systems, Princeton, NJ — June - Aug. 2012
Research and Development Intern

- Designed Focal Plane Array testing fixtures for high-temperature testing using OrCAD
- Improved security protocols for industrial and military cameras
- Researched image streaming protocols for next-generation short-wave infrared cameras

AWARDS & PUBLICATIONS

- Princeton University's Sigma Xi Book Award for excellence in research
- Princeton University's Hisashi Kobayashi Prize for achievement in the field of computer science
- "Piton: A Manycore Processor for Multi-Tenant Clouds," *IEEE Micro*, March/April 2017
- "Piton: A 25-Core Academic Manycore Processor," *Hot Chips*, August 2016

SKILLS

Proficient in C, C++, Python, Java, Verilog, Matlab, Git, Perforce

EXTRACURRICULARS

Institute for Electrical and Electronics Engineers (IEEE), Student Chapter — Sept. 2011 - June 2014
Founder, Chair (2013-2014); Sophomore and Junior Liaison (2011-2013)

- Gained official recognition from IEEE for Princeton University Student Branch
- Organized educational talks and events for undergraduate and graduate electrical engineers

Improvisational Comedy: Princeton's "Fuzzy Dice" — Sept. 2011 - June 2014

- Performed on campus and toured in Los Angeles and New York City