Current Address:
Nvidia Corporation
2701 San Tomas Expy.
Santa Clara, CA 95050

Samuel Leland Payne
(248) 770-0912
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Alternate Address:
1000 Escalon Ave.
Apt. # D2031
Sunnyvale, CA 94085

EDUCATION

Princeton University, Princeton NJ — June 2014
Bachelor of Science in Engineering: Electrical Engineering, High Honors — GPA: 4.0/4.0 (dept)
Minor in Computer Science
Coursework includes: High Tech Entrepreneurship*, Human-Computer Interface Technology, Algorithms and Data Structures, Advanced Programming Techniques, Operating Systems*, Advanced Systems and Signals, Very-Large-Scale Integrated Circuit Design (VLSI)*, System Design and Analysis, Photonics and Light Wave Communication*, Advanced Computer Architecture*, Parallel Computer Architecture*, Computer Graphics
** Indicates Graduate Level Course*

Detroit Country Day School, Bloomfield Hills, MI — June 2010
Additional Degree in the Conservatory of the Fine and Performing Arts — Summa Cum Laude

EXPERIENCE

NVIDIA Corporation, Santa Clara, CA — Sept. 2013 - Present
Software Systems Engineer – Android Kernel Team — Jun. – Aug. 2013

- Assisted in software bring-up for next generation Tegra processors
- Enhanced display, memory, GPU and CPU system software for next generation Tegra devices
- Implemented a test plan for display controller, framebuffer, hdmi and other display drivers
- Managed processes between teams to maintain code stability

Princeton University Labs, Princeton, NJ — Sept. 2010 – Jun. 2014
Student, Research Assistant and Teaching Assistant

- Designed and tested the memory system for a massively-parallel, many-node system
- Designed and built a website to help users interact with their internet history (project lead)
- Designed wearable electronics to provide "musical sensation" to users (project lead)
- Designed and developed a 3-D game engine with realistic physics and fully functional game (project lead)
- Designed and programmed system level integration for an embedded large-area sensing network
- Designed and built a miniature, self-driving, robotic car
- Assisted in teaching robotic design course for two years

Goodrich ISR Systems (acquired by United Technologies), Princeton, NJ — Jun. – Aug. 2012
Research and Development Intern

- Designed Focal Plane Array testing fixtures for high temperature testing using OrCAD
- Improved security protocols for industrial and military cameras
- Researched image streaming protocols for next generation short-wave infrared cameras

Pratt & Miller Engineering, New Hudson, MI — May – Aug. 2010
Quality Assurance and Material Testing Department Intern — May – Aug. 2011

- Assembled electronic harness systems for water-proof performance.
- Utilized CAM Q measurement software and FARO Arm measurement equipment

CAMPUS ACTIVITIES

Institute for Electrical and Electronics Engineers, Student Chapter — Sept. 2011 – Jun. 2014
Founder, Chair (2013-2014) Sophomore and Junior Liaison (2011 – 2013)

- Gained official recognition from IEEE for Princeton University Student Branch
- Organized educational talks and events for undergraduate and graduate electrical engineers

"Fuzzy Dice" Improvisational Comedy — Sept. 2011 – Jun. 2014

- Performed on campus and toured in Los Angeles and New York City

SKILLS

Proficient in C, Java, Matlab, Verilog, Microsoft Office and Git

AWARDS & ACHIEVEMENTS

- Princeton University's Sigma Xi Book Award for excellence in research
- Princeton University's Hisashi Kobayashi Prize for achievement in the field of computer science