Update of NE1 Lab

- 1. Why virtual machine? Linux to support Pyplane.
- 2. What is AER (address event representation)? Configure the circuits. Two documents: test circuits.pdf and chip architecture.pdf
- 3. You should finish prelab part before your lab session. Insert hand drawing figures when necessary.
- 4. Lab 2 is difficult but one of the most important labs. Please come half an hour earlier.