



Diagram illustrating the PCB layout for a 4-core system, showing connections between headers J4, J5, J6, and J7.

Header J4 (Conn_01x03_Male):

- Pin 1: -/BLK
- Pin 2: +/WHT
- Pin 3: C/RED

Header J5 (Conn_01x03_Male):

- Pin 1: -/BLK
- Pin 2: +/WHT
- Pin 3: C/RED

Header J6 (Conn_01x03_Male):

- Pin 1: -/BLK
- Pin 2: +/WHT
- Pin 3: C/RED

Header J7 (Conn_01x03_Male):

- Pin 1: -/BLK
- Pin 2: +/WHT
- Pin 3: C/RED

Connections:

- J4 Pin 3 (C/RED) is connected to GND.
- J5 Pin 3 (C/RED) is connected to J6 Pin 2 (+/WHT) and J7 Pin 3 (C/RED).
- J6 Pin 3 (C/RED) is connected to J7 Pin 2 (+/WHT).
- J7 Pin 3 (C/RED) is connected to NAU_VDDA.

Wire colours vary:

- Largest resistance between + and -
- The wire with lower resistance is center.
- center: red or sometimes white

