1. Description

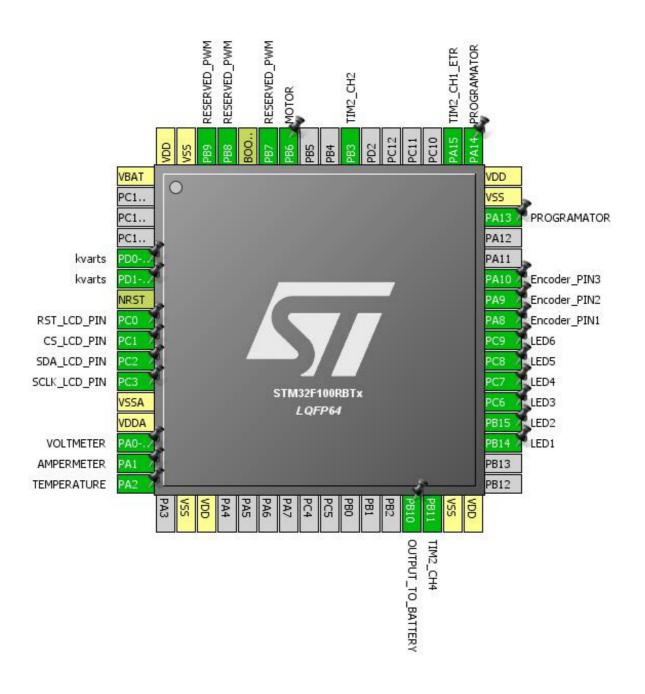
1.1. Project

Project Name	pins_configuration
Generated with:	STM32CubeMX 4.5.0
Date	12/17/2014

1.2. MCU

MCU Serie	STM32F1
MCU Line	STM32F100 Value Line
MCU name	STM32F100RBTx
MCU Package	LQFP64
MCU Pin number	64

2. Pinout Configuration



3. IPs and Middlewares Configuration

IP	Mode	Fonction	Pin
	INO	ADC1_IN0	PA0-WKUP
	IN1	ADC1_IN1	PA1
ADC1	IN2	ADC1_IN2	PA2
	Vrefint Channel	N/A	N/A
	Vbat Channel	N/A	N/A
	Debug:	SYS_JTCK-SWCLK	PA14
0)/0	Serial-Wire	SYS_JTMS-SWDIO	PA13
SYS	OSC:	SYS_OSC_IN	PD0-OSC_IN
	HSE-External-Clock-Source	SYS_OSC_OUT	PD1-OSC_OUT
		TIM1_CH1	PA8
TIM1	Mode: Xored-Inputs-Hall-Sensor-Interface	TIM1_CH2	PA9
	Aureu-inpuis-naii-Serisor-interface	TIM1_CH3	PA10
		TIM2_CH1_ETR	PA15
TIMO	Mode:	TIM2_CH2	PB3
TIM2	PWM-Generation	TIM2_CH3	PB10
		TIM2_CH4	PB11
		TIM3_CH1	PC6
TIM3	Mode:	TIM3_CH2	PC7
1 11013	PWM-Generation	TIM3_CH3	PC8
		TIM3_CH4	PC9
		TIM4_CH1	PB6
TIM4	Mode:	TIM4_CH2	PB7
	PWM-Generation	TIM4_CH3	PB8
		TIM4_CH4	PB9
TIMAS	Mode:	TIM15_CH1	PB14
TIM15	PWM-Generation	TIM15_CH2	PB15

4. Pins Configuration

Pin	Pos	Function(s)	Label
PD0-OSC_IN	5	SYS_OSC_IN	kvarts
PD1-OSC_OUT	6	SYS_OSC_OUT	kvarts
PC0 *	8	GPIO_Output	RST_LCD_PIN
PC1 *	9	GPIO_Output	CS_LCD_PIN
PC2 *	10	GPIO_Output	SDA_LCD_PIN
PC3 *	11	GPIO_Output	SCLK_LCD_PIN
PA0-WKUP	14	ADC1_IN0	VOLTMETER
PA1	15	ADC1_IN1	AMPERMETER
PA2	16	ADC1_IN2	TEMPERATURE
PB10	29	TIM2_CH3	OUTPUT_TO_BATTERY
PB11	30	TIM2_CH4	
PB14	35	TIM15_CH1	LED1
PB15	36	TIM15_CH2	LED2
PC6	37	TIM3_CH1	LED3
PC7	38	TIM3_CH2	LED4
PC8	39	TIM3_CH3	LED5
PC9	40	TIM3_CH4	LED6
PA8	41	TIM1_CH1	Encoder_PIN1
PA9	42	TIM1_CH2	Encoder_PIN2
PA10	43	TIM1_CH3	Encoder_PIN3
PA13	46	SYS_JTMS-SWDIO	PROGRAMATOR
PA14	49	SYS_JTCK-SWCLK	PROGRAMATOR
PA15	50	TIM2_CH1_ETR	
PB3	55	TIM2_CH2	
PB6	58	TIM4_CH1	MOTOR
PB7	59	TIM4_CH2	RESERVED_PWM
PB8	61	TIM4_CH3	RESERVED_PWM
PB9	62	TIM4_CH4	RESERVED_PWM

^{*} The pin is affected with an I/O function

pins_configuration Project						
i. Power Pl	ugin rep	oort				