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## Closer look at a Direct Sequence Spread Spectrum Signal Posted on October 30, 2012 by DB6SW 2 Comments

This article is going to take a closer look at the properties of a pseudo random bit sequence and a simple direct

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sequence spread spectrum signal both in the time- and frequency domain using the FFT capabilities of a Teledyne LeCroy WaveAce 1002. A while back, I posted an article about a Pseudorandom\_Number\_Generator\_(PRNG) for Direct\_Sequence\_Spread

Spectrum (DSS) experiments. PRNGs are the key element in many cryptographic applications and they also play a major role in DSSS. To generate a pseudorandom binary sequence (PRBS), I am using a linear feedback shift register (LFSR). A LFSR is a

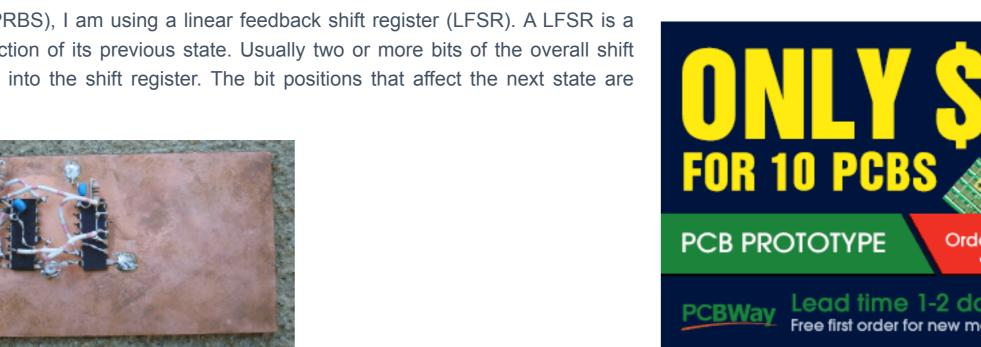
shift register of which the input bit is a linear function of its previous state. Usually two or more bits of the overall shift register value are exclusive-OR-ed and fed back into the shift register. The bit positions that affect the next state are called the taps.



ugly construction style (aka 'dead bug' or 'Mahatten style') The next picture shows a simple 7 stage PRNG which I am using for the following experiments. A 7-stage shift register

can produce a code with a maximal length of  $2^7-1 = 127$  code bits. The output sequence almost equals the statistical expectation for a truly random sequence. The numbers have a nearly perfect bell-shaped Gaussian distribution. However, LFSR is deterministic. If any given status of the shift register is known, the next state can be predicted.

A pseudorandom number generator (PRNG)





X



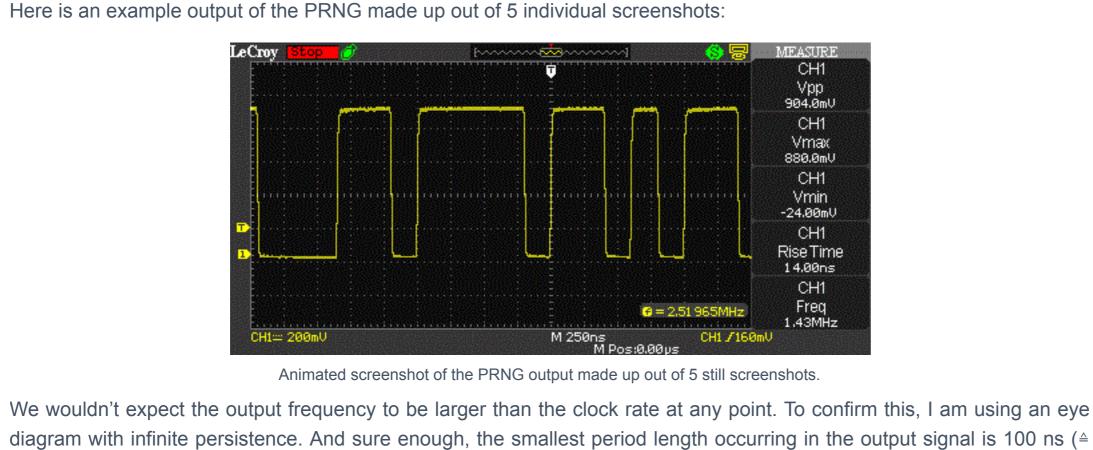
DE (Deutsch):

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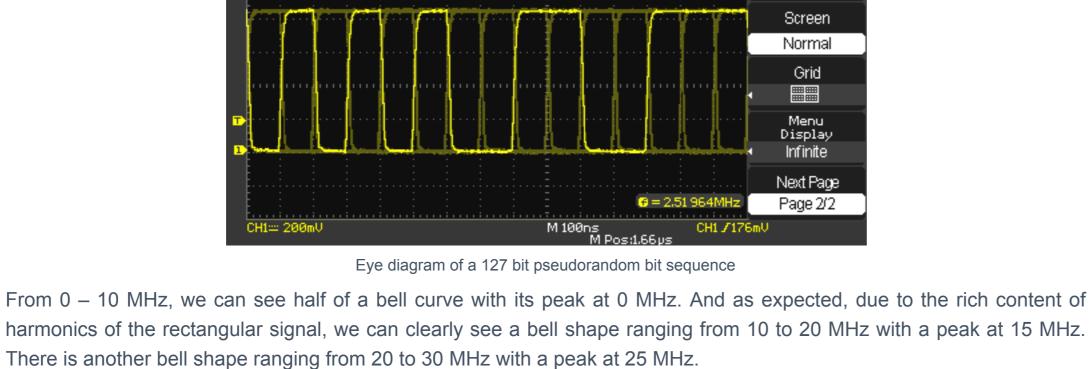
Turn-Key PCB Assembly

Low volume PCB Assembl

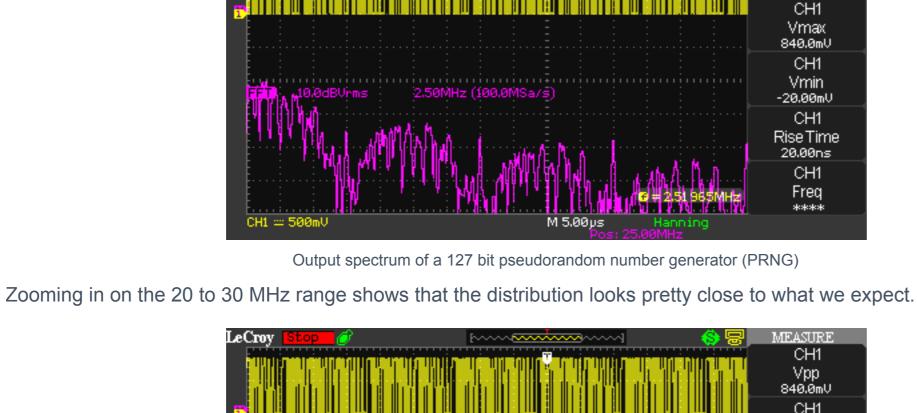
Low cost SMT Assembly



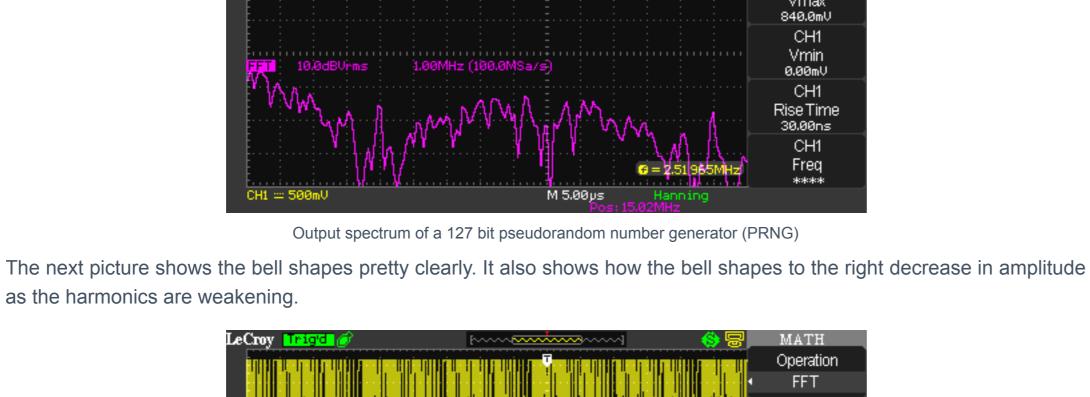
10 MHz). DISPLAY [-------] Format YΤ



CH1 Vmin -20.00mV



CH1 Vmax 840.0m∪



2X Next Page

Output spectrum of a 127 bit pseudorandom number generator (PRNG)

To get an idea what an actual DSSS signal looks like, we are going mix a PRBS with a constant carrier. Normally, some

Source CH1

Window Hanning

FFT Zoom

CH1 Vmax

CH1 Vpp 824.0m∪

CH1 Vmax 392.0mU CH1 Vmin -432.0mV CH1 RiseTime

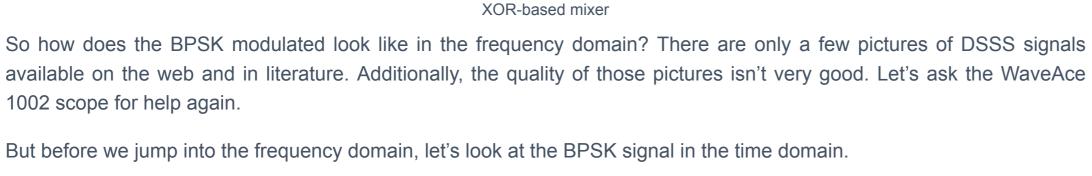
> CH1 Vmin

> > N X

transmit data would be XORed with the PRBS before transmission. But for the sake of this experiment, we don't need any data other than the code sequence (PRBS) itself. Just like in a real DSSS application, we are going to use an XOR gate as mixer. Essentially, this results in the carrier being modulated with the code sequence (PRBS) using Binary Phase-Shift Keying (BPSK). This is because the XOR will

invert (= 180 degree phase shift) the phase relation of the carrier if the modulation input is 'high' (logical 1).

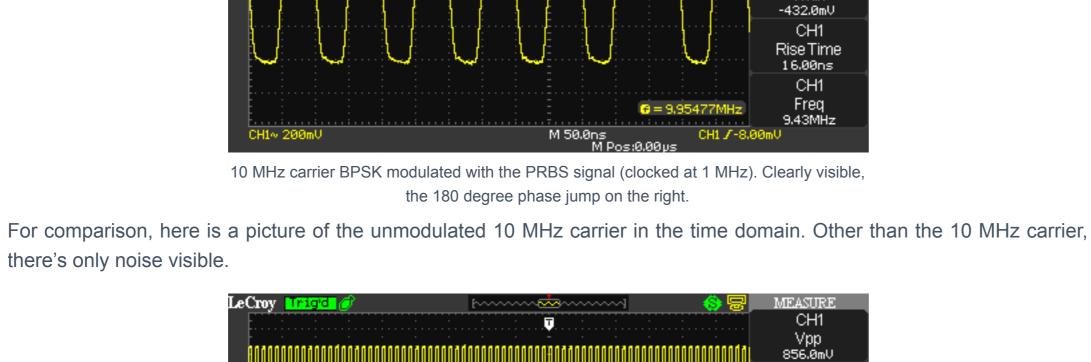
A pseudorandom number generator (PRNG) with simple



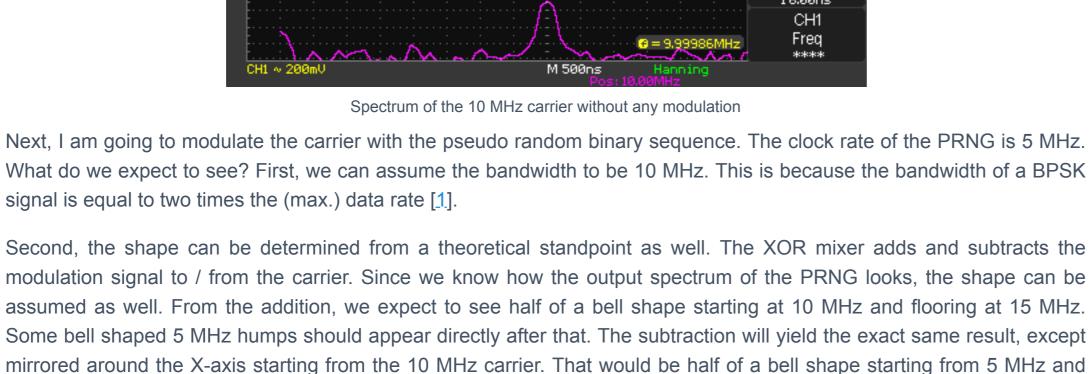
But before we jump into the frequency domain, let's look at the BPSK signal in the time domain. MEASURE LeCroy CH1 Vpp 848.0m∪

CH1 Vmax 416.0mU CH1

1002 scope for help again.



416.0mU CH1 Vmin -440.0mU CH1 RiseTime

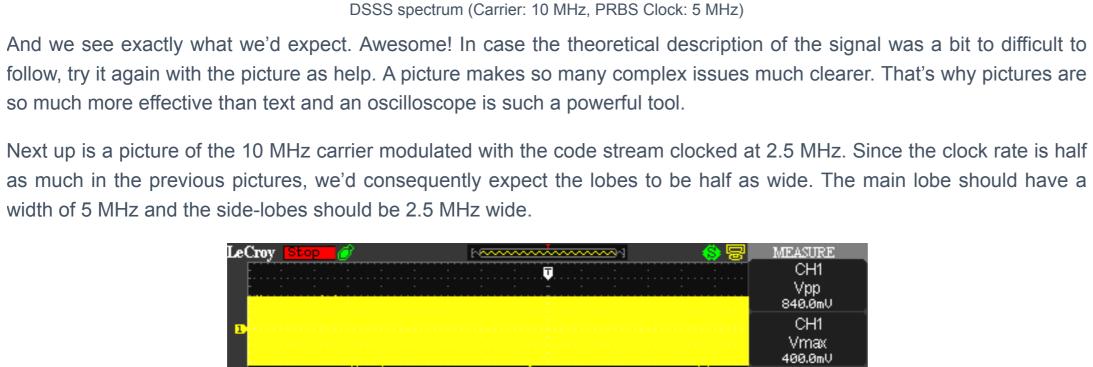


peaking at 10 MHz. Side lobes will appear left from the 5 MHz point with a bandwidth of 5 MHz each. Since negative frequencies don't exist, there is actually just room for one side lobe in this scenario. **Oplev Fredericia Teaters** 

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Too much theory? I agree, here's a pretty picture of the 10 MHz carrier modulated with the code stream clocked at 5 MHz.

20.00ns CH1 Freq

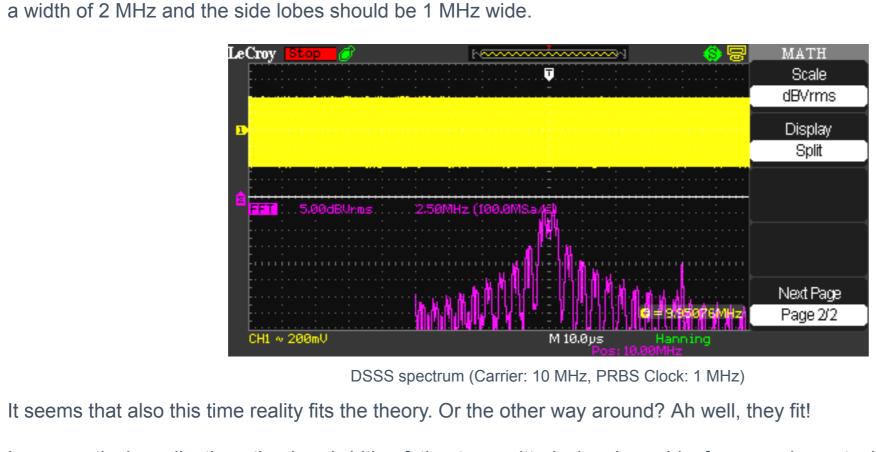


M 10.0 ps

DSSS spectrum (Carrier: 10 MHz, PRBS Clock: 2.5 MHz)

The last picture shows the 10 MHz carrier modulated with the code stream clocked at 1 MHz. The main lobe should have

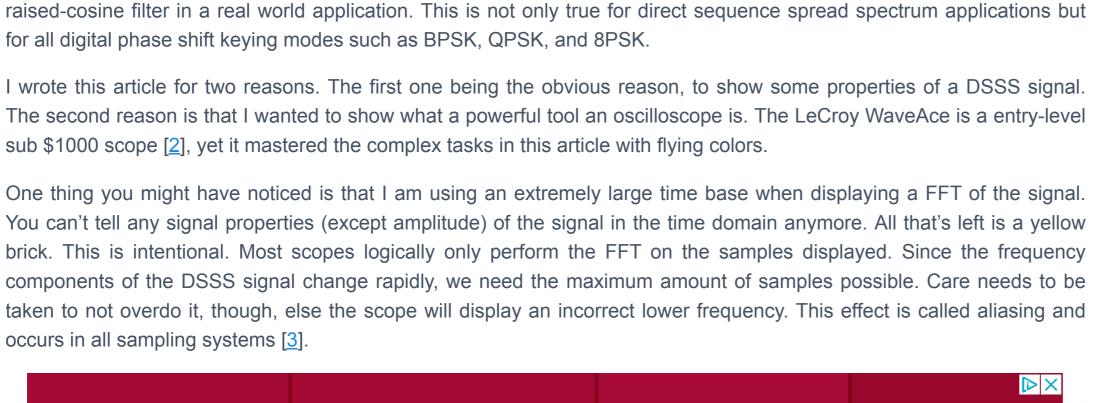
-440.0mU CH1 RiseTime CH1 Freq 11.11MHz

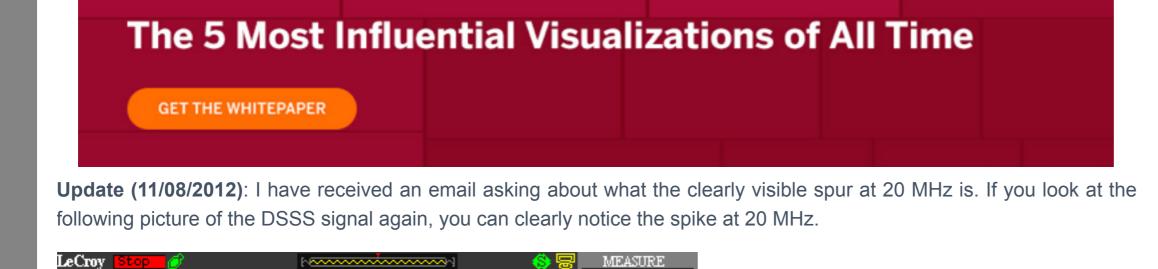


In a practical application, the bandwidth of the transmitted signal would of course have to be limited for spectrum efficiency. Interestingly, not all of the main lobe is needed to reliably transport data. To be precise, the overall bandwidth of the transmitted signal can be cut down to be equal to the maximum data rate. This is called the Nyquist bandwidth.

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In the case of the 5 Mbps PRBS stream, we could cut the main lobe off to be only 5 MHz wide. This is being done with a



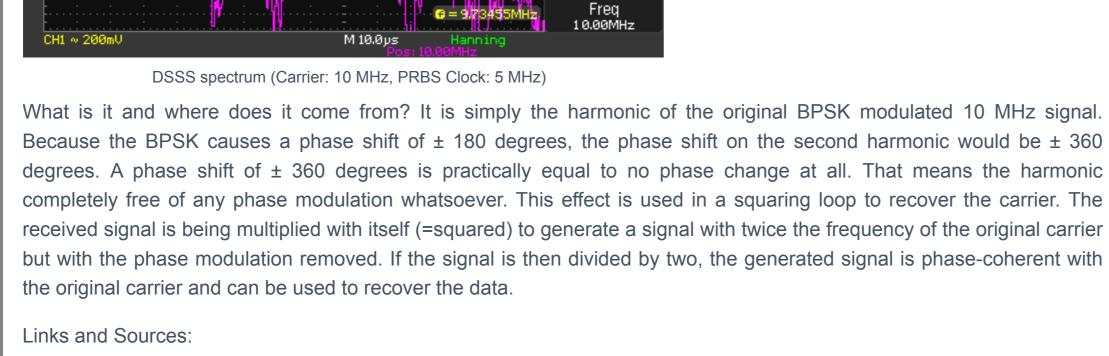


CH1

CH1

CH1

∨max 392.0mV CH1 Vmin RiseTime



[1] BPSK, QPSK, 8PSK and QAM Calculator, KF5OBS: http://jaunty-electronics.com [2] Teledyne LeCroy WaveAce 1002 Review, KF5OBS: <a href="http://jaunty-electronics.com">http://jaunty-electronics.com</a>

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[3] Aliasing, Wikipedia: <a href="http://en.wikipedia.org/">http://en.wikipedia.org/</a> **Share this:** 

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