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An Inexpensive Vector Network Analyzer Project for the Walla Walla University Electronics II Class

📄 136 commits

🔗 9 branches

📦 0 releases

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Branch: master ▾

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	frohro Added vna2019.asc to the LTspice simulations.	Latest commit 5f93e1f on 10 Jun
📁 2017 VNA Kicad	Added vna2019.asc to the LTspice simulations.	5 months ago
📁 2017 VNA	Adding to README for CCSv8.2, and making compatible with Kicad v5.	11 months ago
📁 2018 Simple VNA	Cleaned up comments in adc14vna.cpp	6 months ago
📁 LTSpice-Simulations	Added vna2019.asc to the LTspice simulations.	5 months ago
📁 Octave	Added some test octave scripts & improved serial port choosing in	6 months ago
📁 Python	Added some arduino python test scripts.	5 months ago
📁 Reflection_Bridge Kicad	Hwi interrupt header testing....	5 months ago
📁 Testing Si5351a	Added important info to the README aout necessities for building.	10 months ago
📁 quantizationNoiseSimulator	Push up quantization noise simulator python script	10 months ago
📄 .gitignore	Update .gitignore	2 years ago
📄 LICENSE	Initial commit	2 years ago
📄 README	Some changes to the Octave directory.	6 months ago
📄 README.md	Initial commit	2 years ago

📖 README.md

WWU-VNA

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