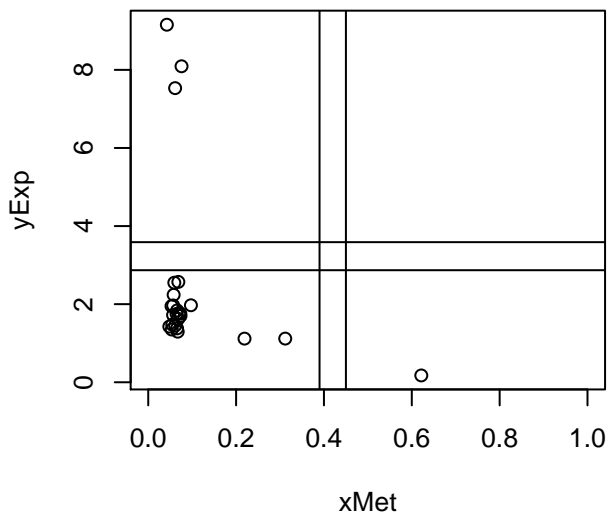
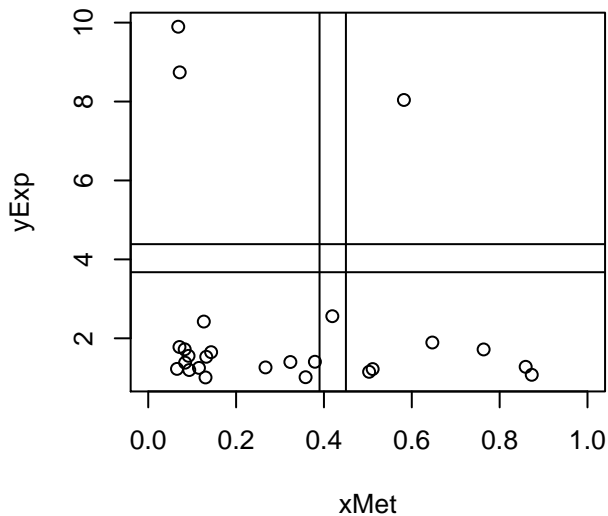


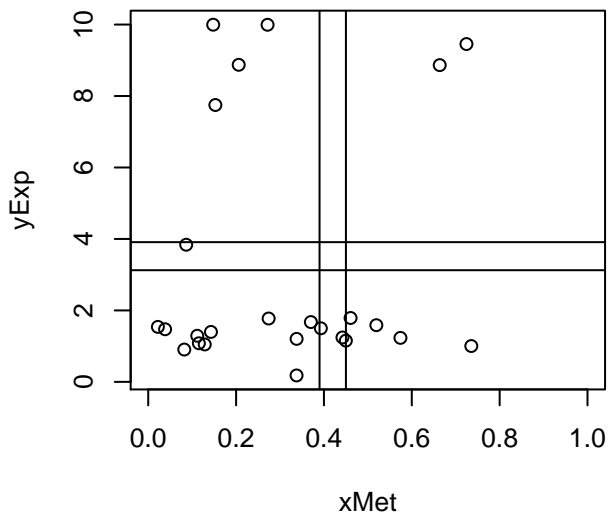
**B4GALT6**  
**L-shaped = FALSE**



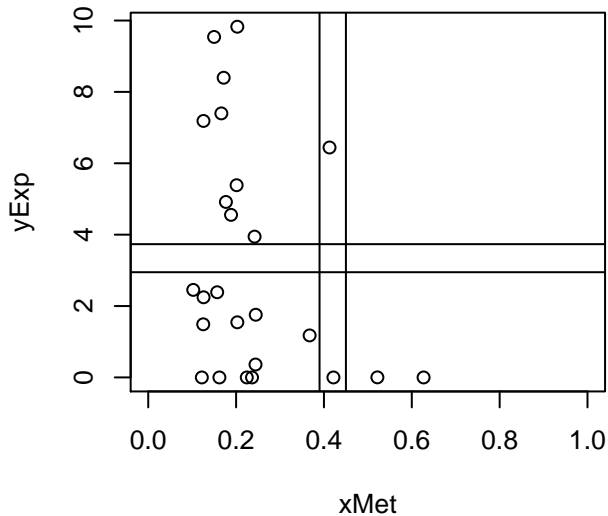
**C1QL1**  
**L-shaped = FALSE**



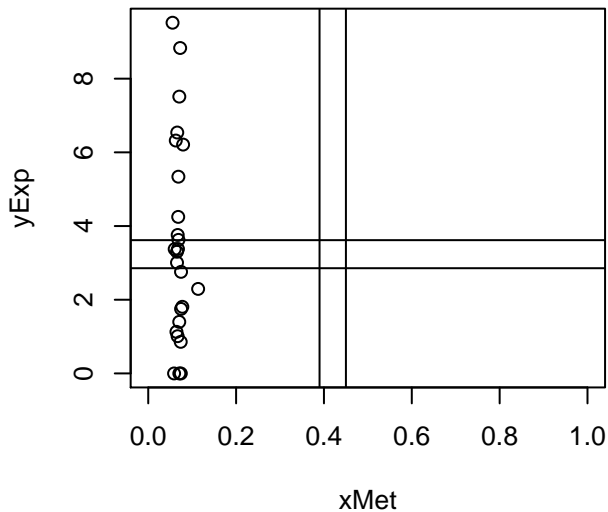
**CNR1**  
**L-shaped = FALSE**



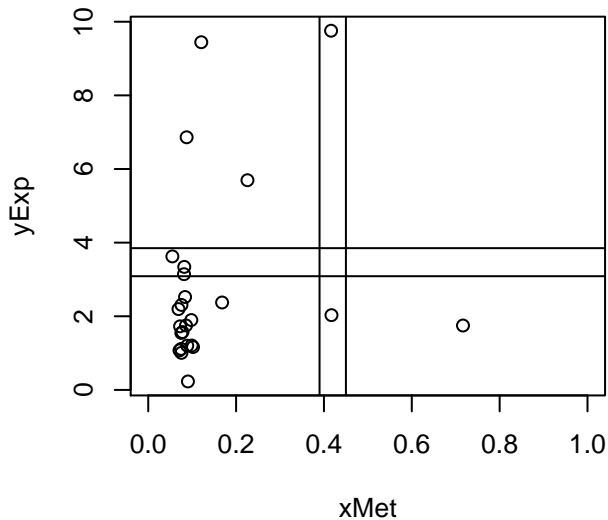
**DMBT1**  
**L-shaped = FALSE**



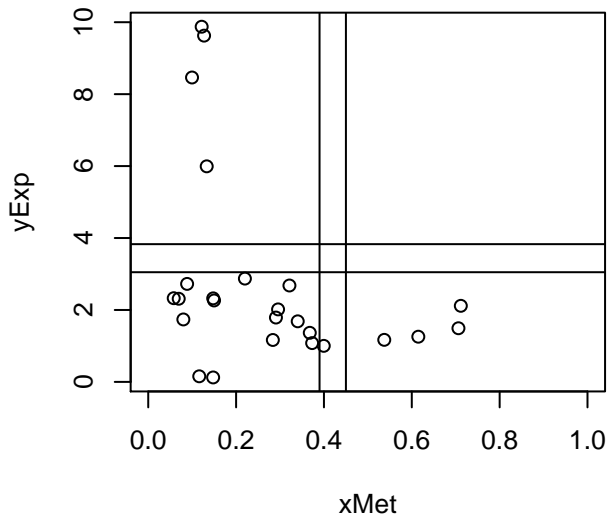
**ELOVL6**  
**L-shaped = FALSE**



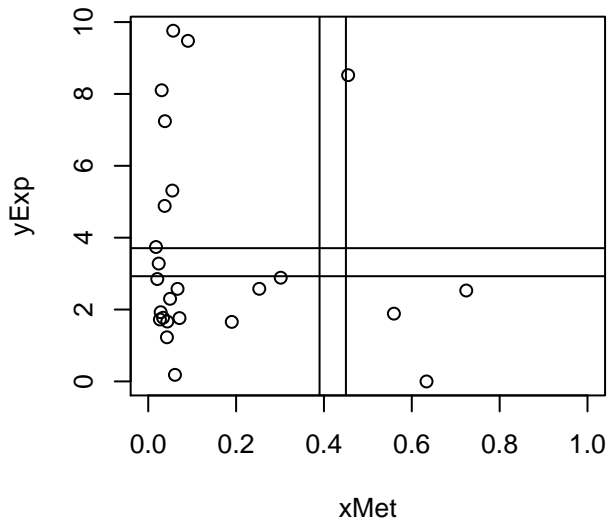
**EOMES**  
**L-shaped = FALSE**



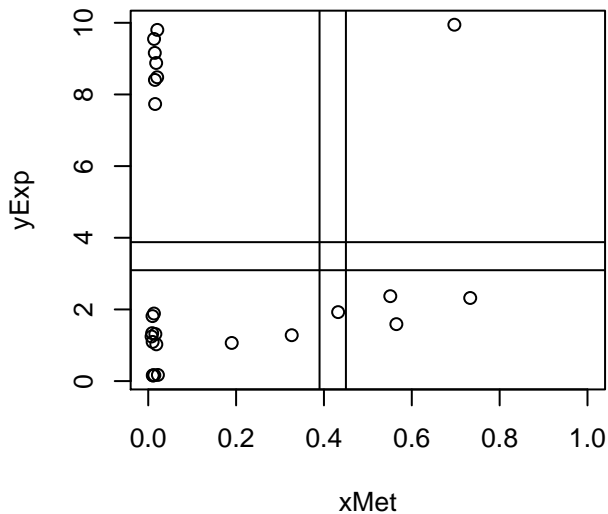
**ERG**  
**L-shaped = FALSE**



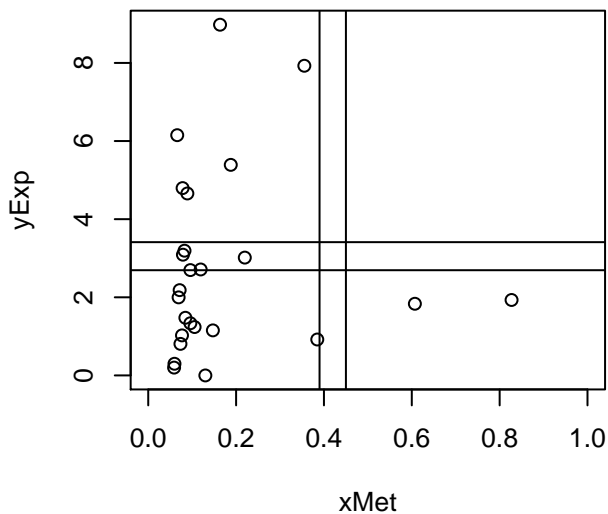
**GSTM3**  
**L-shaped = FALSE**



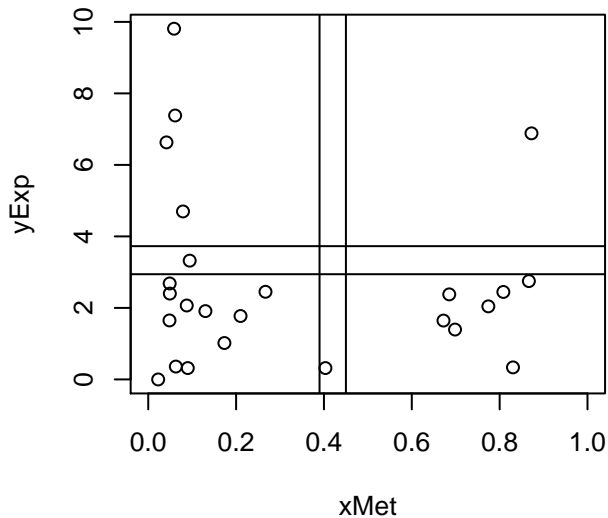
**IL17RC**  
**L-shaped = FALSE**



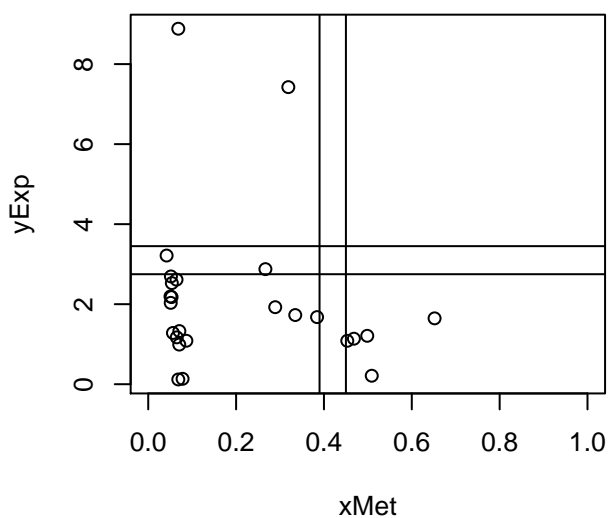
**LPHN2**  
**L-shaped = FALSE**



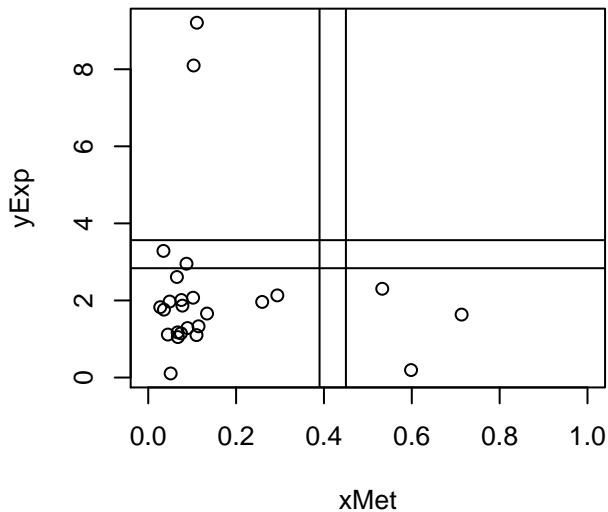
**MME**  
**L-shaped = FALSE**



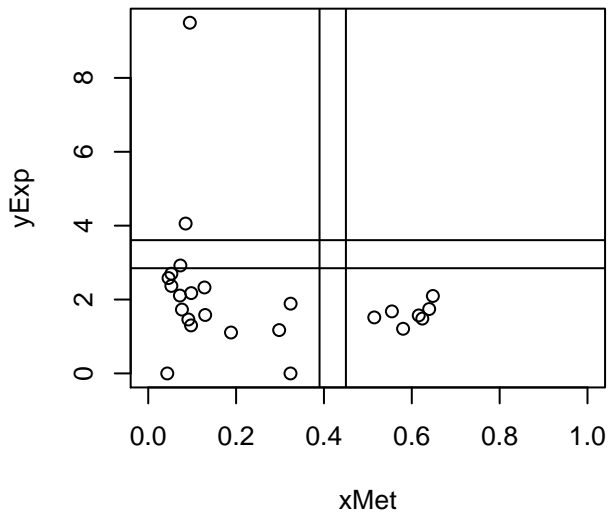
**MOSPD2**  
**L-shaped = FALSE**



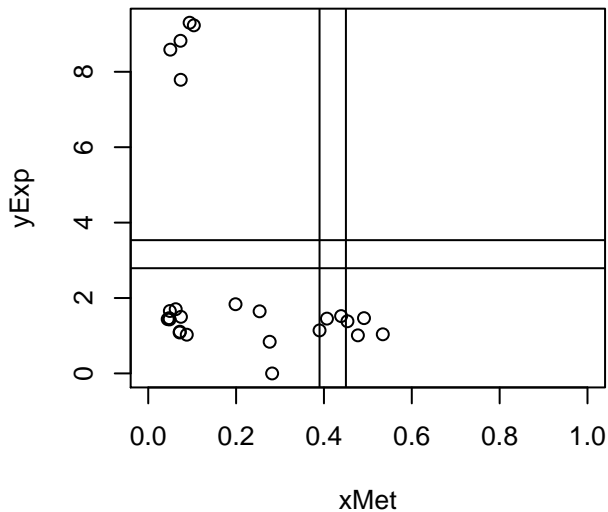
**MSX2**  
**L-shaped = FALSE**



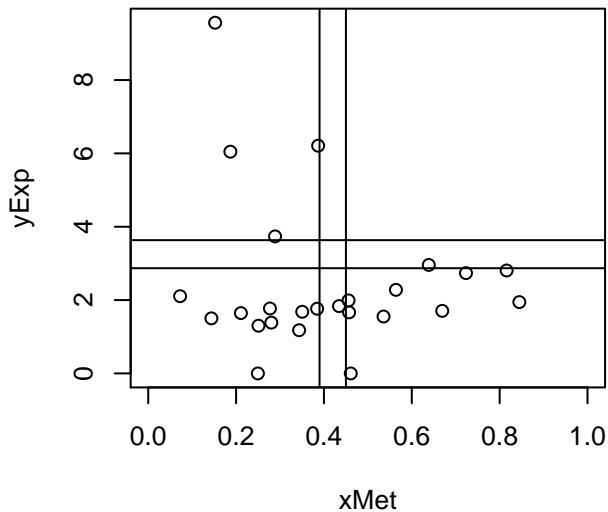
**MTCP1**  
**L-shaped = FALSE**



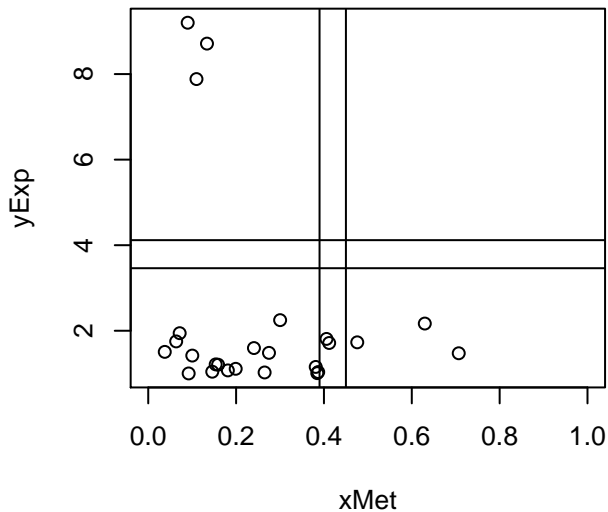
**MTMR8**  
**L-shaped = FALSE**



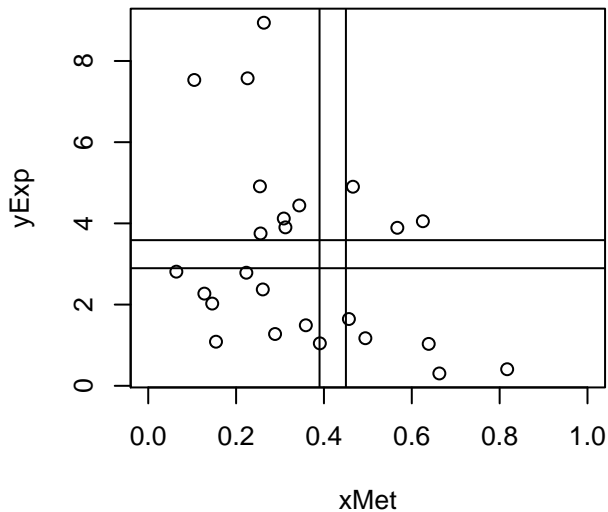
**NCOR2**  
**L-shaped = FALSE**



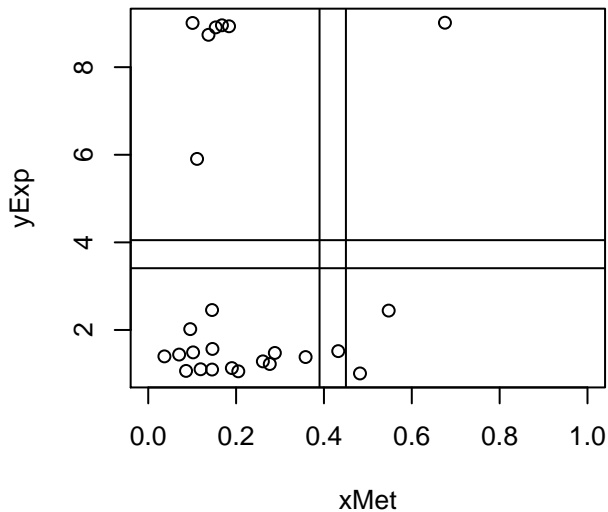
**P4HA3**  
**L-shaped = FALSE**



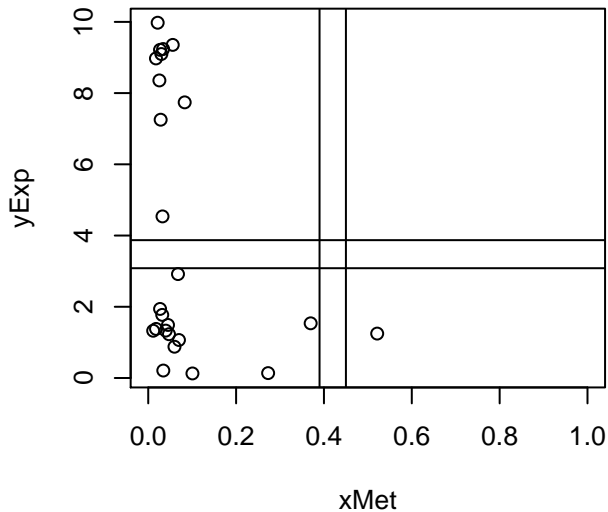
**P8**  
**L-shaped = FALSE**



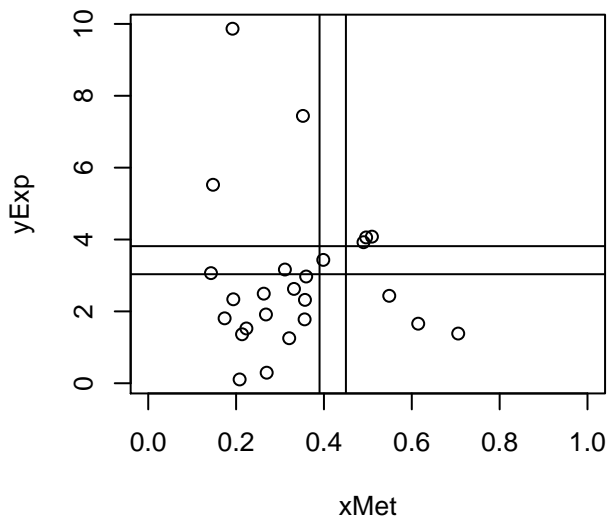
**SH3GL2**  
**L-shaped = FALSE**



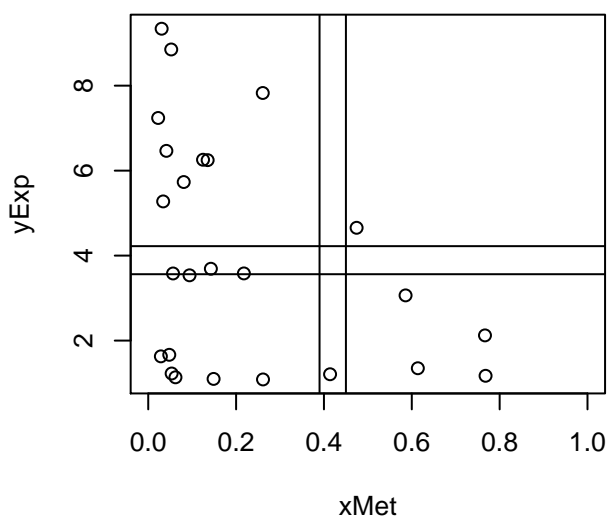
**TITF1**  
**L-shaped = FALSE**



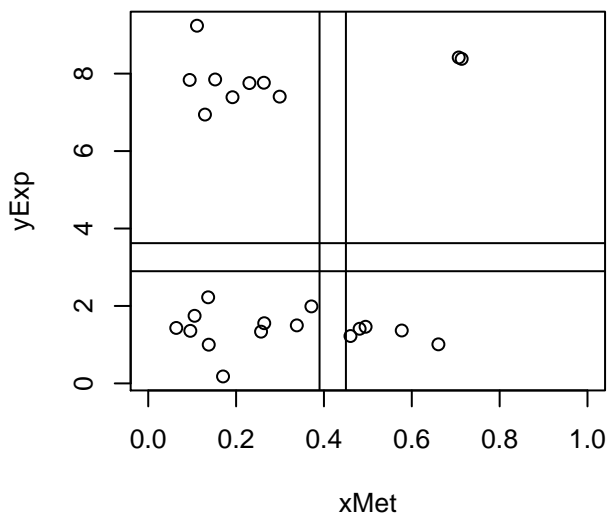
**TLR7**  
**L-shaped = FALSE**



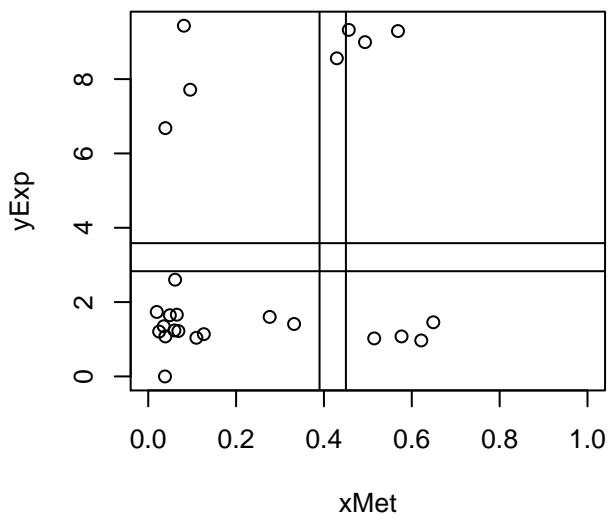
**TP53INP1**  
**L-shaped = FALSE**



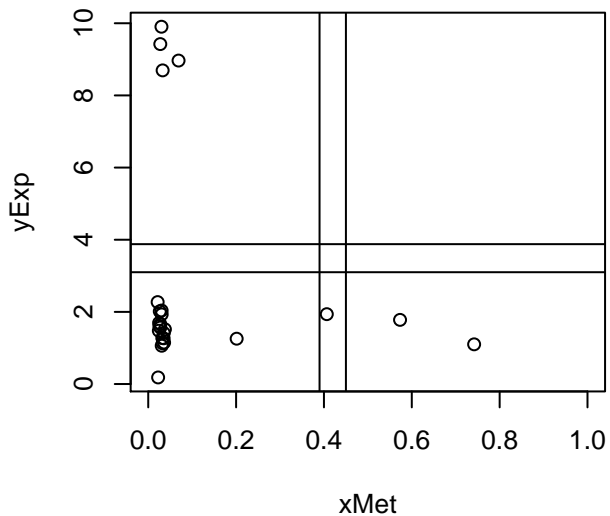
**TRPM8**  
**L-shaped = FALSE**



**UBL4A**  
**L-shaped = FALSE**



**ZNF550**  
**L-shaped = FALSE**



**ZNF71**  
**L-shaped = FALSE**

